DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D Max T_C = +25°C
20V	23mΩ @ V_{GS} = 4.5V	5.2A
	27mΩ @ V_{GS} = 2.5V	4.8A

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **ESD Protected Gate**
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

Description and Applications

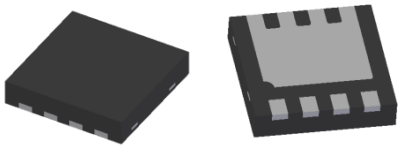
This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, which makes it ideal for high-efficiency power management applications.

Mechanical Data

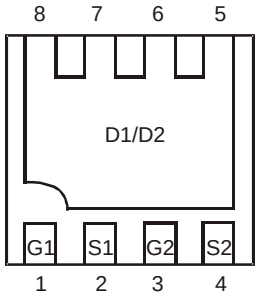
- Case: U-DFN3030-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 e4
- Polarity: See Diagram
- Weight: 0.0172 grams (Approximate)

U-DFN3030-8

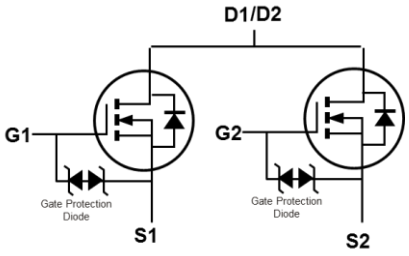
Top View



Bottom View



Bottom View
Pin Configuration



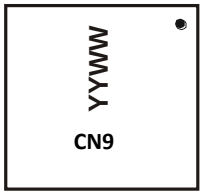
Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2024UDH-7	U-DFN3030-8	3000/Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



CN9 = Product Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 18 for 2018)
WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V_{GSS}	±10	V
Continuous Drain Current (Note 5)	Steady State	T_A = +25°C T_A = +70°C	I_D	5.2 4.2	A
Pulsed Drain Current			I_{DM}	45	A
Avalanche Current (Note 7) L = 0.1mH			I_{AS}	12	A
Avalanche Energy (Note 7) L = 0.1mH			E_{AS}	8	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T_A = +25°C	P_D	0.95	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	132	°C/W
Total Power Dissipation (Note 6)	T_A = +25°C	P_D	1.76	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	71	°C/W
Thermal Resistance, Junction to Case (Note 6)	Steady State	$R_{\theta JC}$	14	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	20	—	—	V	V_{GS} = 0V, I_D = 250µA
Zero Gate Voltage Drain Current T_J = +25°C	I_{DSS}	—	—	1.0	µA	V_{DS} = 20V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±10	µA	V_{GS} = ±8V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	0.35	—	1.0	V	V_{DS} = V_{GS}, I_D = 250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	— — —	16 19 24	23 27 34	mΩ	V_{GS} = 4.5V, I_D = 6.5A V_{GS} = 2.5V, I_D = 5.5A V_{GS} = 1.8V, I_D = 3.5A
Diode Forward Voltage	V_{SD}	—	0.65	1.0	V	V_{GS} = 0V, I_S = 1A
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{iss}	—	647	—	pF	V_{DS} = 10V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{oss}	—	78	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	38	—	pF	
Gate Resistance	R_g	—	628	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge	Q_g	—	7.1	—	nC	V_{GS} = 4.5V, V_{DS} = 10V, I_D = 6.5A
Gate-Source Charge	Q_{gs}	—	0.9	—	nC	
Gate-Drain Charge	Q_{gd}	—	0.7	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	98	—	ns	V_{DD} = 10V, V_{GS} = 4.5V, R_L = 10Ω, R_G = 6Ω
Turn-On Rise Time	t_R	—	140	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	1024	—	ns	
Turn-Off Fall Time	t_F	—	434	—	ns	
Reverse Recovery Time	t_{RR}	—	245	—	ns	I_F = 1A, di/dt = 100A/µs
Reverse Recovery Charge	Q_{RR}	—	149	—	nC	

Notes: 5. Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PCB, 2oz copper, with 1inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.

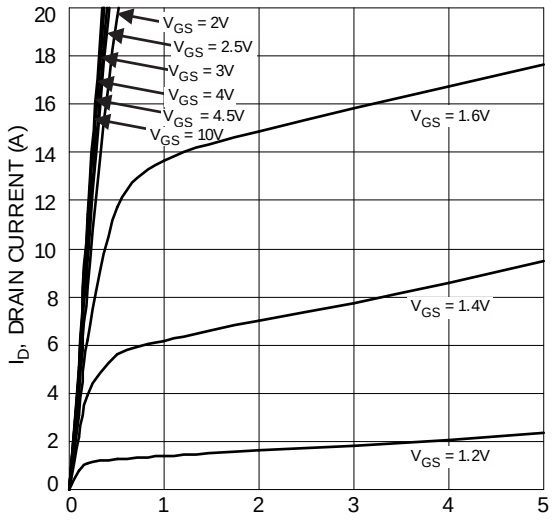


Figure 1 Typical Output Characteristic

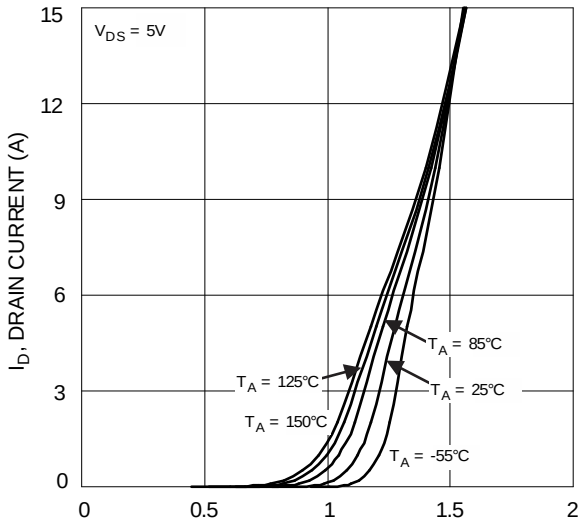


Figure 2 Typical Transfer Characteristics

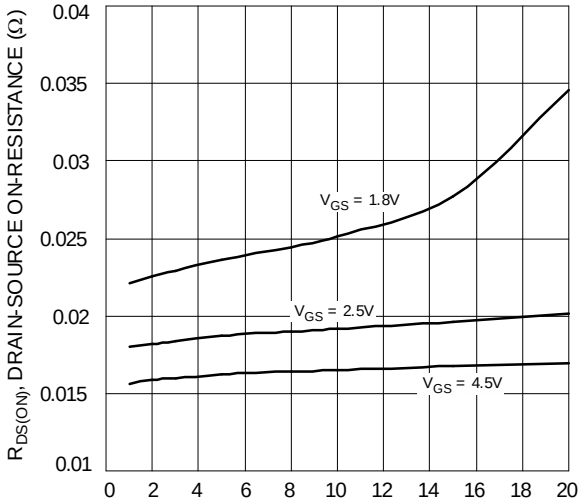


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

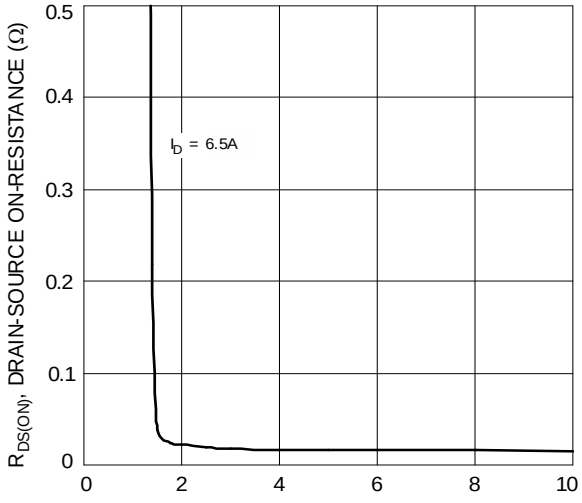


Figure 4 Typical Drain-Source On-Resistance vs. Gate-Source Voltage

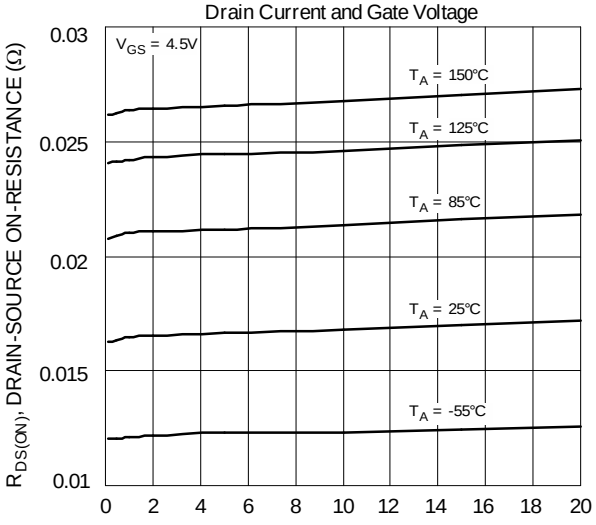


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

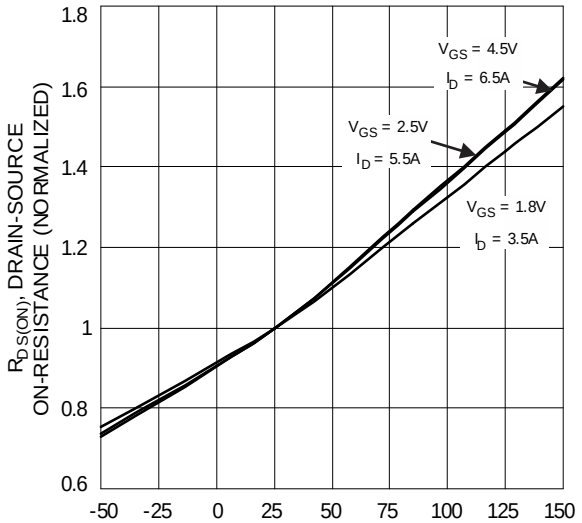


Figure 6 On-Resistance Variation with Temperature

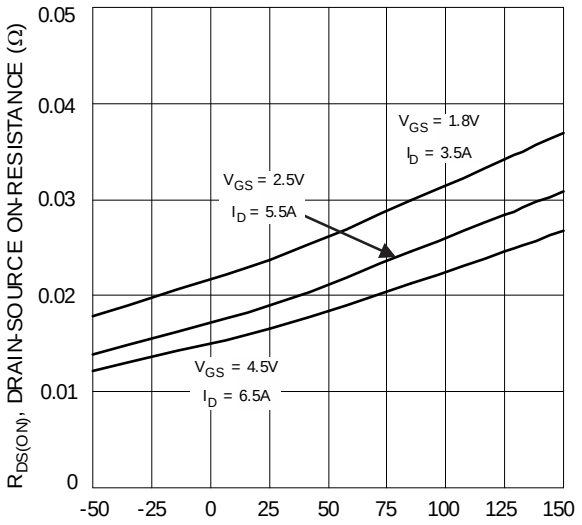


Figure 7 On-Resistance Variation with Temperature

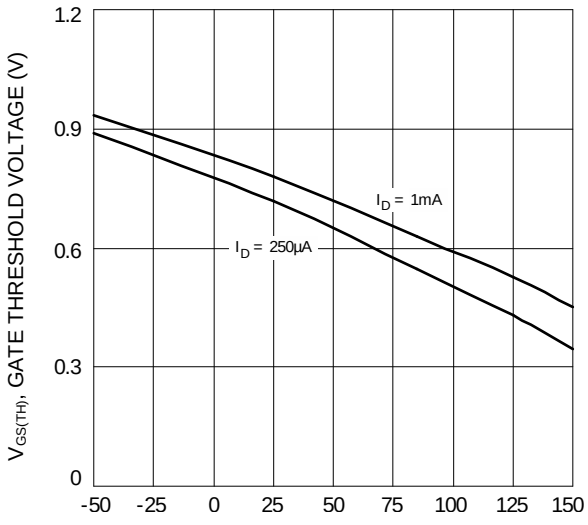


Figure 8 Gate Threshold Variation vs. Junction Temperature

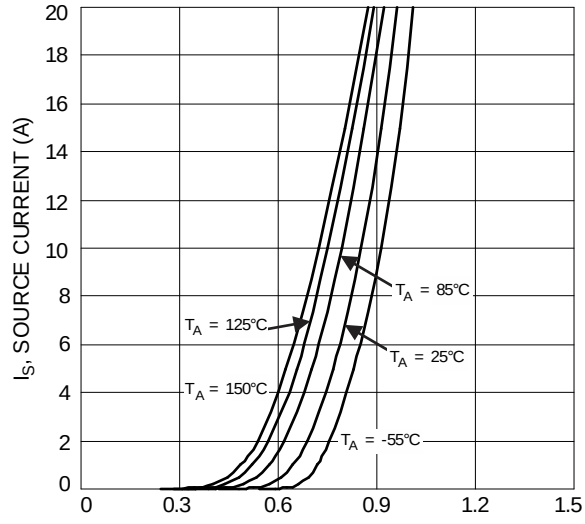


Figure 9 Diode Forward Voltage vs. Current

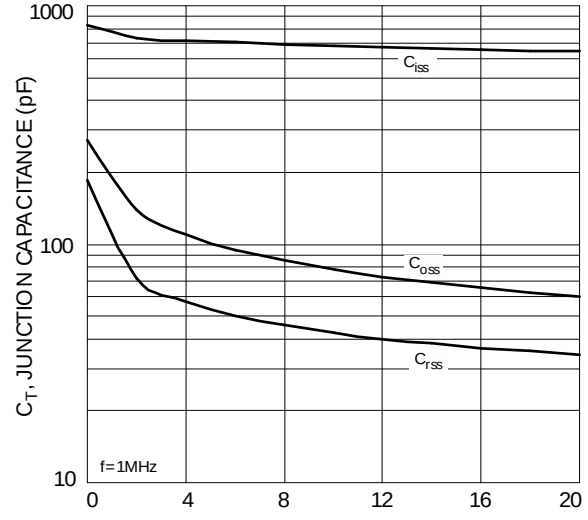


Figure 10 Typical Junction Capacitance

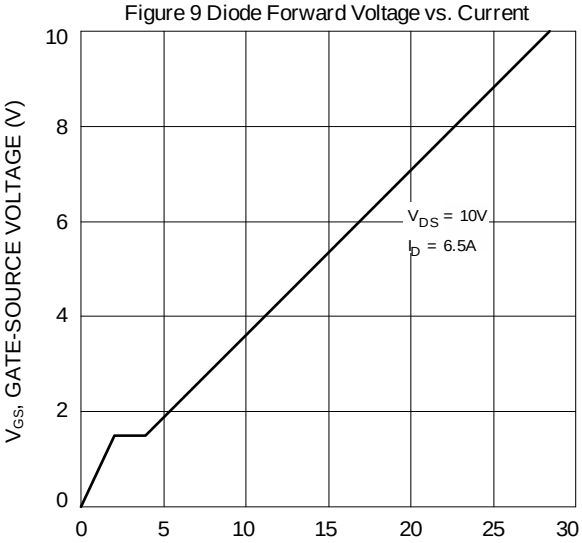


Figure 11 Gate Charge

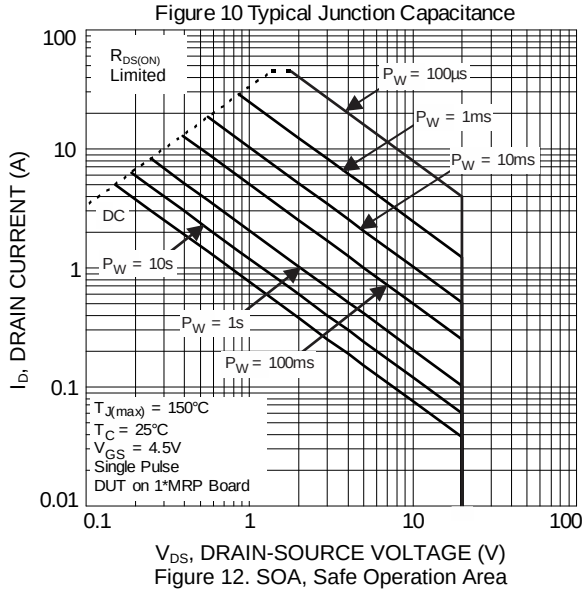


Figure 12. SOA, Safe Operation Area

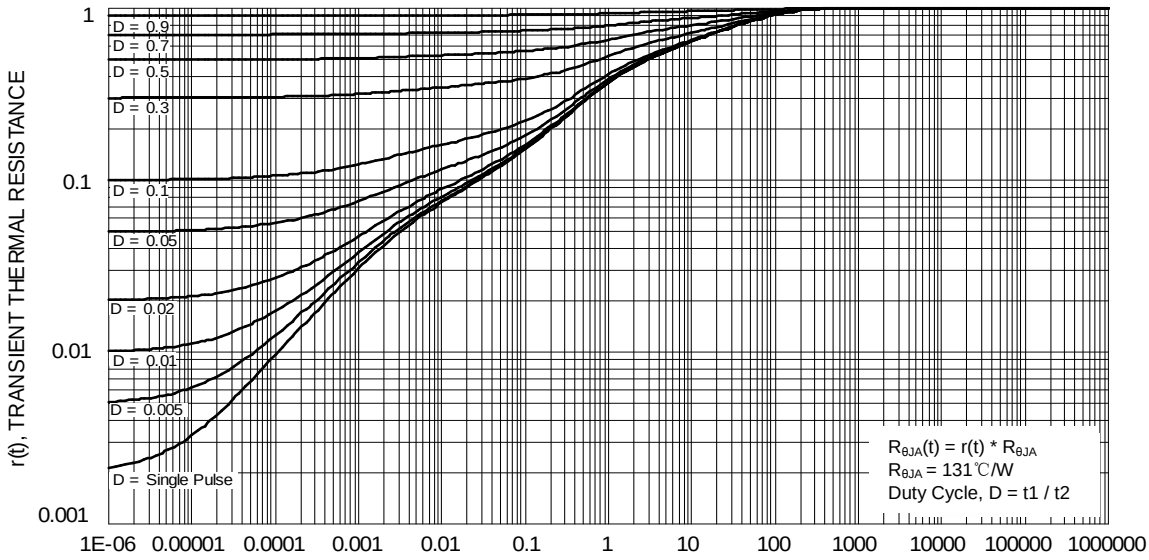
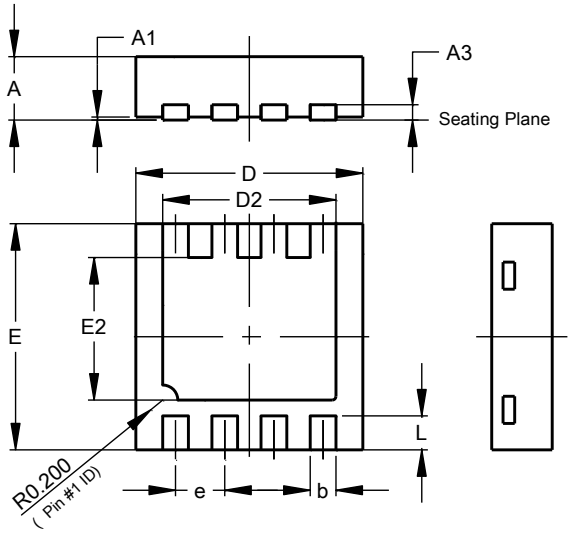


Figure 13 Transient Thermal Resistance

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN3030-8

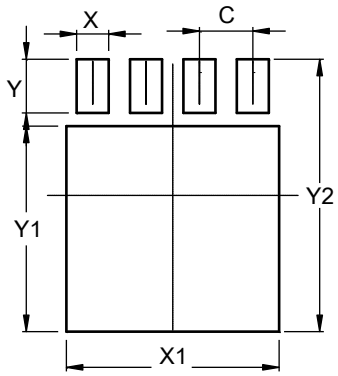


U-DFN3030-8			
Dim	**Min**	**Max**	**Typ**
A	0.57	0.63	0.60
A1	0	0.05	0.02
A3	-	-	0.15
b	0.29	0.39	0.34
D	2.90	3.10	3.00
D2	2.19	2.39	2.29
e	-	-	0.65
E	2.90	3.10	3.00
E2	1.64	1.84	1.74
L	0.30	0.60	0.45
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN3030-8



Dimensions	Value (in mm)
C	0.650
X	0.390
X1	2.590
Y	0.650
Y1	2.490
Y2	3.300

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