

# NSR10F30QNXT5G

## Schottky Diode Optimized for High Frequency Switching Power Supplies

These Schottky barrier diodes are optimized for low forward voltage drop and low leakage current and are offered in a Chip Scale Package (CSP) to reduce board space. The low thermal resistance enables designers to meet the challenging task of achieving higher efficiency and meeting reduced space requirements.

### Features

- Low Forward Voltage Drop – 420 mV @ 1.0 A
- Low Reverse Current – 20  $\mu$ A @ 10 V VR
- 1.0 A of Continuous Forward Current
- ESD Rating – Human Body Model: Class 3B  
– Machine Model: Class C
- High Switching Speed
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

### Typical Applications

- LCD and Keypad Backlighting
- Camera Photo Flash
- Buck and Boost dc-dc Converters
- Reverse Voltage and Current Protection
- Clamping & Protection

### Markets

- Mobile Handsets
- MP3 Players
- Digital Camera and Camcorders
- Notebook PCs & PDAs
- GPS

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Reverse Voltage	$V_R$	30	V
Forward Current (DC)	$I_F$	1.0	A
Forward Surge Current (60 Hz @ 1 cycle)	$I_{FSM}$	18	A
ESD Rating:	Human Body Model Machine Model	ESD > 8 > 400	kV V

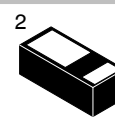
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



ON Semiconductor®

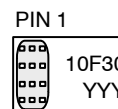
<http://onsemi.com>

### 30 V SCHOTTKY BARRIER DIODE



DSN2  
(0502)  
CASE 152AD

### MARKING DIAGRAM



10F30 = Specific Device Code  
YYY = Year Code

### ORDERING INFORMATION

Device	Package	Shipping†
NSR10F30QNXT5G	DSN2 (Pb-Free)	5000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# NSR10F30QNXT5G

## Thermal Characteristics

Characteristic	Symbol	Min	Typ	Max	Unit
Thermal Resistance Junction-to-Ambient (Note 1) Total Power Dissipation @ $T_A = 25^\circ\text{C}$	$R_{\theta JA}$ $P_D$			228 548	$^\circ\text{C}/\text{W}$ mW
Thermal Resistance Junction-to-Ambient (Note 2) Total Power Dissipation @ $T_A = 25^\circ\text{C}$	$R_{\theta JA}$ $P_D$			85 1.47	$^\circ\text{C}/\text{W}$ W
Storage Temperature Range	$T_{stg}$			-40 to +125	$^\circ\text{C}$
Junction Temperature	$T_J$			+150	$^\circ\text{C}$

1. Mounted onto a 4 in square FR-4 board 50 mm sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.
2. Mounted onto a 4 in square FR-4 board 1 in sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.

## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Reverse Leakage ( $V_R = 10\text{ V}$ ) ( $V_R = 30\text{ V}$ )	$I_R$			20 100	$\mu\text{A}$
Forward Voltage ( $I_F = 0.5\text{ A}$ ) ( $I_F = 1.0\text{ A}$ )	$V_F$		0.400 0.450	0.420 0.470	V
Reverse Recovery (Special) Switch from Forward Current to Reverse Voltage Time taken from 1 ns Transition Time to Fully Stabilized ( $I_F = 1.5\text{ A}$ to $V_R = 28\text{ V}$ , $25^\circ\text{C}$ ) ( $I_F = 1.5\text{ A}$ to $V_R = 28\text{ V}$ , $85^\circ\text{C}$ )	$T_{RR}$		21.98 21.38		ns

# NSR10F30QNXT5G

## TYPICAL CHARACTERISTICS

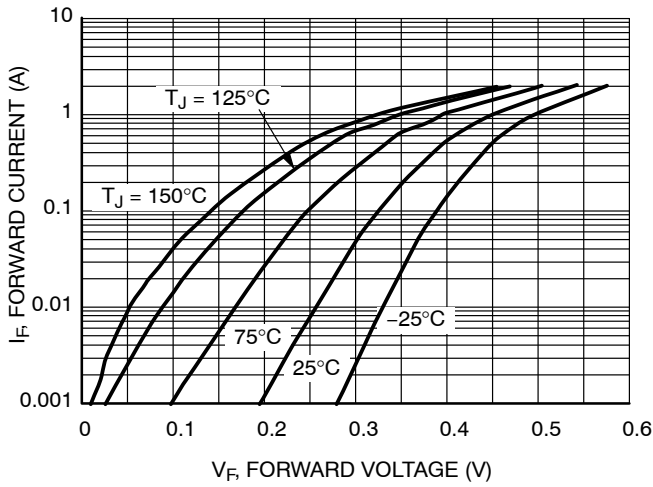


Figure 1. Forward Voltage

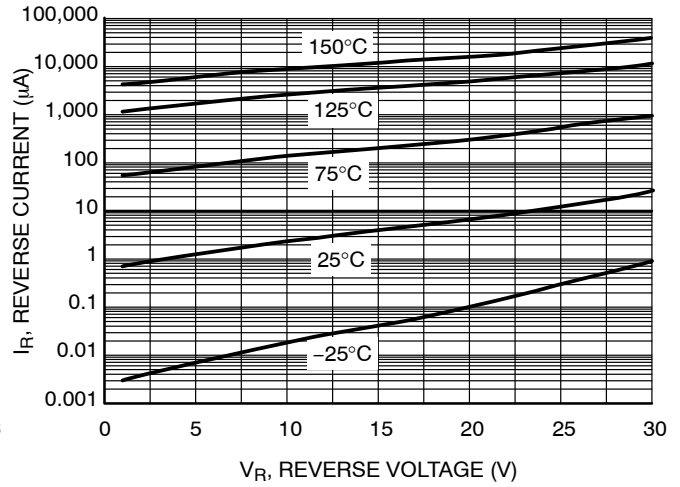


Figure 2. Typical Reverse Current

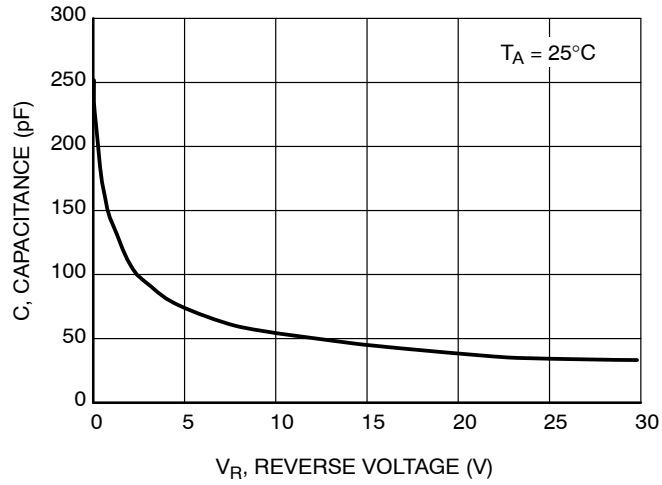


Figure 3. Typical Capacitance

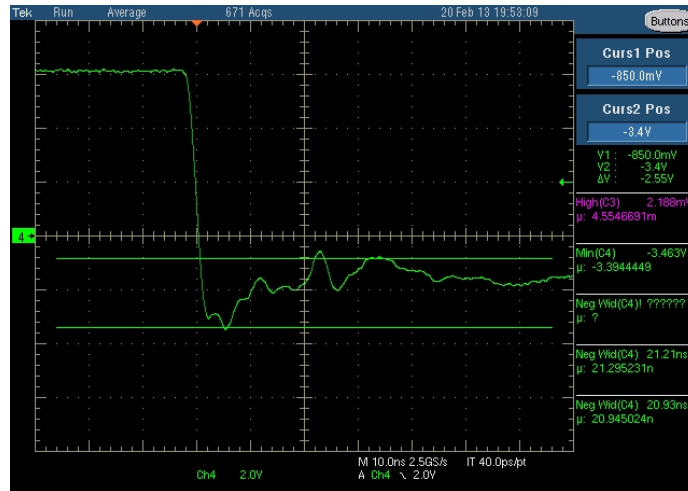


Figure 4. Typical Reverse Recovery  
 $I_F = 1.5 \text{ A}$  to  $V_R = 28 \text{ V}$



# NSR10F30QNXT5G

analysis. This analysis showed that there was no shift in any of the parameters, forward voltage, reverse leakage current, and capacitance.

The graphs below shown below demonstrate the Pre and Post-Stress characterization graphs and how that there was no change in the part performance.

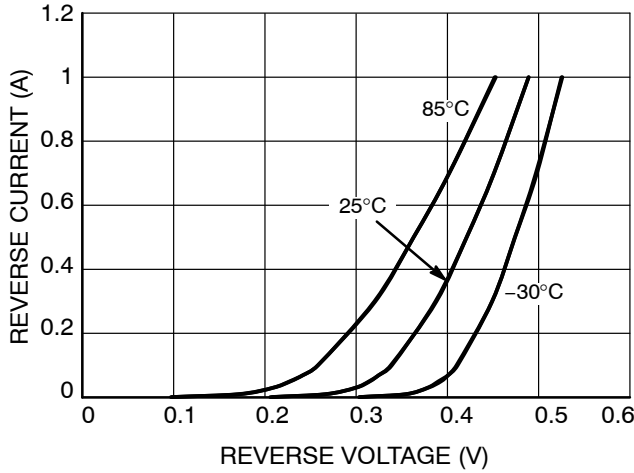


Figure 6. Reverse Leakage Characteristics

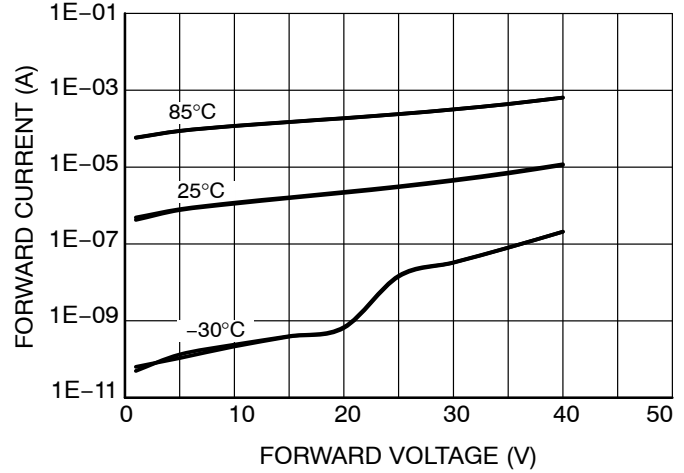


Figure 7. Forward Current Characterization

Finally these diodes were placed in the same circuit at 25°C for 1 week of continuous operation. The screen shots below in Figures 8 and 9 show the operation on the first day of continuous operation and 5 days respectively.

To further evaluate the performance, a thermal camera was used to take pictures of the NSR10F30QNXT5G during heavy load operation and 25°C. As seen in Figure 10 the case only got to 29.2°C. This translates to less than 20mW of total power dissipation.

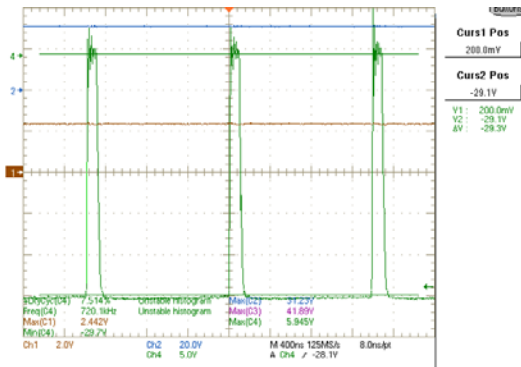


Figure 8. NSR10F30QNXT5G on Day 1 at 25°C

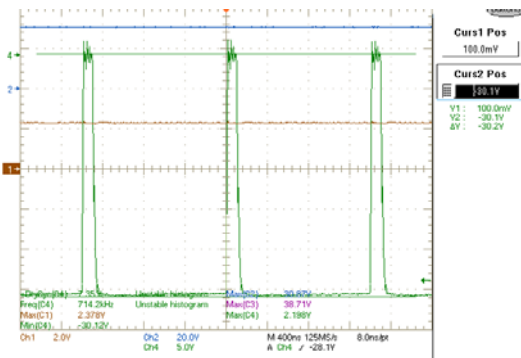


Figure 9. NSR10F30QNXT5G on Day 5 at 25°C

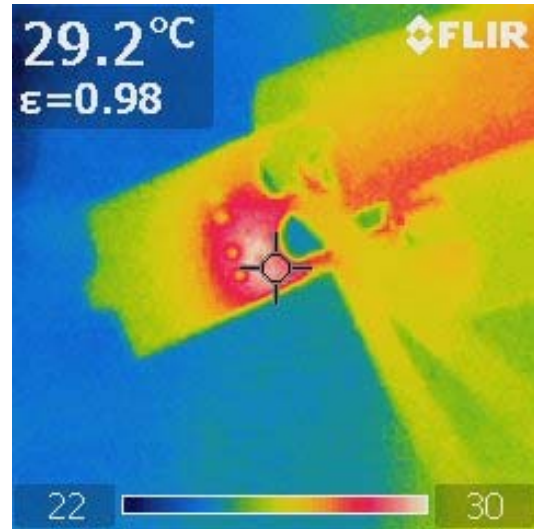


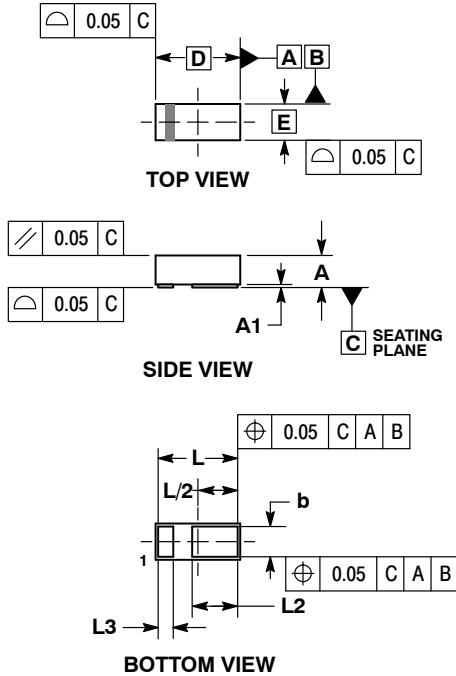
Figure 10. Case Temperature of NSR10F30QNXT5G in Operation at 25°C, 150 mA 34 V Output

With a heavy load condition (up to 1.2 A) through the NSR10F30QNXT5G on a minimum pad size the ambient temperature can rise up to 145°C and not degrade the performance. Using ON Semiconductor’s new ultra low profile Wireless Boost Application Optimized Schottky diodes will increase the overall efficiency and battery life while reducing board size and cost associated with thermal pads.

# NSR10F30QNXT5G

## PACKAGE DIMENSIONS

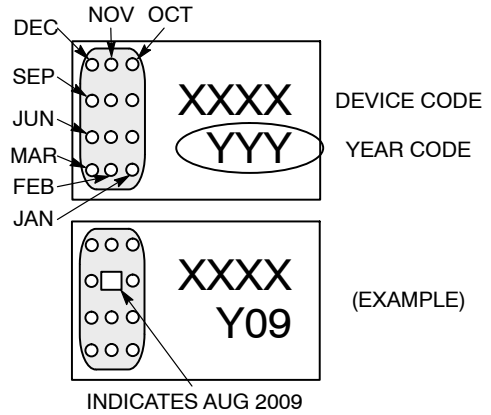
DSN2, 1.4x0.6, 0.75P  
CASE 152AD-01  
ISSUE A



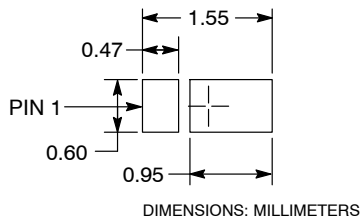
- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.

MILLIMETERS		
DIM	MIN	MAX
A	0.25	0.31
A1	---	0.05
b	0.45	0.55
D	1.40 BSC	
E	0.60 BSC	
L	1.20	1.30
L2	0.70	0.80
L3	0.20	0.30

### CATHODE BAND MONTH CODING



### MOUNTING FOOTPRINT\*



See Application Note AND8398/D for more mounting details

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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Телефон: 8 (812) 309-75-97 (многоканальный)

Факс: 8 (812) 320-03-32

Электронная почта: [ocean@oceanchips.ru](mailto:ocean@oceanchips.ru)

Web: <http://oceanchips.ru/>

Адрес: 198099, г. Санкт-Петербург, ул. Калинина, д. 2, корп. 4, лит. А