# Nch 20V 1.5A Middle Power MOSFET

$V_{DSS}$	20V
R <sub>DS(on)</sub> (Max.)	180mΩ
I <sub>D</sub>	±1.5A
$P_D$	0.8W

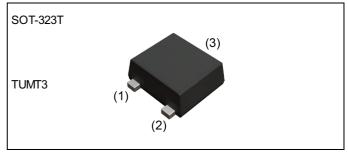
# ●Features

- 1) Low on resistance.
- 2) Space saving small surface mount package (TUMT3).
- 3) Low voltage drive(1.8V drive)

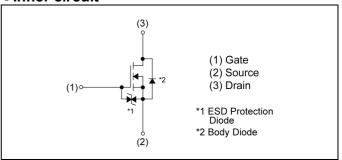
# Application

Switching

## Outline



## ●Inner circuit



Packaging specifications

- r working opcomoducino					
	Packing	Embossed Tape			
	Reel size (mm)	180			
Туре	Tape width (mm)	8			
	Basic ordering unit (pcs)	3000			
	Taping code	TL			
	Marking	PS			

# ullet Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	20	V
Continuous drain current	I <sub>D</sub>	±1.5	А
Pulsed drain current	I <sub>DP</sub> *1	±3.0	Α
Gate - Source voltage	$V_{GSS}$	±10	V
Down discipation	P <sub>D</sub> *2	0.8	W
Power dissipation	P <sub>D</sub> *3	0.75	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

# ●Thermal resistance

Deremeter	Cumb of	Values			Unit
Parameter	Symbol	Min.	Тур.	Max.	Offic
The word reciptores is unation, ambient	R <sub>thJA</sub> *2	-	-	156	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub> *3	-	-	167	°C/W

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions			Values		
Parameter	Symbol	ymbor Conditions		Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA	20	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j} I_D = 1 \text{mA}$ referenced to 25°C		29.0	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V	1	-	1	μA	
Gate - Source leakage current	e - Source leakage current I <sub>GSS</sub>		1	-	10	μA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = 10V, I_{D} = 1mA$	0.3	-	1.0	V	
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-1.6	-	mV/°C	
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 1.5A	-	130	180		
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 1.5A	-	170	240	mΩ	
		$V_{GS} = 1.8V, I_D = 0.8A$	1	220	310		
Gate resistance	R <sub>G</sub> f = 1MHz, open drain		1	17	1	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  *4	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1.5A	1.6	-	-	S	

<sup>\*1</sup> Pw $\leq$ 10µs , Duty cycle $\leq$ 1%

<sup>\*2</sup> Mounted on a ceramic board (30x30x0.8mm)

<sup>\*3</sup> Mounted on a FR4 (25x25x0.8mm)

<sup>\*4</sup> Pulsed

# ●Electrical characteristics (T<sub>a</sub> = 25°C)

Davamatav	Cymah al	Conditions	Values			Lloit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	110	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 10V	-	18	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	15	-	
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq 10V, V_{GS} = 4.5V$	-	5	-	
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 1.0A	-	5	-	no
Turn - off delay time	t <sub>d(off)</sub> *4	R <sub>L</sub> ≃ 10Ω	-	20	-	ns
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	3	-	

# • Gate charge characteristics $(T_a = 25^{\circ}C)$

	\ a	,				
Parameter	Cymbol	Conditions	Values			Unit
raianietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	Qg*4	V <sub>DD</sub> ≃ 10V,	-	1.8	2.5	
Gate - Source charge	Q <sub>gs</sub> *4	I <sub>D</sub> = 1.5A, V <sub>GS</sub> = 4.5V	-	0.3	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4	$V_{GS} = 4.5V$	-	0.3	-	

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Darameter	Symbol	Conditions	200		Values	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	I <sub>S</sub>	T - 25°0	-	-	0.6	Α
Pulse forward current	I <sub>SP</sub> *1	T <sub>a</sub> = 25°C	-	-	2.4	Α
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = 0.6A	-	-	1.2	V

### • Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

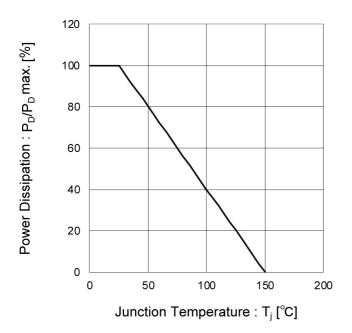
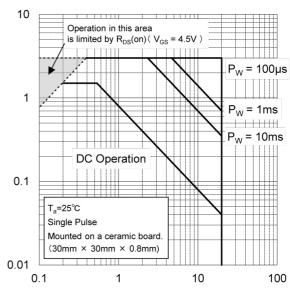


Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

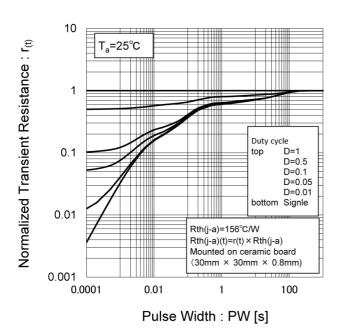
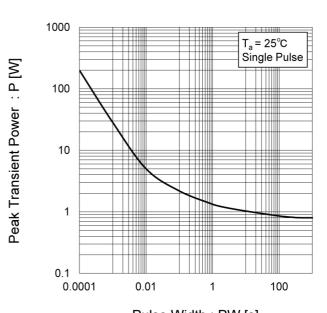


Fig.4 Single Pulse Maximum Power dissipation



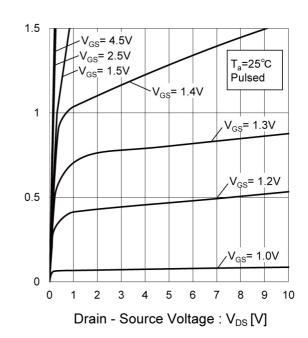
Pulse Width : PW [s]

#### Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

Drain - Source Voltage: V<sub>DS</sub> [V]

Fig.6 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

Fig.7 Breakdown Voltage vs.
Junction Temperature

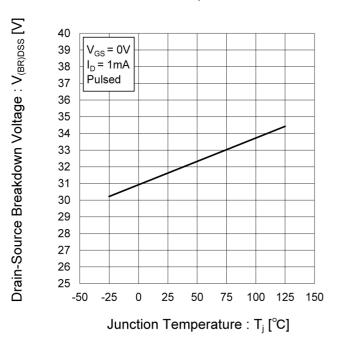
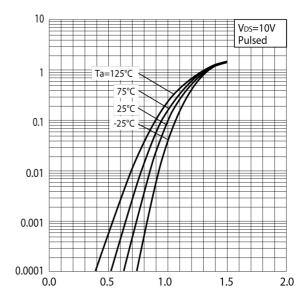


Fig.8 Typical Transfer Characteristics



Gate - Source Voltage : V<sub>GS</sub> [V]

Drain Current : I<sub>D</sub> [A]

### • Electrical characteristic curves

Fig.9 Gate Threshold Voltage vs.
Junction Temperature

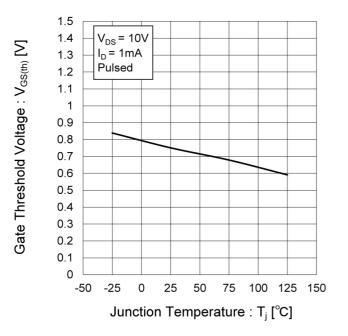


Fig.10 Forward Transfer Admittance vs.
Drain Current

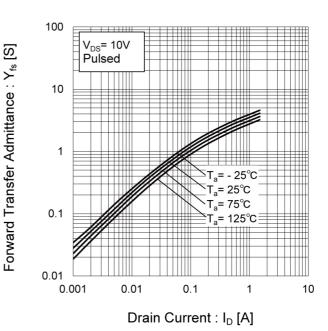


Fig.11 Drain Current Derating Curve

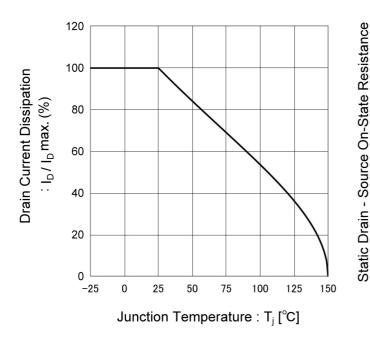
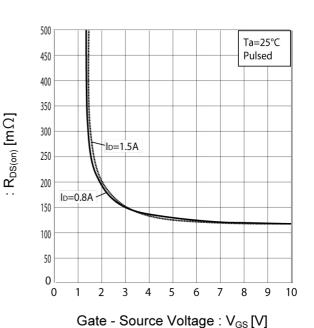


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



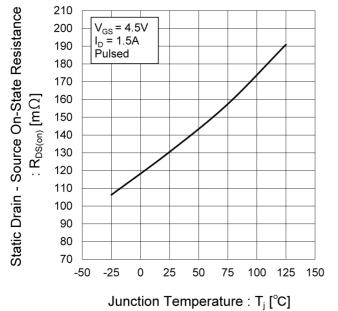
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## • Electrical characteristic curves

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

Resistance vs. Drain Current (I)

Fig.14 Static Drain - Source On - State



Static Drain - Source On-State Resistance On-S

### • Electrical characteristic curves

Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

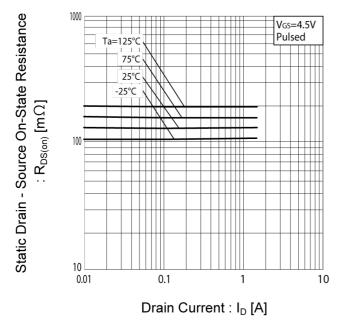


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

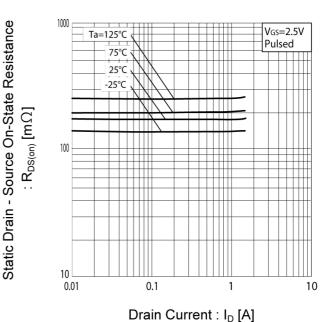
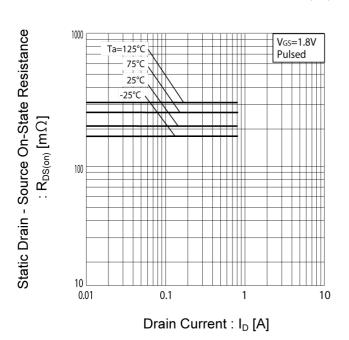


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)



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Capacitance : C [pF]

Gate - Source Voltage : V<sub>GS</sub> [V]

## • Electrical characteristic curves

Fig.18 Typical Capacitance vs.

Drain - Source Voltage

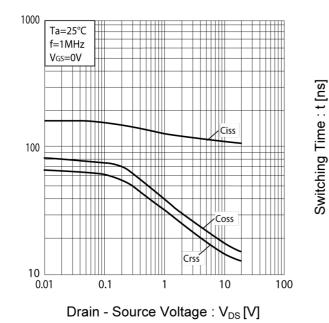


Fig.19 Switching Characteristics

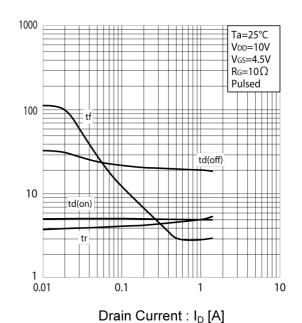


Fig.20 Dynamic Input Characteristics

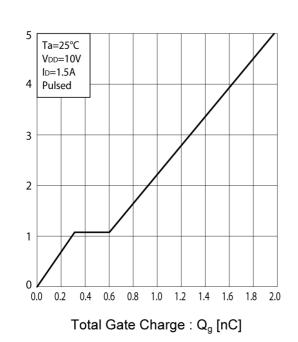
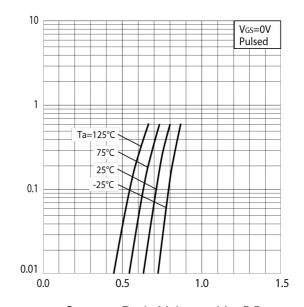


Fig.21 Source Current vs.
Source Drain Voltage



Source - Drain Voltage :  $V_{\text{SD}}[V]$ 

Inverse Diode Forward Current : Is [A]

### Measurement circuits

Fig. 1-1 SWITCHING TIME MEASUREMENT CIRCUIT

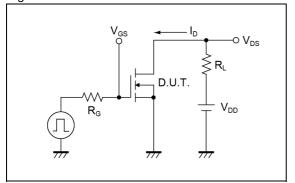


Fig. 2-1 GATE CHARGE MEASUREMENT CIRCUIT

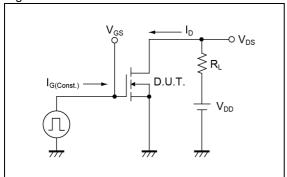


Fig. 1-2 SWITCHING WAVEFORMS

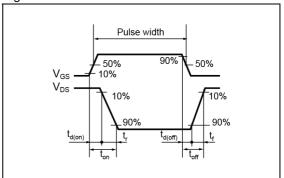
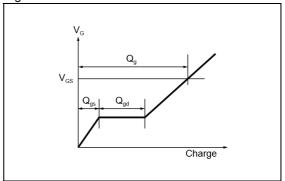


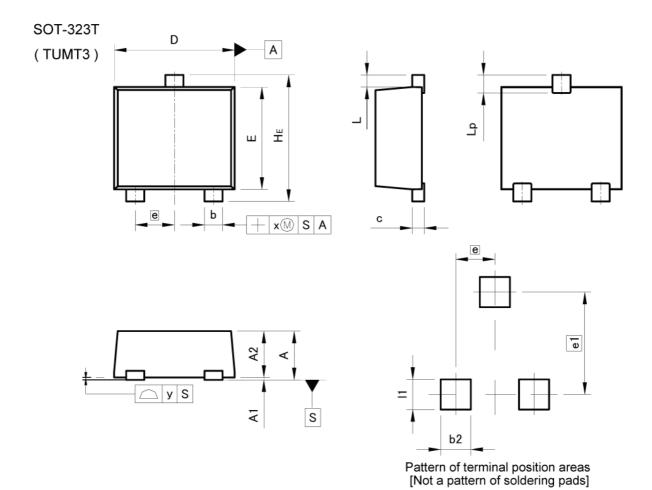
Fig. 2-2 GATE CHARGE WAVEFORM



### Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

# Dimensions



DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α		0.85	-	0.033
A1	0.00	0.10	0.000	0.004
A2	0.72	0.82	0.028	0.032
b	0.25	0.40	0.010	0.016
С	0.12	0.22	0.005	0.009
D	1.90	2.10	0.075	0.083
E	1.60	1.80	0.063	0.071
е	0.65		0.0	26
HE	2.00	2.20	0.079	0.087
L	0.5	20	0.0	08
Lp	-	0.40	_	0.016
Х	_	0.10	_	0.004
У		0.10	i <del>-</del>	0.004

DIM MILIMETERS MIN		ETERS	INC	HES
		MAX	MIN	MAX
b2	_	0.50	7-	0.020
e1	1.70		0.0	67
11	-	0.50	-	0.020

Dimension in mm/inches



# **Notice**

#### **Precaution on using ROHM Products**

1. If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), aircraft/spacecraft, nuclear power controllers, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

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Ì	JÁPAN	USA	EU	CHINA
Γ	CLASSⅢ	CLACCIII	CLASS II b	CI VCCIII
Γ	CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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  - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### **Precaution for Mounting / Circuit board design**

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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