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August 2014

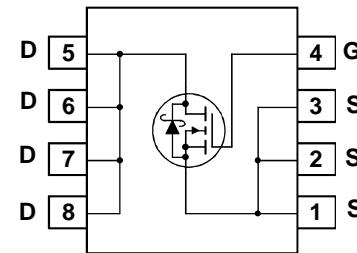
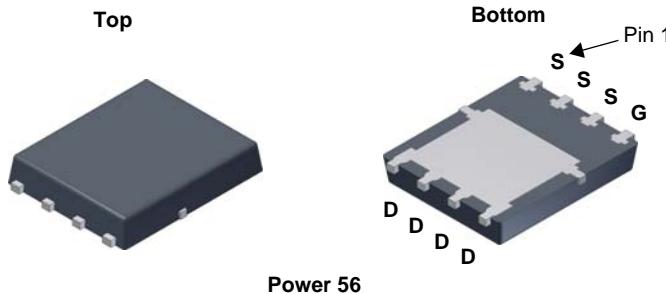
FDMS0310AS

N-Channel PowerTrench® SyncFET™

30 V, 22 A, 4.3 mΩ

Features

- Max $r_{DS(on)} = 4.3$ mΩ at $V_{GS} = 10$ V, $I_D = 19$ A
- Max $r_{DS(on)} = 5.2$ mΩ at $V_{GS} = 4.5$ V, $I_D = 17$ A
- Advanced package and Silicon combination for low $r_{DS(on)}$ and high efficiency
- SyncFET™ Schottky Body Diode
- MSL1 robust package design
- 100% UIL tested
- RoHS Compliant



MOSFET Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{DS}	Drain to Source Voltage	30	V
V_{GS}	Gate to Source Voltage (Note 4)	± 20	V
I_D	Drain Current -Continuous (Package limited) $T_C = 25^\circ\text{C}$	22	A
	-Continuous (Silicon limited) $T_C = 25^\circ\text{C}$	80	
	-Continuous $T_A = 25^\circ\text{C}$ (Note 1a)	19	
	-Pulsed	100	
E_{AS}	Single Pulse Avalanche Energy (Note 3)	33	mJ
P_D	Power Dissipation $T_C = 25^\circ\text{C}$	41	W
	Power Dissipation $T_A = 25^\circ\text{C}$ (Note 1a)	2.5	
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case	3.0	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 1a)	50	

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMS0310AS	FDMS0310AS	Power 56	13 "	12 mm	3000 units

Electrical Characteristics $T_J = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain to Source Breakdown Voltage	$I_D = 1 \text{ mA}, V_{GS} = 0 \text{ V}$	30			V
BV_{DSST}	Drain to Source Breakdown Voltage Transient	$V_{GS} = 0 \text{ V}, \text{Transient} = 100 \text{ ns}$	33			V
$\frac{\Delta \text{BV}_{\text{DSS}}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = 10 \text{ mA, referenced to } 25^\circ\text{C}$		23		$\text{mV/}^\circ\text{C}$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$		500		μA
I_{GSS}	Gate to Source Leakage Current, Forward	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$		100		nA

On Characteristics

$V_{GS(\text{th})}$	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 1 \text{ mA}$	1.2	1.5	3.0	V
$\frac{\Delta V_{GS(\text{th})}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	$I_D = 10 \text{ mA, referenced to } 25^\circ\text{C}$		-4		$\text{mV/}^\circ\text{C}$
$r_{DS(\text{on})}$	Static Drain to Source On Resistance	$V_{GS} = 10 \text{ V}, I_D = 19 \text{ A}$		3.6	4.3	$\text{m}\Omega$
		$V_{GS} = 4.5 \text{ V}, I_D = 17 \text{ A}$		4.5	5.2	
		$V_{GS} = 10 \text{ V}, I_D = 19 \text{ A}, T_J = 125^\circ\text{C}$		4.8	6.0	
g_{FS}	Forward Transconductance	$V_{DS} = 5 \text{ V}, I_D = 19 \text{ A}$		103		S

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1\text{MHz}$		1715	2280	pF
C_{oss}	Output Capacitance			655	870	pF
C_{rss}	Reverse Transfer Capacitance			75	110	pF
R_g	Gate Resistance			0.7	2.5	Ω

Switching Characteristics

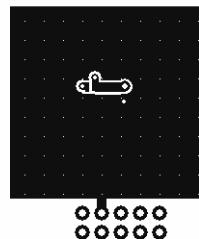
$t_{d(\text{on})}$	Turn-On Delay Time	$V_{DD} = 15 \text{ V}, I_D = 19 \text{ A}, V_{GS} = 10 \text{ V}, R_{\text{GEN}} = 6 \Omega$		9.0	18	ns	
t_r	Rise Time			3.9	10	ns	
$t_{d(\text{off})}$	Turn-Off Delay Time			25	40	ns	
t_f	Fall Time			3.2	10	ns	
Q_g	Total Gate Charge	$V_{GS} = 0 \text{ V to } 10 \text{ V}$		27	37	nC	
Q_g	Total Gate Charge		$V_{GS} = 0 \text{ V to } 4.5 \text{ V}$	$V_{DD} = 15 \text{ V}, I_D = 19 \text{ A}$	13	19	nC
Q_{gs}	Gate to Source Charge				4.2		nC
Q_{gd}	Gate to Drain "Miller" Charge				3.7		nC

Drain-Source Diode Characteristics

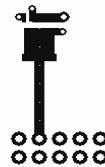
V_{SD}	Source-Drain Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = 2 \text{ A}$ (Note 2)		0.6	0.8	V
		$V_{GS} = 0 \text{ V}, I_S = 19 \text{ A}$ (Note 2)		0.8	1.2	
t_{rr}	Reverse Recovery Time	$I_F = 19 \text{ A}, \text{di/dt} = 300 \text{ A}/\mu\text{s}$		24	38	ns
Q_{rr}	Reverse Recovery Charge			24	38	

Notes:

1. R_{0JA} is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. R_{0JC} is guaranteed by design while R_{0CA} is determined by the user's board design.



a. 50 °C/W when mounted on a 1 in² pad of 2 oz copper.



b. 125 °C/W when mounted on a minimum pad of 2 oz copper.

2. Pulse Test: Pulse Width < 300 μs , Duty cycle < 2.0%.

3. E_{AS} of 33 mJ is based on starting $T_J = 25^\circ\text{C}$, $L = 0.3 \text{ mH}$, $I_{AS} = 15 \text{ A}$, $V_{DD} = 27 \text{ V}$, $V_{GS} = 10 \text{ V}$.

4. As an N-ch device, the negative V_{GS} rating is for low duty cycle pulse occurrence only. No continuous rating is implied.

Typical Characteristics $T_J = 25^\circ\text{C}$ unless otherwise noted

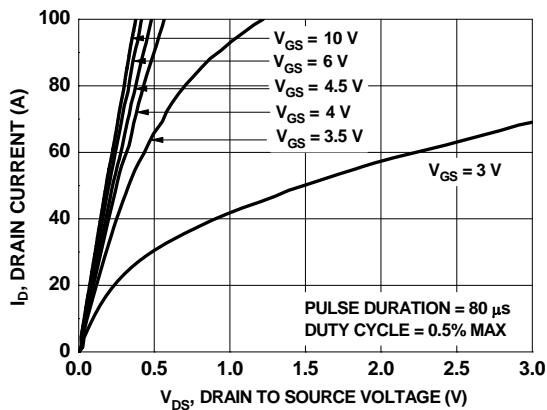


Figure 1. On Region Characteristics

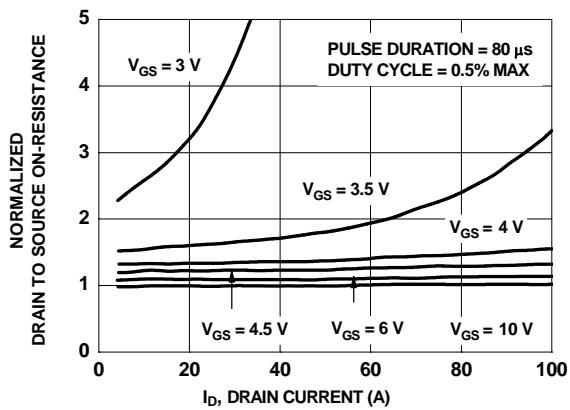


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

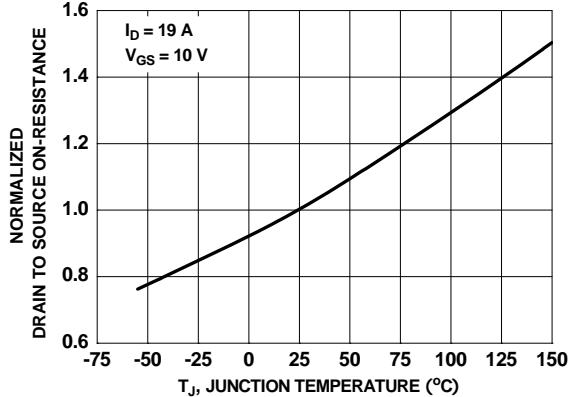


Figure 3. Normalized On Resistance vs Junction Temperature

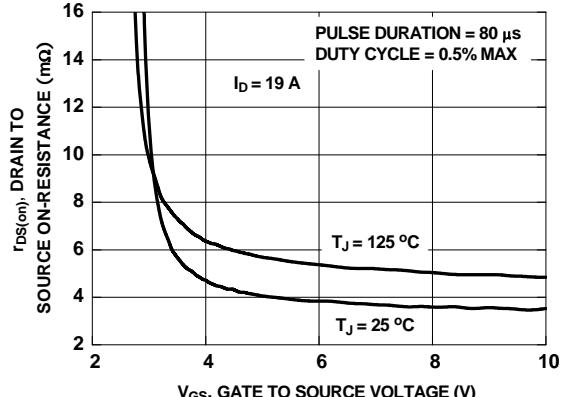


Figure 4. On-Resistance vs Gate to Source Voltage

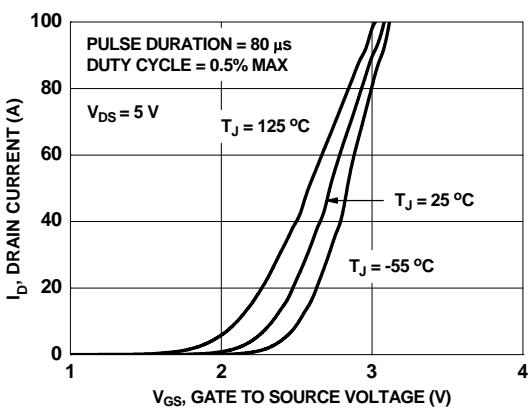


Figure 5. Transfer Characteristics

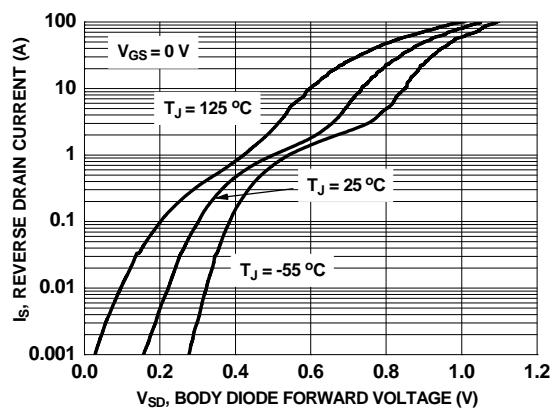


Figure 6. Source to Drain Diode Forward Voltage vs Source Current

Typical Characteristics $T_J = 25^\circ\text{C}$ unless otherwise noted

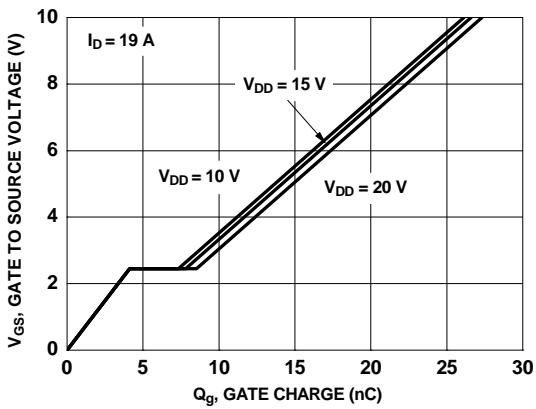


Figure 7. Gate Charge Characteristics

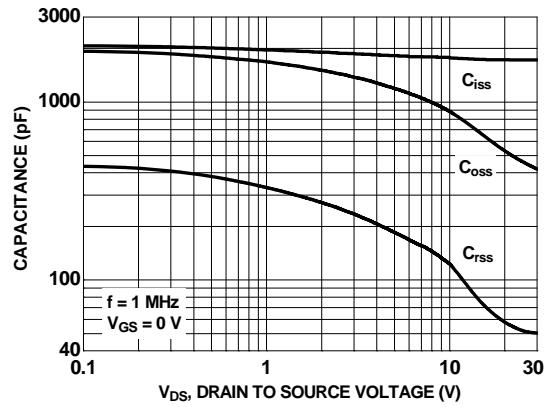


Figure 8. Capacitance vs Drain to Source Voltage

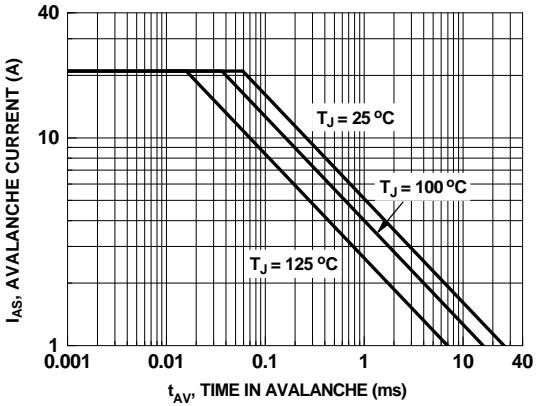


Figure 9. Unclamped Inductive Switching Capability

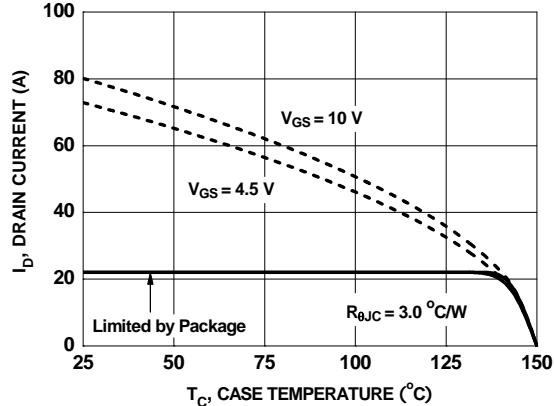


Figure 10. Maximum Continuous Drain Current vs Case Temperature

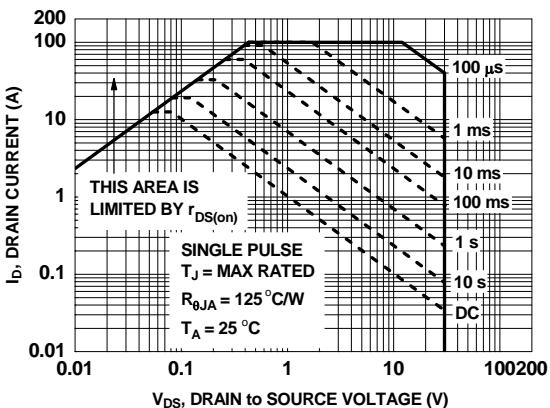


Figure 11. Forward Bias Safe Operating Area

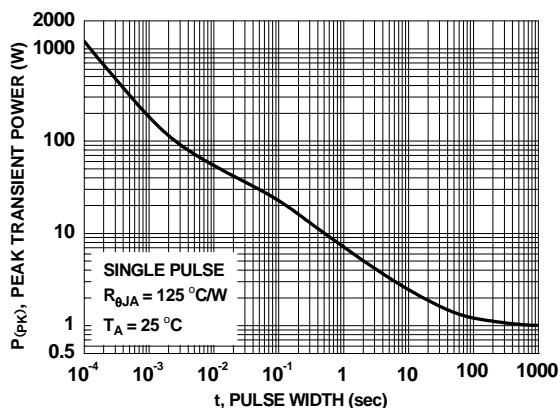


Figure 12. Single Pulse Maximum Power Dissipation

Typical Characteristics $T_J = 25^\circ\text{C}$ unless otherwise noted

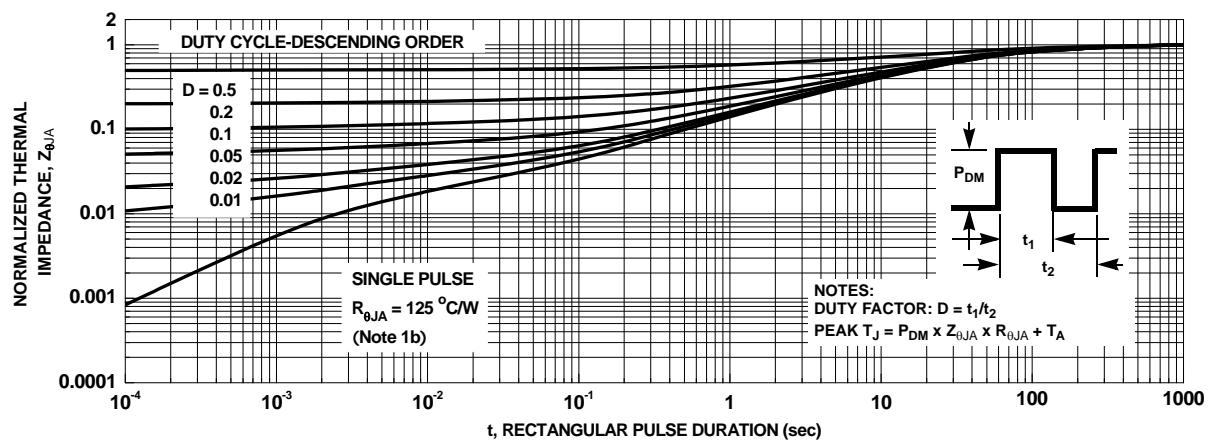


Figure 13. Junction-to-Ambient Transient Thermal Response Curve

Typical Characteristics (continued)

SyncFET™ Schottky body diode Characteristics

Fairchild's SyncFET™ process embeds a Schottky diode in parallel with PowerTrench MOSFET. This diode exhibits similar characteristics to a discrete external Schottky diode in parallel with a MOSFET. Figure 14 shows the reverse recovery characteristic of the FDMS0310AS.

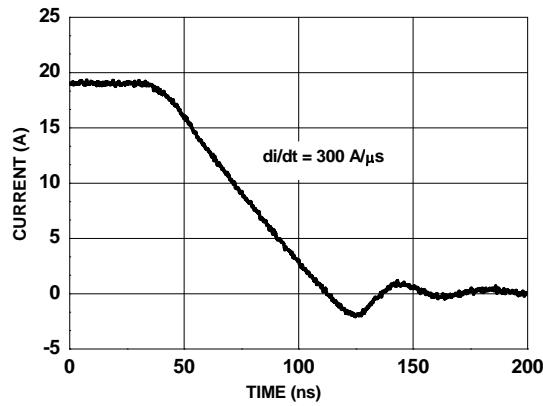


Figure 14. FDMS0310AS SyncFET™ body diode reverse recovery characteristic

Schottky barrier diodes exhibit significant leakage at high temperature and high reverse voltage. This will increase the power in the device.

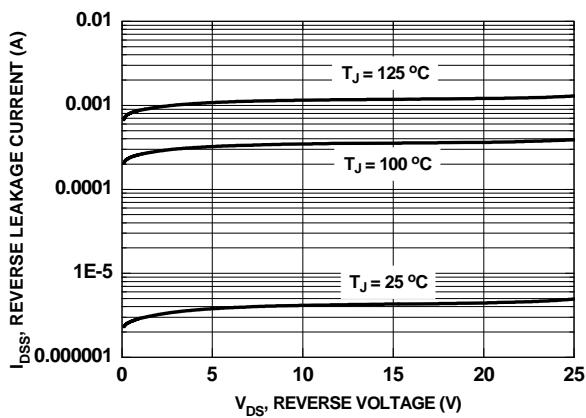
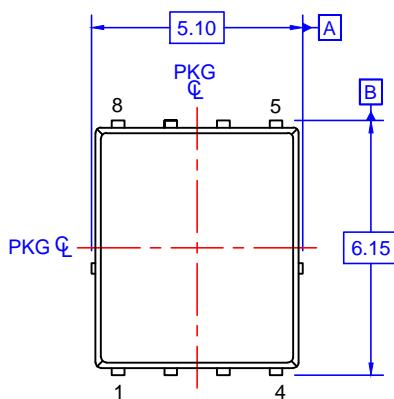
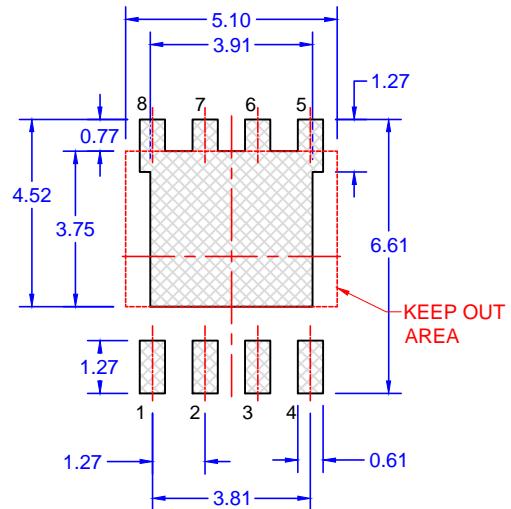
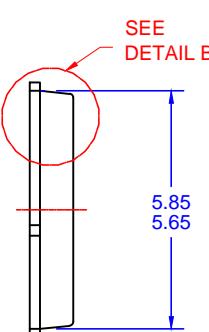


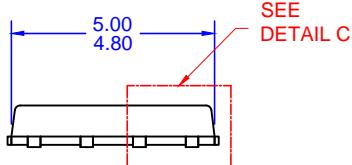
Figure 15. SyncFET™ body diode reverse leakage versus drain-source voltage



TOP VIEW

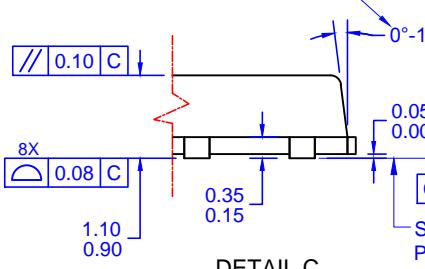


LAND PATTERN RECOMMENDATION

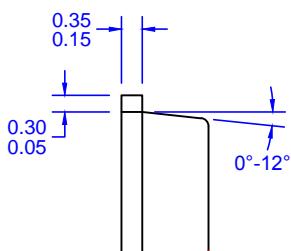


SIDE VIEW

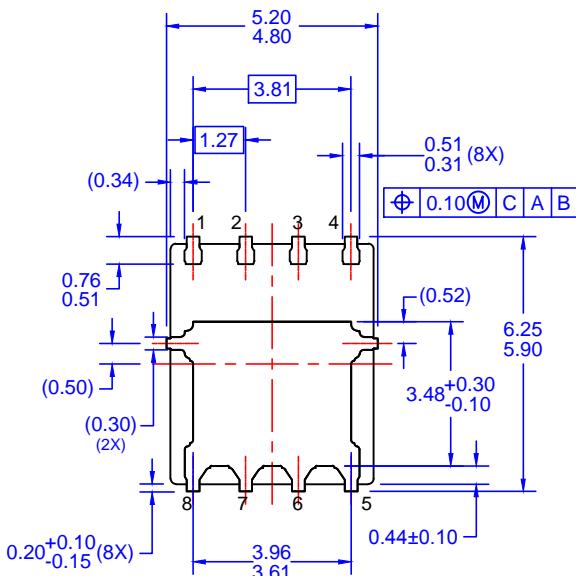
OPTIONAL DRAFT
ANGLE MAY APPEAR
ON FOUR SIDES
OF THE PACKAGE



DETAIL C



DETAIL B



BOTTOM VIEW

NOTES: UNLESS OTHERWISE SPECIFIED

NOTES: UNLESS OTHERWISE SPECIFIED

- A. PACKAGE STANDARD REFERENCE: JEDEC MO-240,
ISSUE A, VAR. AA.,
- B. DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH.
MOLD FLASH OR BURRS DOES NOT EXCEED 0.10MM.
- C. ALL DIMENSIONS ARE IN MILLIMETERS.
- D. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-2009.
- E. IT IS RECOMMENDED TO HAVE NO TRACES OR
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