√RoHS

Surface Mount Micromachined Accelerometer

The MMA series of silicon capacitive, micromachined accelerometers feature signal conditioning, a 4-pole low pass filter and temperature compensation. Zero-g offset full scale span and filter cut-off are factory set and require no external devices. A full system self-test capability verifies system functionality.

Features

- Integral Signal Conditioning
- · Linear Output
- Ratiometric Performance
- 4th Order Bessel Filter Preserves Pulse Shape Integrity
- Calibrated Self-test
- · Low Voltage Detect, Clock Monitor, and EPROM Parity Check Status
- · Transducer Hermetically Sealed at Wafer Level for Superior Reliability
- · Robust Design, High Shocks Survivability
- Qualified AEC-Q100, Rev. F Grade 2 (-40°C/+105°C)

Typical Applications

- · Vibration Monitoring and Recording
- Impact Monitoring

ORDERING INFORMATION				
Device Name	Temperature Range	Case No.	Package	
MMA2206EG	–40° to 125°C	475-01	SOIC-16	
MMA2206EGR2	–40° to 125°C	475-01	SOIC-16, Tape & Reel	
MMA2206KEG*	–40° to 125°C	475-01	SOIC-16	
MMA2206KEGR2*	–40° to 125°C	475-01	SOIC-16, Tape & Reel	

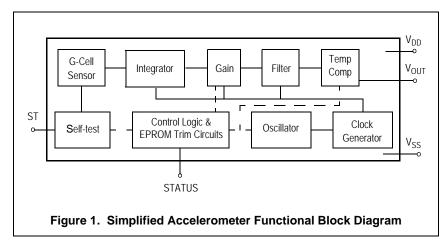
^{*}Part number sourced from a different facility.

MMA2206KEG

MMA2206KEG: X-AXIS SENSITIVITY
MICROMACHINED
ACCELEROMETER
±80g



KEG SUFFIX (Pb-FREE) 16-LEAD SOIC CASE 475-01



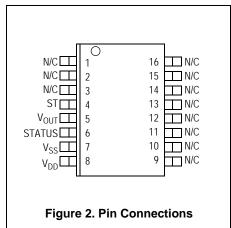




Table 1. Maximum Ratings

(Maximum ratings are the limits to which the device can be exposed without causing permanent damage.)

Rating	Symbol	Value	Unit
Powered Acceleration (all axes)	G _{pd}	1500	g
Unpowered Acceleration (all axes)	G _{upd}	2000	g
Supply Voltage	V _{DD}	-0.3 to +7.0	V
Drop Test (1)	D _{drop}	1.2	m
Storage Temperature Range	T _{stg}	-40 to +125	°C

^{1.} Dropped onto concrete surface from any axis.

ELECTRO STATIC DISCHARGE (ESD)

WARNING: This device is sensitive to electrostatic discharge.

Although the Freescale accelerometers contain internal 2 kV ESD protection circuitry, extra precaution must be taken by the user to protect the chip from ESD. A charge of over 2000 volts can accumulate on the human body or associated test equipment. A charge of this magnitude can alter the

performance or cause failure of the chip. When handling the accelerometer, proper ESD precautions should be followed to avoid exposing the device to discharges which may be detrimental to its performance.

Table 2. Operating Characteristics

(Unless otherwise noted: $-40^{\circ}\text{C} \le \text{T}_{A} \le +105^{\circ}\text{C}$, $4.75 \le \text{V}_{DD} \le 5.25$, Acceleration = 0g, Loaded output. (1)

Characteristic	Symbol	Min	Тур	Max	Unit
Operating Range ⁽²⁾					
Supply Voltage ⁽³⁾	V_{DD}	4.75	5.00	5.25	V
Supply Current	I _{DD}	3.0	_	6.0	mA
Operating Temperature Range	T _A	-40	_	+125	С
Acceleration Range	9 _{FS}	_	80	_	g
Output Signal					
Zero g ($T_A = 25$ °C, $V_{DD} = 5.0 \text{ V}$) ⁽⁴⁾	V _{OFF}	2.25	2.5	2.75	V
Zero g	$V_{OFF,V}$	0.45 V _{DD}	0.50 V _{DD}	0.55 V _{DD}	V
Sensitivity $(T_A = 25^{\circ}C, V_{DD} = 5.0 \text{ V})^{(5)}$	S	18.6	20.0	21.4	mV/g
Sensitivity	S_V	3.72	4.0	4.28	mV/g/V
Bandwidth Response	f_3dB	360	400	440	Hz
Nonlinearity	NL _{OUT}	-1.0	_	1.0	% FSO
Noise					
RMS (10 Hz – 1 kHz)	n _{RMS}	_	_	2.8	mVrms
Power Spectral Density	n _{PSD}	_	110	_	μV/(Hz ^{1/2})
Clock Noise (without RC load on output) ⁽⁶⁾	n _{CLK}	_	2.0	_	m∨pk
Self-Test					
Output Response ⁽⁷⁾	9st	8.4	12	15.6	g
Input Low	V_{IL}	V_{SS}	_	0.3 x V _{DD}	V
Input High	VIH	0.7 x V _{DD}	_	V_{DD}	V
Input Loading ⁽⁸⁾	I _{IN}	-30	-100	-260	μΑ
Response Time ⁽⁹⁾	t _{ST}	_	2.0	10	ms
Status ^{(10), (11)}					
Output Low (I _{load} = 100 μA)	V_{OL}	_	_	0.4	V
Output High ($I_{load} = 100 \mu A$)	V _{OH}	$V_{DD} - 0.8$	_	_	V
Minimum Supply Voltage (LVD Trip)	V _{LVD}	2.7	3.25	4.0	V
Clock Monitor Fail Detection Frequency	f _{min}	50	_	260	kHz
Output Stage Performance					
Electrical Saturation Recovery Time ⁽¹²⁾	t _{DELAY}	_	0.2	_	ms
Full Scale Output Range (I _{OUT} = 200 μA)	V_{FSO}	0.25	_	V _{DD} -0.25	V
Capacitive Load Drive ⁽¹³⁾	C_L	-	_	100	pF
Output Impedance	Z _O	_	300	_	Ω
Mechanical Characteristics					
Transverse Sensitivity ⁽¹⁴⁾	$V_{XZ,YZ}$	_	_	5.0	% FSO
Package Resonance	f _{PKG}	_	10	_	kHz

- 1. For a loaded output the measurements are observed after an RC filter consisting of a 1 kΩ resistor and a 0.01 μF capacitor to ground.
- 2. These limits define the range of operation for which the part will meet specification.
- 3. Within the supply range of 4.75 and 5.25 V, the device operates as a fully calibrated linear accelerometer. Beyond these supply limits the device may operate as a linear device but is not guaranteed to be in calibration.
- The device can measure both + and acceleration. With no input acceleration the output is at midsupply. For positive acceleration the output will increase above V_{DD}/2 and for negative acceleration the output will decrease below V_{DD}/2.
- 5. The device is calibrated at 20g.
- 6. At clock frequency \cong 70 kHz.
- 7. ΔV_{OFF} calculated with typical sensitivity.
- 8. The digital input pin has an internal pull-down current source to prevent inadvertent self test initiation due to external board level leakages.
- 9. Time for the output to reach 90% of its final value after a self-test is initiated.
- 10. The Status pin output is not valid following power-up until at least one rising edge has been applied to the self-test pin. The Status pin is high whenever the self-test input is high, as a means to check the connectivity of the self-test and Status pins in the application.
- 11. The Status pin output latches high if a Low Voltage Detection or Clock Frequency failure occurs, or the EPROM parity changes to odd. The Status pin can be reset low if the self-test pin is pulsed with a high input for at least 100 μs, unless a fault condition continues to exist.
- 12. Time for amplifiers to recover after an acceleration signal causes them to saturate.
- 13. Preserves phase margin (60°) to guarantee output amplifier stability.
- 14. A measure of the device's ability to reject an acceleration applied 90° from the true axis of sensitivity.

MMA2206KEG

PRINCIPLE OF OPERATION

The Freescale accelerometer is a surface-micromachined integrated-circuit accelerometer.

The device consists of a surface micromachined capacitive sensing cell (g-cell) and a CMOS signal conditioning ASIC contained in a single integrated circuit package. The sensing element is sealed hermetically at the wafer level using a bulk micromachined "cap" wafer.

The g-cell is a mechanical structure formed from semiconductor materials (polysilicon) using semiconductor processes (masking and etching). It can be modeled as two stationary plates with a moveable plate in-between. The center plate can be deflected from its rest position by subjecting the system to an acceleration (Figure 3).

When the center plate deflects, the distance from it to one fixed plate will increase by the same amount that the distance to the other plate decreases. The change in distance is a measure of acceleration.

The g-cell plates form two back-to-back capacitors (Figure 4). As the center plate moves with acceleration, the distance between the plates changes and each capacitor's value will change, (C = $A\epsilon/D$). Where A is the area of the plate, ϵ is the dielectric constant, and D is the distance between the plates.

The CMOS ASIC uses switched capacitor techniques to measure the g-cell capacitors and extract the acceleration data from the difference between the two capacitors. The ASIC also signal conditions and filters (switched capacitor) the signal, providing a high level output voltage that is ratiometric and proportional to acceleration.

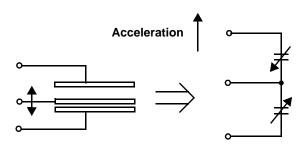


Figure 3. Transducer Physical Model

Figure 4. Equivalent Circuit Model

SPECIAL FEATURES

Filtering

The Freescale accelerometers contain an onboard 2-pole switched capacitor filter. A Bessel implementation is used because it provides a maximally flat delay response (linear phase) thus preserving pulse shape integrity. Because the filter is realized using switched capacitor techniques, there is no requirement for external passive components (resistors and capacitors) to set the cut-off frequency.

Self-Test

The sensor provides a self-test feature that allows the verification of the mechanical and electrical integrity of the accelerometer at any time before or after installation. This feature is critical in applications such as automotive airbag systems where system integrity must be ensured over the life of the vehicle. A fourth "plate" is used in the g-cell as a self-test plate. When the user applies a logic high input to the self-test pin, a calibrated potential is applied across the self-test plate and the moveable plate. The resulting electrostatic

force
$$\left(F_{e} = \frac{1}{2} \varepsilon A \frac{V^{2}}{\sigma^{2}}\right)$$
 causes the center plate to deflect.

The resultant deflection is measured by the accelerometer's control ASIC and a proportional output voltage results. This procedure assures that both the mechanical (g-cell) and electronic sections of the accelerometer are functioning.

Status

Freescale accelerometers include fault detection circuitry and a fault latch. The Status pin is an output from the fault latch, OR'd with self-test, and is set high whenever the following event occurs:

Parity of the EPROM bits becomes odd in number.

The fault latch can be reset by a rising edge on the self-test input pin, unless one (or more) of the fault conditions continues to exist.

BASIC CONNECTIONS

Pinout Description

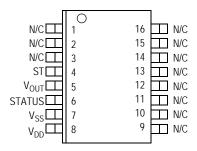


Table 3. Pin Descriptions

Pin No.	Pin Name	Description
1 thru 3	_	Leave unconnected.
4	ST	Logic input pin used to initiate self-test.
5	V _{OUT}	Output voltage of the accelerometer.
6	STATUS	Logic output pin to indicate fault.
7	V _{SS}	The power supply ground.
8	V_{DD}	The power supply input.
9 thru 13	Trim pins	Used for factory trim. Leave unconnected.
14 thru 16	_	No internal connection. Leave unconnected.

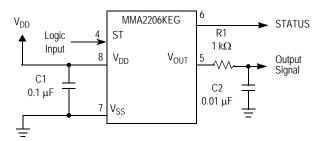


Figure 5. SOIC Accelerometer with Recommended Connection Diagram

PCB Layout

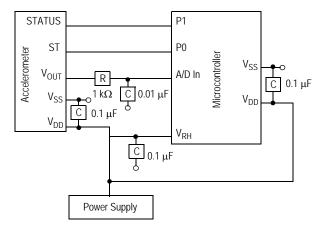
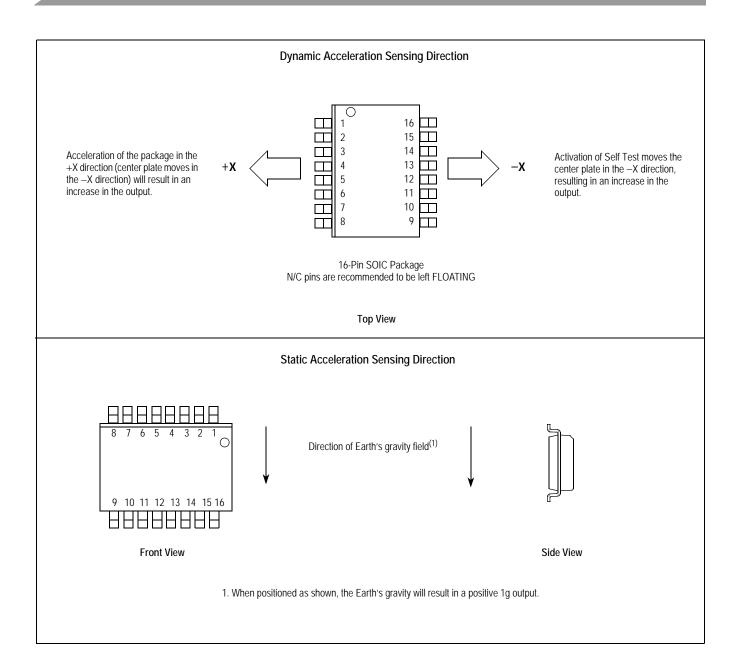


Figure 6. Recommended PCB Layout for Interfacing Accelerometer to Microcontroller

NOTES

- 1. Use a 0.1 μF capacitor on V_{DD} to decouple the power source.
- Physical coupling distance of the accelerometer to the microcontroller should be minimal.
- 3. Place a ground plane beneath the accelerometer to reduce noise, the ground plane should be attached to all of the open ended terminals shown in Figure 6.
- 4. Use an RC filter of 1 k Ω and 0.01 μ F on the output of the accelerometer to minimize clock noise (from the switched capacitor filter circuit).
- 5. PCB layout of power and ground should not couple power supply noise.
- 6. Accelerometer and microcontroller should not be a high current path.
- 7. A/D sampling rate and any external power supply switching frequency should be selected such that they do not interfere with the internal accelerometer sampling frequency. This will prevent aliasing errors.



MINIMUM RECOMMENDED FOOTPRINT FOR SURFACE MOUNTED APPLICATIONS

Surface mount board layout is a critical portion of the total design. The footprint for the surface mount packages must be the correct size to ensure proper solder connection interface between the board and the package. With the correct

footprint, the packages will self-align when subjected to a solder reflow process. It is always recommended to design boards with a solder mask layer to avoid bridging and shorting between solder pads.

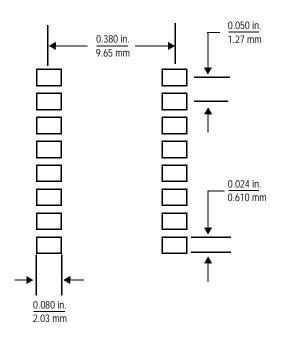
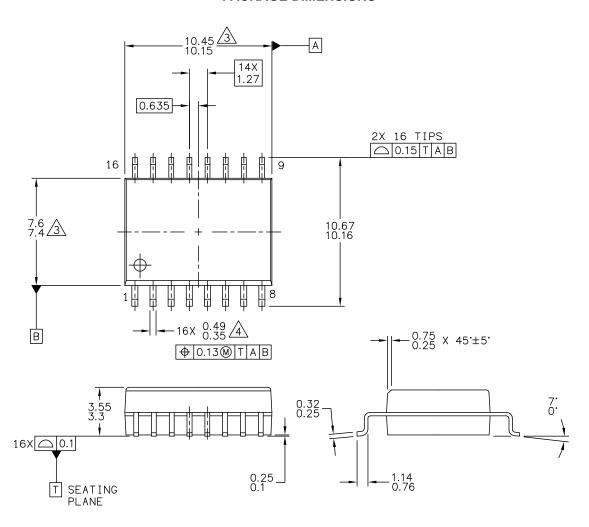


Figure 7. Footprint SOIC-16 (Case 475-01)

PACKAGE DIMENSIONS



© FREESCALE SEMICONDUCTOR, INC. ALL RIGHTS RESERVED.	MECHANICA	L OUTLINE	PRINT VERSION NO	OT TO SCALE
TITLE:		DOCUMENT NO): 98ASB16926C	REV: C
16 LEAD SOIC ACCELEROMETER		CASE NUMBER	R: 475–01	17 MAR 2005
NOOLLENOWE LEN		STANDARD: NO	N-JEDEC	

PAGE 1 OF 2

CASE 475-01 ISSUE C 16 LEAD SOIC

MMA2206KEG

PACKAGE DIMENSIONS

NOTES:

- 1. ALL DIMENSONS ARE IN MILLIMETERS.
- 2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M-1994.



3 THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.15 PER SIDE.



THIS DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION.
PROTRUSIONS SHALL NOT CAUSE THE LEAD WIDTH TO EXCEED 0.75

© FREESCALE SEMICONDUCTOR, INC. ALL RIGHTS RESERVED.	MECHANICA	NICAL OUTLINE PRINT VERSION NO		OT TO SCALE
TITLE:		DOCUMENT NO): 98ASB16926C	REV: C
16 LEAD SOIC ACCELEROMETER		CASE NUMBER	R: 475–01	17 MAR 2005
//OULLENGINE FEIN		STANDARD: NO	N-JEDEC	

PAGE 2 OF 2

CASE 475-01 ISSUE C 16 LEAD SOIC

MMA2206KEG

9

Table 4. Revision History

Revision number	Revision date	Description of changes
0	08/2011	Initial Release
1	09/2012	• Table 2. Operating Characteristics, added footnote for Self-Test Output Response, updated page 4: Principle of Operation

How to Reach Us:

Home Page: freescale.com

Web Support: freescale.com/support

Information in this document is provided solely to enable system and software implementers to use Freescale products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits based on the information in this document.

Freescale reserves the right to make changes without further notice to any products herein. Freescale makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale data sheets and/or specifications can and do vary in different applications, and actual performance may vary over time. All operating parameters, including "typicals," must be validated for each customer application by customer's technical experts. Freescale does not convey any license under its patent rights nor the rights of others. Freescale sells products pursuant to standard terms and conditions of sale, which can be found at the following address: freescale.com/salestermsandconditions.

Freescale, the Freescale logo, Energy Efficient Solutions logo, are trademarks of Freescale Semiconductor, Inc., Reg. U.S. Pat. & Tm. Off. Xtrinsic is a trademark of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners.

© 2012 Freescale Semiconductor, Inc.

Document Number: MMA2206KEG

Rev. 1 09/2012





Компания «Океан Электроники» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Поставка оригинальных импортных электронных компонентов напрямую с производств Америки, Европы и Азии, а так же с крупнейших складов мира;
- Широкая линейка поставок активных и пассивных импортных электронных компонентов (более 30 млн. наименований);
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Помощь Конструкторского Отдела и консультации квалифицированных инженеров;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Поставка электронных компонентов под контролем ВП;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- При необходимости вся продукция военного и аэрокосмического назначения проходит испытания и сертификацию в лаборатории (по согласованию с заказчиком);
- Поставка специализированных компонентов военного и аэрокосмического уровня качества (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Actel, Aeroflex, Peregrine, VPT, Syfer, Eurofarad, Texas Instruments, MS Kennedy, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Компания «Океан Электроники» является официальным дистрибьютором и эксклюзивным представителем в России одного из крупнейших производителей разъемов военного и аэрокосмического назначения «JONHON», а так же официальным дистрибьютором и эксклюзивным представителем в России производителя высокотехнологичных и надежных решений для передачи СВЧ сигналов «FORSTAR».



«JONHON» (основан в 1970 г.)

Разъемы специального, военного и аэрокосмического назначения:

(Применяются в военной, авиационной, аэрокосмической, морской, железнодорожной, горно- и нефтедобывающей отраслях промышленности)

«**FORSTAR**» (основан в 1998 г.)

ВЧ соединители, коаксиальные кабели, кабельные сборки и микроволновые компоненты:

(Применяются в телекоммуникациях гражданского и специального назначения, в средствах связи, РЛС, а так же военной, авиационной и аэрокосмической отраслях промышленности).



Телефон: 8 (812) 309-75-97 (многоканальный)

Факс: 8 (812) 320-03-32

Электронная почта: ocean@oceanchips.ru

Web: http://oceanchips.ru/

Адрес: 198099, г. Санкт-Петербург, ул. Калинина, д. 2, корп. 4, лит. А