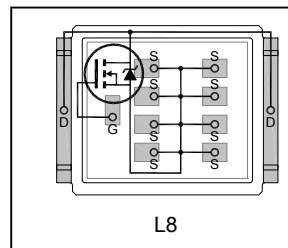


## Automotive DirectFET® Power MOSFET ②

- Advanced Process Technology
- Optimized for Automotive Motor Drive, DC-DC and other Heavy Load Applications
- Exceptionally Small Footprint and Low Profile
- High Power Density
- Low Parasitic Parameters
- Dual Sided Cooling
- 175°C Operating Temperature
- Repetitive Avalanche Capability for Robustness and Reliability
- Lead free, RoHS and Halogen free
- Automotive Qualified \*

|                          |              |
|--------------------------|--------------|
| $V_{(BR)DSS}$            | <b>75V</b>   |
| $R_{DS(on)}$ <b>typ.</b> | <b>1.8mΩ</b> |
| <b>max.</b>              | <b>2.3mΩ</b> |
| $I_D$ (Silicon Limited)  | <b>160A</b>  |
| $Q_g$ (typical)          | <b>200nC</b> |



## Applicable DirectFET® Outline and Substrate Outline ①

|           |           |  |  |           |           |  |           |           |           |  |
|-----------|-----------|--|--|-----------|-----------|--|-----------|-----------|-----------|--|
| <b>SB</b> | <b>SC</b> |  |  | <b>M2</b> | <b>M4</b> |  | <b>L4</b> | <b>L6</b> | <b>L8</b> |  |
|-----------|-----------|--|--|-----------|-----------|--|-----------|-----------|-----------|--|

**Description**

The AUIRF7759L2TR(1) combines the latest Automotive HEXFET® Power MOSFET Silicon technology with the advanced DirectFET® packaging to achieve the lowest on-state resistance in a package that has the footprint of a DPak (TO-252AA) and only 0.7 mm profile. The DirectFET® package is compatible with existing layout geometries used in power applications, PCB assembly equipment and vapor phase, infra-red or convection soldering techniques, when application note AN-1035 is followed regarding the manufacturing methods and processes. The DirectFET® package allows dual sided cooling to maximize thermal transfer in automotive power systems.

This HEXFET® Power MOSFET is designed for applications where efficiency and power density are essential. The advanced DirectFET® packaging platform coupled with the latest silicon technology allows the AUIRF7759L2TR(1) to offer substantial system level savings and performance improvement specifically in motor drive, high frequency DC-DC and other heavy load applications on ICE, HEV and EV platforms. This MOSFET utilizes the latest processing techniques to achieve low on-resistance and low  $Q_g$  per silicon area. Additional features of this MOSFET are 175°C operating junction temperature and high repetitive peak current capability. These features combine to make this MOSFET a highly efficient, robust and reliable device for high current automotive applications.

| Base Part Number | Package Type        | Standard Pack |          | Orderable Part Number |
|------------------|---------------------|---------------|----------|-----------------------|
|                  |                     | Form          | Quantity |                       |
| AUIRF7759L2      | DirectFET Large Can | Tape and Reel | 4000     | AUIRF7759L2TR         |

**Absolute Maximum Ratings**

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature ( $T_A$ ) is 25°C, unless otherwise specified.

|                                         | Parameter                                                           | Max.                      | Units |
|-----------------------------------------|---------------------------------------------------------------------|---------------------------|-------|
| V <sub>DS</sub>                         | Drain-to-Source Voltage                                             | 75                        | V     |
| V <sub>GS</sub>                         | Gate-to-Source Voltage                                              | ±20                       |       |
| I <sub>D</sub> @ T <sub>C</sub> = 25°C  | Continuous Drain Current, V <sub>GS</sub> @ 10V (Silicon Limited) ④ | 160                       | A     |
| I <sub>D</sub> @ T <sub>C</sub> = 100°C | Continuous Drain Current, V <sub>GS</sub> @ 10V (Silicon Limited) ④ | 113                       |       |
| I <sub>D</sub> @ T <sub>A</sub> = 25°C  | Continuous Drain Current, V <sub>GS</sub> @ 10V (Silicon Limited) ③ | 26                        |       |
| I <sub>D</sub> @ T <sub>C</sub> = 25°C  | Continuous Drain Current, V <sub>GS</sub> @ 10V (Package Limited)   | 375                       |       |
| I <sub>DM</sub>                         | Pulsed Drain Current ⑤                                              | 640                       |       |
| P <sub>D</sub> @T <sub>C</sub> = 25°C   | Power Dissipation ④                                                 | 125                       | W     |
| P <sub>D</sub> @T <sub>C</sub> = 100°C  | Power Dissipation ④                                                 | 63                        |       |
| P <sub>D</sub> @T <sub>A</sub> = 25°C   | Power Dissipation ③                                                 | 3.3                       |       |
| E <sub>AS</sub>                         | Single Pulse Avalanche Energy (Thermally Limited) ⑥                 | 257                       | mJ    |
| I <sub>AR</sub>                         | Avalanche Current ⑤                                                 | See Fig. 16, 17, 18a, 18b | A     |
| E <sub>AR</sub>                         | Repetitive Avalanche Energy ⑤                                       |                           | mJ    |
| T <sub>P</sub>                          | Peak Soldering Temperature                                          | 270                       | °C    |
| T <sub>J</sub>                          | Operating Junction and                                              | -55 to + 175              |       |
| T <sub>STG</sub>                        | Storage Temperature Range                                           |                           |       |

HEXFET® is a registered trademark of Infineon.

\*Qualification standards can be found at [www.infineon.com](http://www.infineon.com)

**Thermal Resistance**

| Symbol             | Parameter                | Typ. | Max. | Units |
|--------------------|--------------------------|------|------|-------|
| $R_{\theta JA}$    | Junction-to-Ambient ③    | —    | 45   | °C/W  |
| $R_{\theta JA}$    | Junction-to-Ambient ⑧    | 12.5 | —    |       |
| $R_{\theta JA}$    | Junction-to-Ambient ⑨    | 20   | —    |       |
| $R_{\theta J-Can}$ | Junction-to-Can ④ ⑩      | —    | 1.2  |       |
| $R_{\theta J-PCB}$ | Junction-to-PCB Mounted  | —    | 0.5  |       |
|                    | Linear Derating Factor ④ | 0.83 |      | W/°C  |

**Static Electrical Characteristics @  $T_J = 25^\circ\text{C}$  (unless otherwise specified)**

| Symbol                          | Parameter                            | Min. | Typ. | Max. | Units      | Conditions                                             |
|---------------------------------|--------------------------------------|------|------|------|------------|--------------------------------------------------------|
| $V_{(BR)DSS}$                   | Drain-to-Source Breakdown Voltage    | 75   | —    | —    | V          | $V_{GS} = 0V, I_D = 250\mu A$                          |
| $\Delta V_{(BR)DSS}/\Delta T_J$ | Breakdown Voltage Temp. Coefficient  | —    | 0.02 | —    | V/°C       | Reference to $25^\circ\text{C}$ , $I_D = 2.0\text{mA}$ |
| $R_{DS(on)}$                    | Static Drain-to-Source On-Resistance | —    | 1.8  | 2.3  | m $\Omega$ | $V_{GS} = 10V, I_D = 96A$ ⑦                            |
| $V_{GS(th)}$                    | Gate Threshold Voltage               | 2.0  | 3.0  | 4.0  | V          | $V_{DS} = V_{GS}, I_D = 250\mu A$                      |
| $\Delta V_{GS(th)}/\Delta T_J$  | Gate Threshold Voltage Coefficient   | —    | -11  | —    | mV/°C      |                                                        |
| $g_{fs}$                        | Forward Transconductance             | 74   | —    | —    | S          | $V_{DS} = 25V, I_D = 96A$                              |
| $I_{DSS}$                       | Drain-to-Source Leakage Current      | —    | —    | 20   | $\mu A$    | $V_{DS} = 75V, V_{GS} = 0V$                            |
|                                 |                                      | —    | —    | 250  |            | $V_{DS} = 60V, V_{GS} = 0V, T_J = 125^\circ\text{C}$   |
| $I_{GSS}$                       | Gate-to-Source Forward Leakage       | —    | —    | 100  | nA         | $V_{GS} = 20V$                                         |
|                                 | Gate-to-Source Reverse Leakage       | —    | —    | -100 |            | $V_{GS} = -20V$                                        |

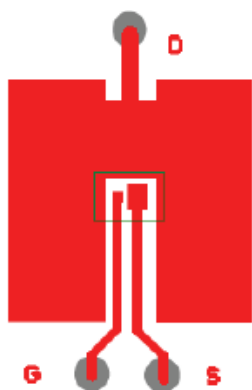
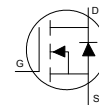
**Dynamic Electrical Characteristics @  $T_J = 25^\circ\text{C}$  (unless otherwise specified)**

| Symbol       | Parameter                            | Min. | Typ.  | Max. | Units    | Conditions                                                         |
|--------------|--------------------------------------|------|-------|------|----------|--------------------------------------------------------------------|
| $Q_g$        | Total Gate Charge                    | —    | 200   | 300  | nC       | $V_{DS} = 38V$<br>$V_{GS} = 10V$<br>$I_D = 96A$<br>See Fig.11      |
| $Q_{gs1}$    | Gate-to-Source Charge                | —    | 37    | —    |          |                                                                    |
| $Q_{gs2}$    | Gate-to-Source Charge                | —    | 11    | —    |          |                                                                    |
| $Q_{gd}$     | Gate-to-Drain ("Miller") Charge      | —    | 62    | 93   |          |                                                                    |
| $Q_{godr}$   | Gate Charge Overdrive                | —    | 91    | —    |          |                                                                    |
| $Q_{sw}$     | Switch Charge ( $Q_{gs2} + Q_{gd}$ ) | —    | 73    | —    | nC       | $V_{DS} = 16V, V_{GS} = 0V$                                        |
| $Q_{oss}$    | Output Charge                        | —    | 60    | —    |          |                                                                    |
| $R_G$        | Internal Gate Resistance             | —    | 1.1   | —    | $\Omega$ |                                                                    |
| $t_{d(on)}$  | Turn-On Delay Time                   | —    | 18    | —    | ns       | $V_{DD} = 38V, V_{GS} = 10V$ ⑦<br>$I_D = 96A$<br>$R_G = 1.8\Omega$ |
| $t_r$        | Rise Time                            | —    | 37    | —    |          |                                                                    |
| $t_{d(off)}$ | Turn-Off Delay Time                  | —    | 80    | —    |          |                                                                    |
| $t_f$        | Fall Time                            | —    | 33    | —    |          |                                                                    |
| $C_{iss}$    | Input Capacitance                    | —    | 12222 | —    | pF       | $V_{GS} = 0V$                                                      |
| $C_{oss}$    | Output Capacitance                   | —    | 1465  | —    |          | $V_{DS} = 25V$                                                     |
| $C_{rss}$    | Reverse Transfer Capacitance         | —    | 609   | —    |          | $f = 1.0\text{MHz}$                                                |
| $C_{oss}$    | Output Capacitance                   | —    | 7457  | —    |          | $V_{GS} = 0V, V_{DS} = 1.0V, f = 1.0\text{MHz}$                    |
| $C_{oss}$    | Output Capacitance                   | —    | 955   | —    |          | $V_{GS} = 0V, V_{DS} = 60V, f = 1.0\text{MHz}$                     |

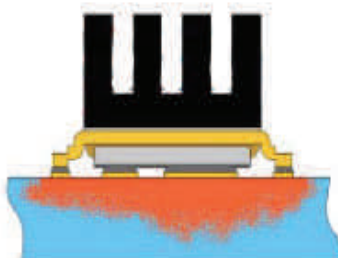
Notes ① through ⑩ are on page 3

**Diode Characteristics**

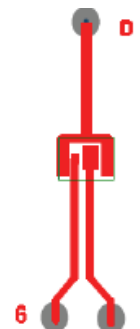
| Symbol   | Parameter                                 | Min. | Typ. | Max. | Units | Conditions                                                              |
|----------|-------------------------------------------|------|------|------|-------|-------------------------------------------------------------------------|
| $I_S$    | Continuous Source Current<br>(Body Diode) | —    | —    | 160  | A     | MOSFET symbol<br>showing the<br>integral reverse<br>p-n junction diode. |
| $I_{SM}$ | Pulsed Source Current<br>(Body Diode) ⑤   | —    | —    | 640  |       |                                                                         |
| $V_{SD}$ | Diode Forward Voltage                     | —    | —    | 1.3  | V     | $T_J = 25^\circ\text{C}$ , $I_S = 96\text{A}$ , $V_{GS} = 0\text{V}$ ⑦  |
| $t_{rr}$ | Reverse Recovery Time                     | —    | 64   | 96   | ns    | $T_J = 25^\circ\text{C}$ , $I_F = 96\text{A}$ , $V_{DD} = 38\text{V}$   |
| $Q_{rr}$ | Reverse Recovery Charge                   | —    | 150  | 225  | nC    | $dv/dt = 100\text{A}/\mu\text{s}$ ⑦                                     |



③ Surface mounted on 1 in. square Cu board (still air).

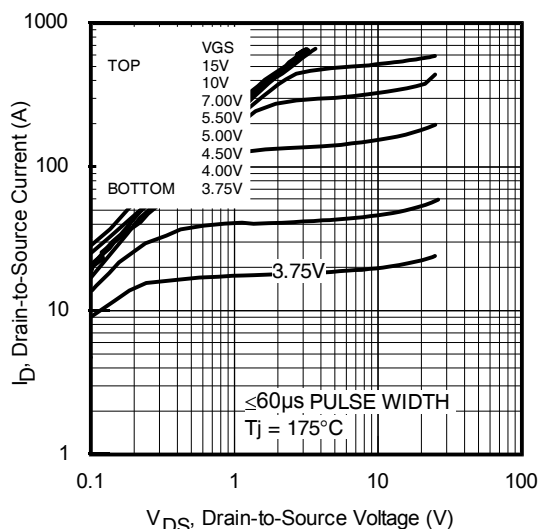
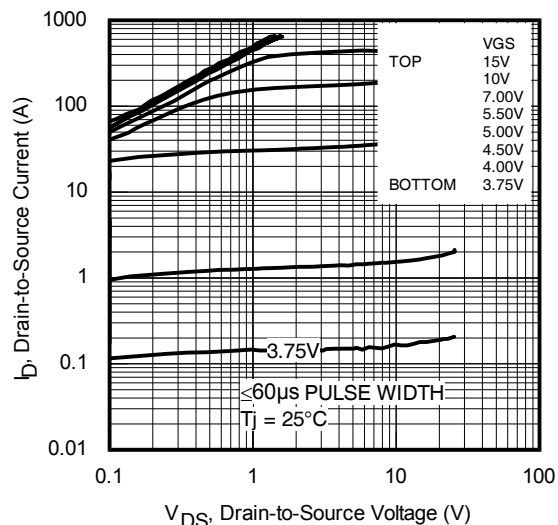


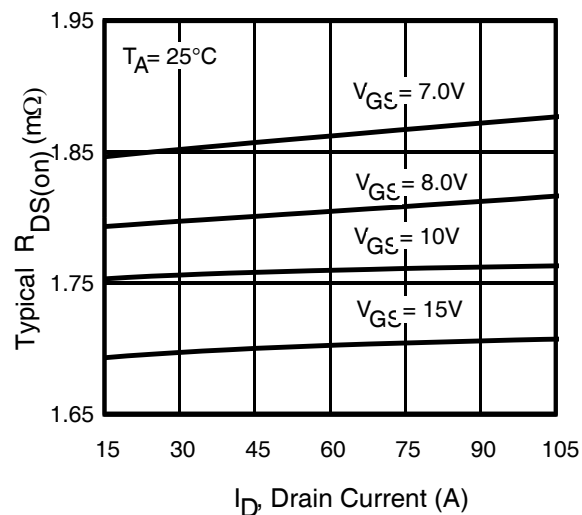
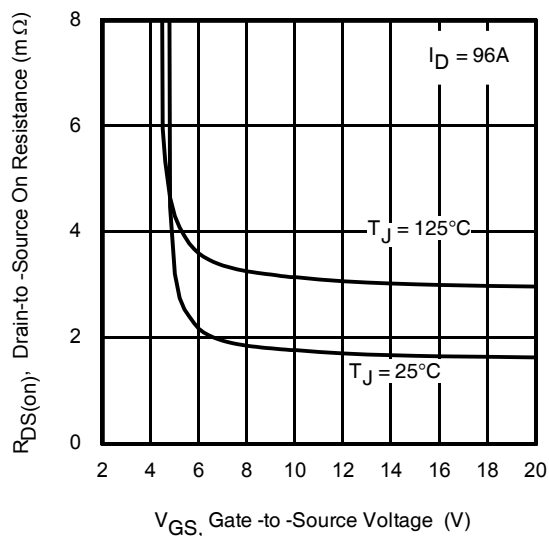
⑨ Mounted to a PCB with small clip heatsink (still air)

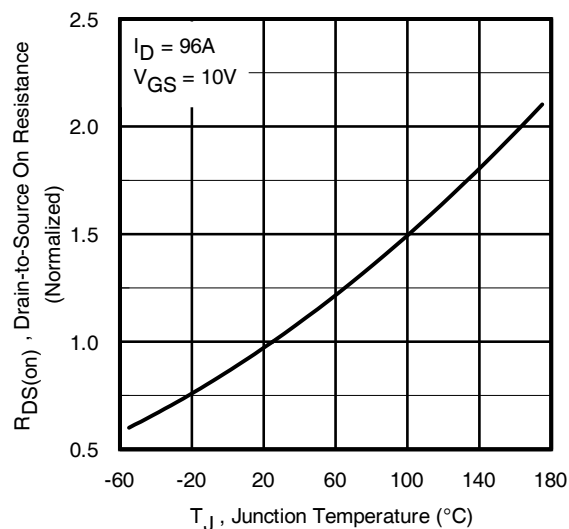
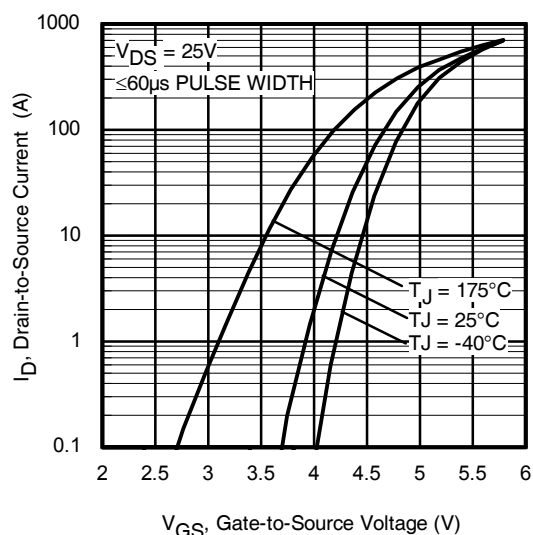


⑨ Mounted on minimum footprint full size board with metalized back and with small clip heatsink (still air).

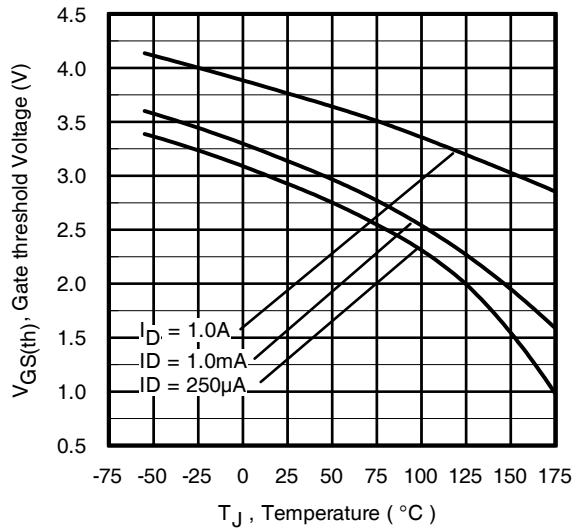
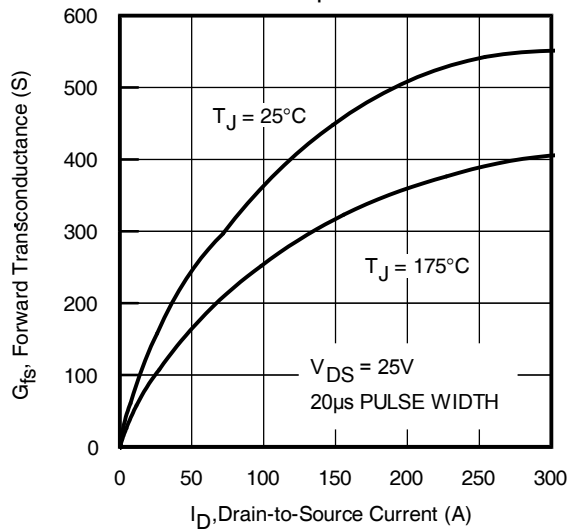
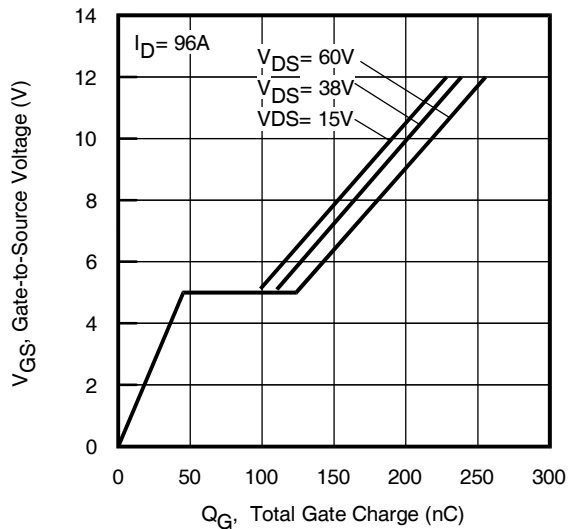
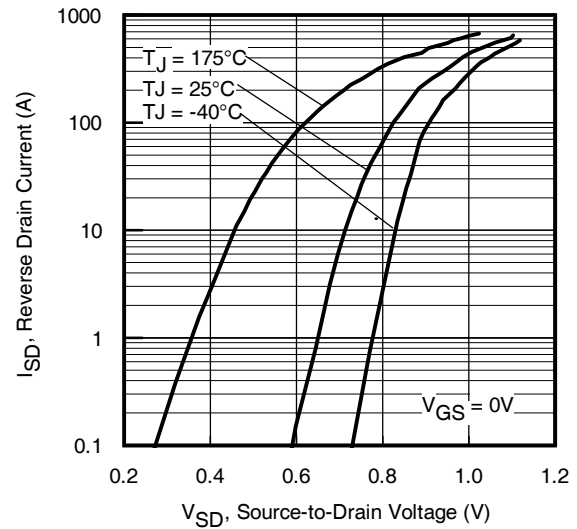
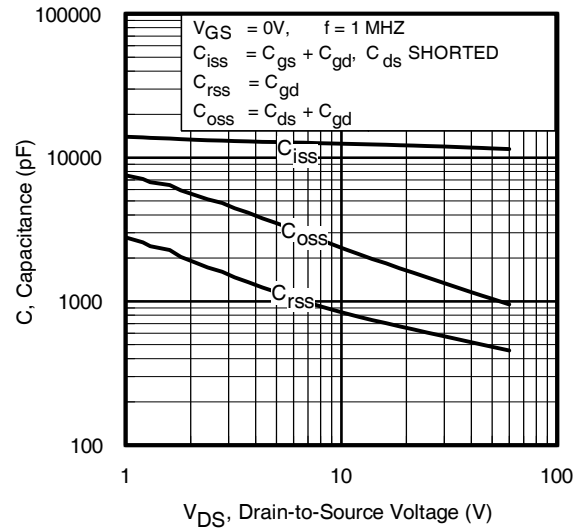
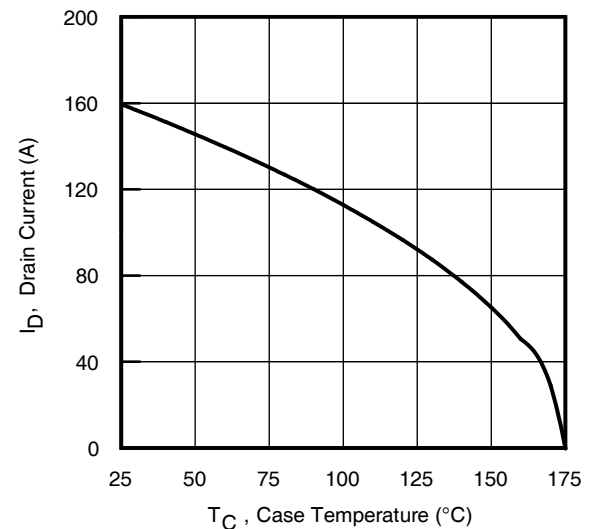
- ① Click on this section to link to the appropriate technical paper.
- ② Click on this section to link to the DirectFET® Website.
- ③ Surface mounted on 1 in. square Cu board, steady state.
- ④  $T_C$  measured with thermocouple mounted to top (Drain) of part.
- ⑤ Repetitive rating; pulse width limited by max. junction temperature.
- ⑥ Starting  $T_J = 25^\circ\text{C}$ ,  $L = 0.056\text{mH}$ ,  $R_G = 25\Omega$ ,  $I_{AS} = 96\text{A}$ .
- ⑦ Pulse width  $\leq 400\mu\text{s}$ ; duty cycle  $\leq 2\%$ .
- ⑧ Used double sided cooling, mounting pad with large heatsink.
- ⑨ Mounted on minimum footprint full size board with metalized back and with small clip heat sink.
- ⑩  $R_\theta$  is measured at  $T_J$  of approximately  $90^\circ\text{C}$ .

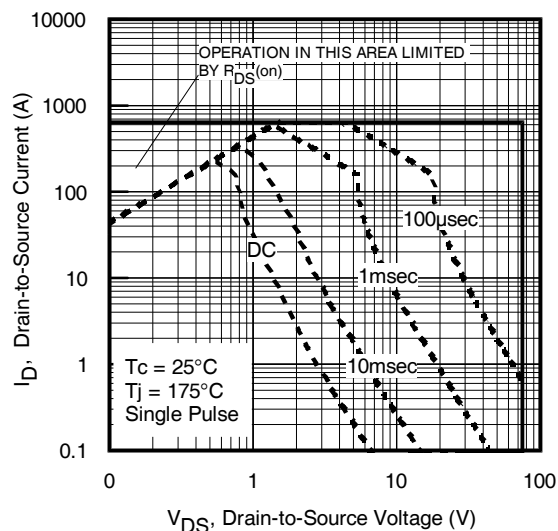
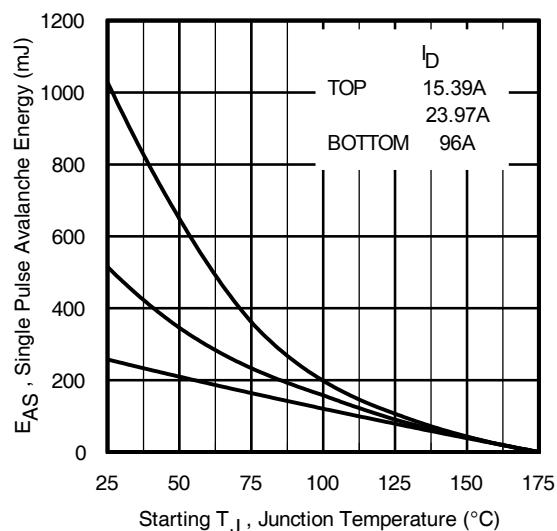
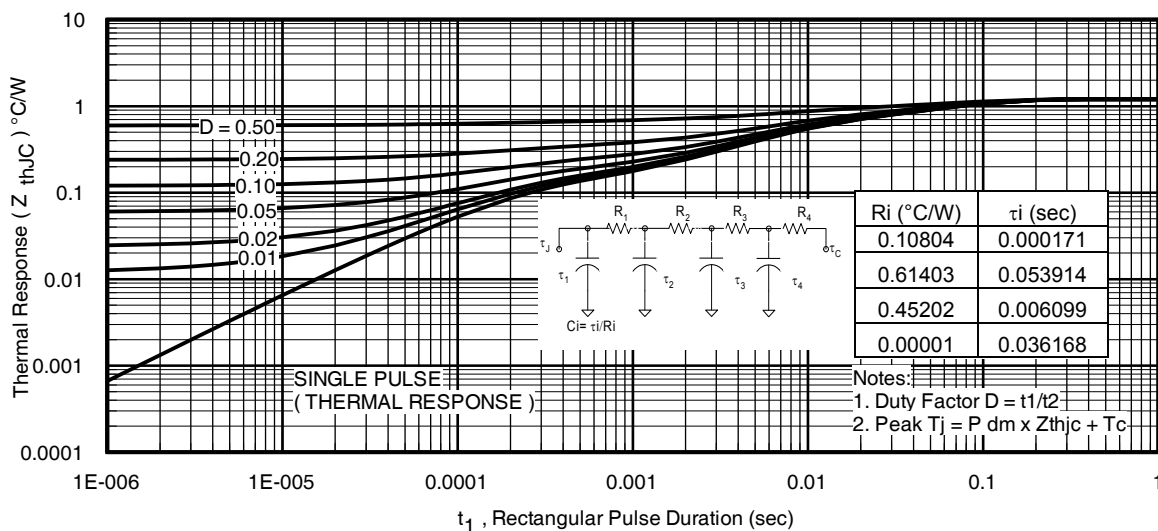
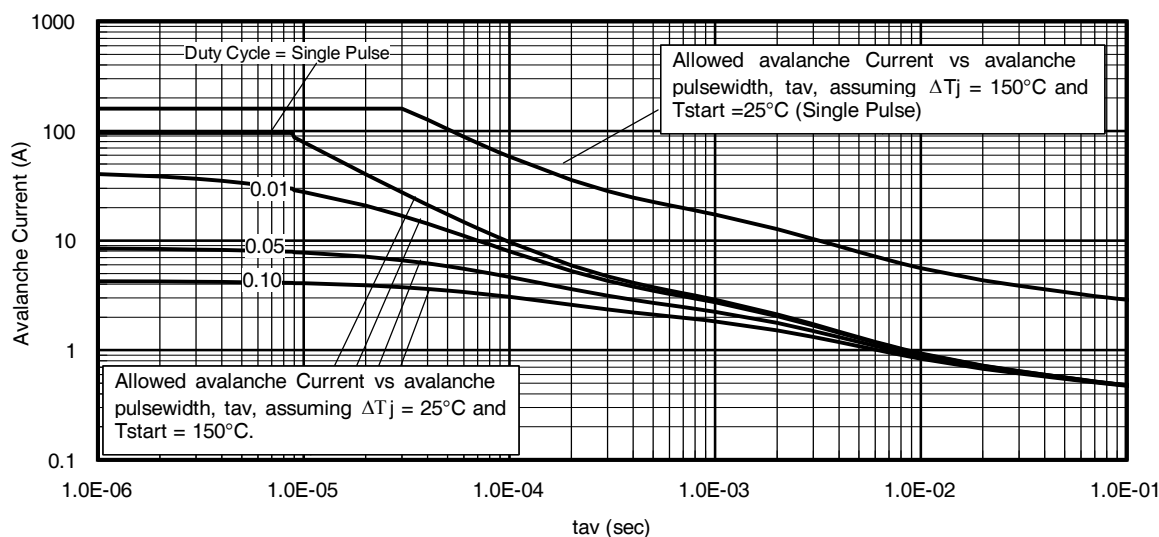

**Fig. 1** Typical Output Characteristics

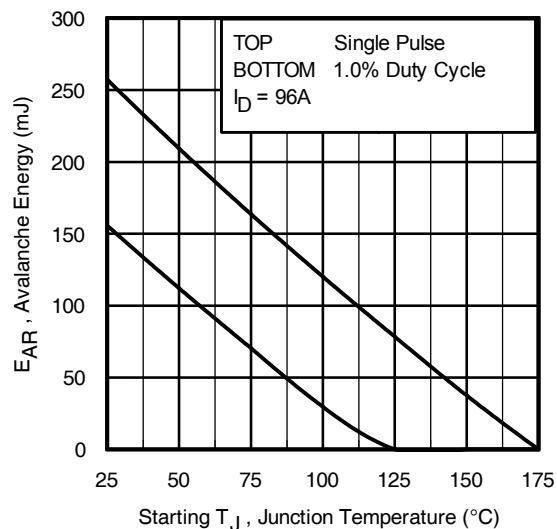
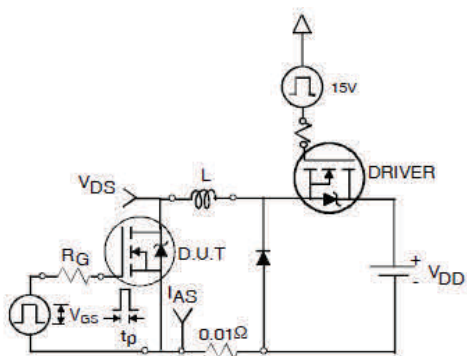
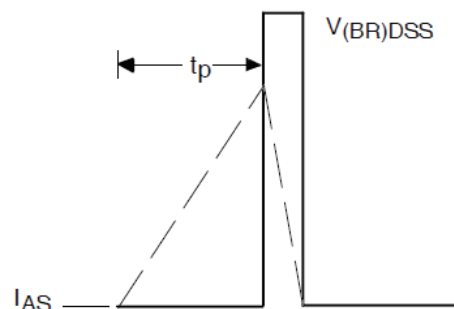
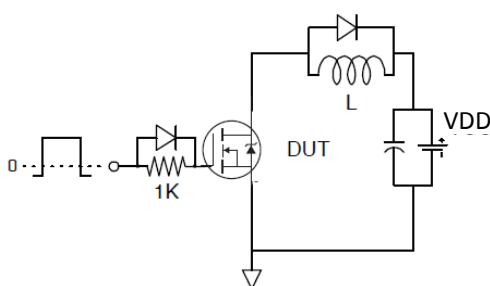
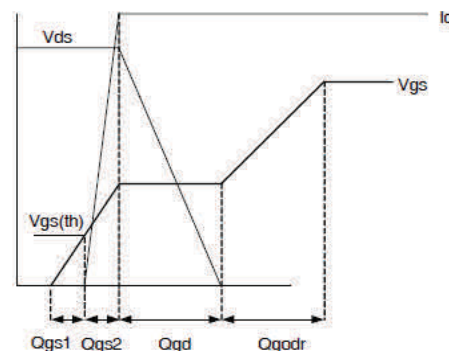
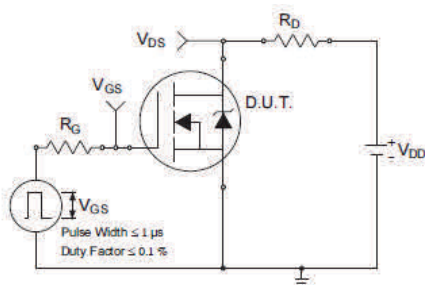
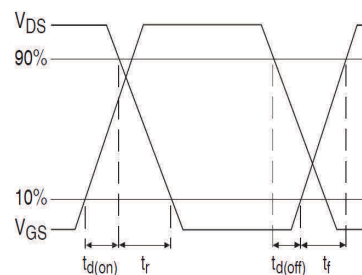
**Fig. 2** Typical Output Characteristics

**Fig. 3** Typical On-Resistance vs. Gate Voltage

**Fig. 4** Typical On-Resistance vs. Drain Current

**Fig. 5.** Typical Transfer Characteristics

**Fig. 6.** Normalized On-Resistance vs. Temperature


**Fig. 7** Typical Threshold Voltage vs. Junction Temperature

**Fig. 9.** Typical Forward Trans conductance vs. Drain Current

**Fig 11.** Typical Gate Charge vs. Gate-to-Source Voltage

**Fig 8.** Typical Source-Drain Diode Forward Voltage

**Fig 10.** Typical Capacitance vs. Drain-to-Source Voltage

**Fig 12.** Maximum Drain Current vs. Case Temperature


**Fig 13. Maximum Safe Operating Area**

**Fig 14. Maximum Avalanche Energy vs. Temperature**

**Fig 15. Maximum Effective Transient Thermal Impedance, Junction-to-Case**

**Fig 16. Typical Avalanche Current vs. Pulse Width**


**Fig 17.** Maximum Avalanche Energy vs. Temperature

**Fig 18a.** Unclamped Inductive Test Circuit

**Fig 18b.** Unclamped Inductive Waveforms

**Fig 19a.** Gate Charge Test Circuit

**Fig 19b.** Gate Charge Waveform

**Fig 20a.** Switching Time Test Circuit

**Fig 20b.** Switching Time Waveforms

**Notes on Repetitive Avalanche Curves , Figures 16, 17:**
**(For further info, see AN-1005 at [www.infineon.com](http://www.infineon.com))**

1. Avalanche failures assumption:  
Purely a thermal phenomenon and failure occurs at a temperature far in excess of  $T_{jmax}$ . This is validated for every part type.
2. Safe operation in Avalanche is allowed as long as  $T_{jmax}$  is not exceeded.
3. Equation below based on circuit and waveforms shown in Figures 18a, 18b.
4.  $P_{D(ave)}$  = Average power dissipation per single avalanche pulse.
5.  $BV$  = Rated breakdown voltage (1.3 factor accounts for voltage increase during avalanche).
6.  $I_{av}$  = Allowable avalanche current.
7.  $\Delta T$  = Allowable rise in junction temperature, not to exceed  $T_{jmax}$  (assumed as 25°C in Figure 16, 17).  
 $t_{av}$  = Average time in avalanche.  
 $D$  = Duty cycle in avalanche =  $t_{av} \cdot f$   
 $Z_{thJC}(D, t_{av})$  = Transient thermal resistance, see Figures 15)

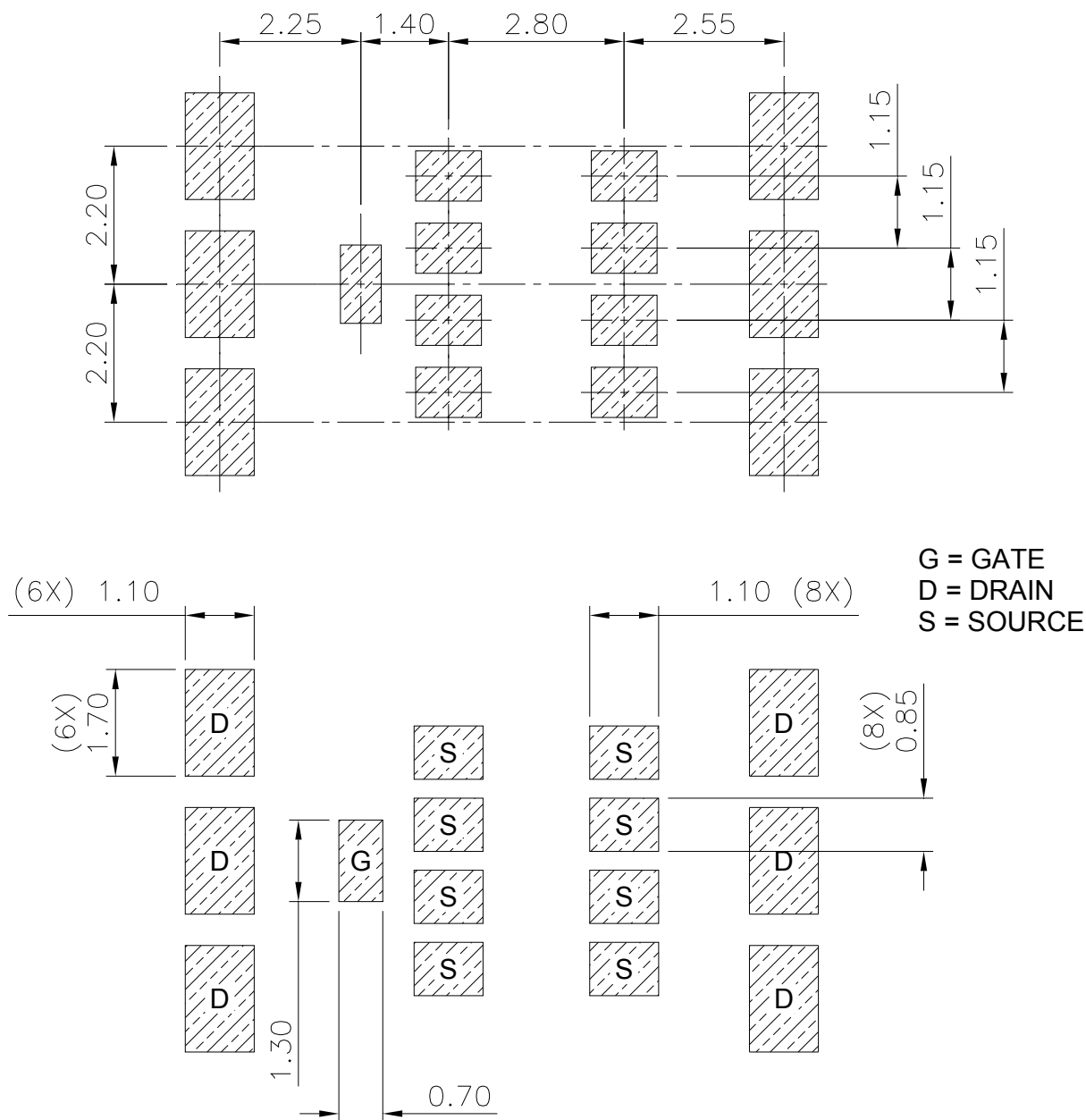
$$P_{D(ave)} = 1/2 (1.3 \cdot BV \cdot I_{av}) = \Delta T / Z_{thJC}$$

$$I_{av} = 2\Delta T / [1.3 \cdot BV \cdot Z_{th}]$$

$$E_{AS(AR)} = P_{D(ave)} \cdot t_{av}$$

**DirectFET® Board Footprint, L8 (Large Size Can).**

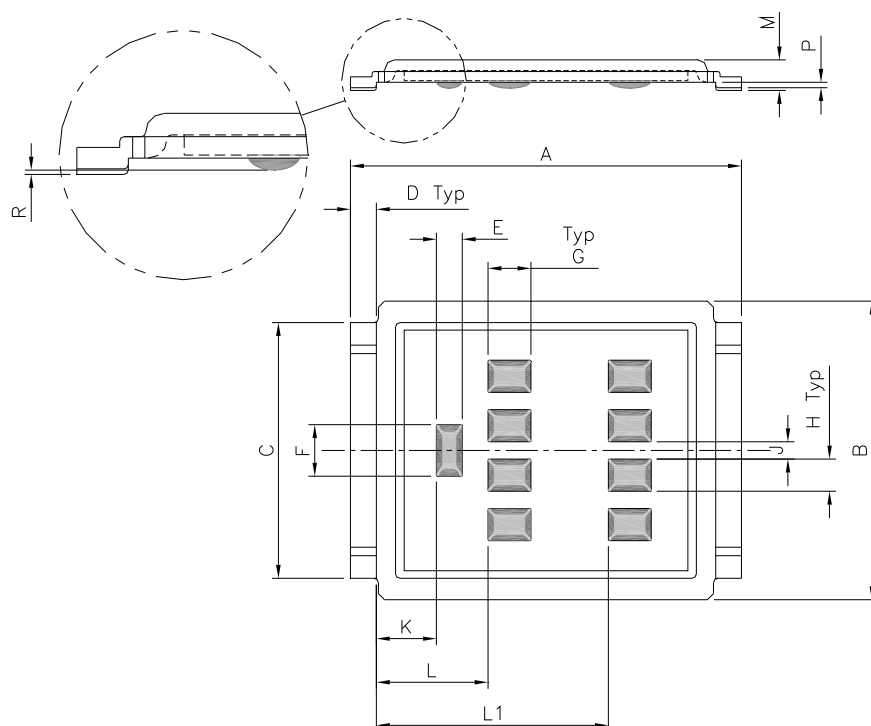
Please see DirectFET® application note AN-1035 for all details regarding the assembly of DirectFET®. This includes all recommendations for stencil and substrate designs.



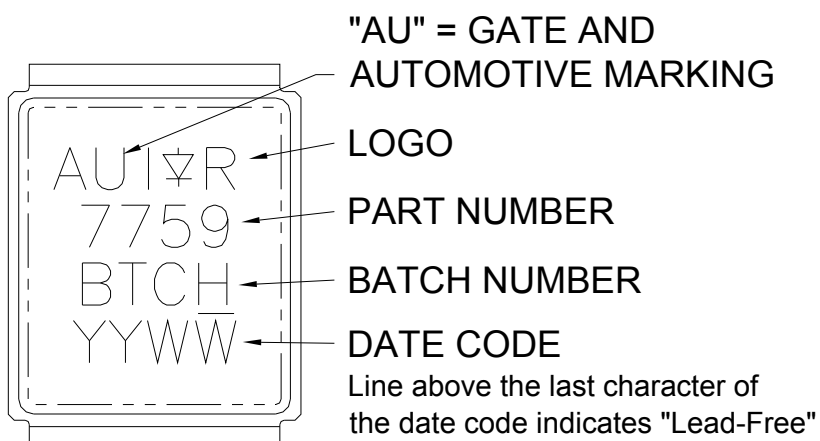
Note: For the most current drawing please refer to IR website at <http://www.irf.com/package/>

**DirectFET® Outline Dimension, L8 (Large Size Can).**

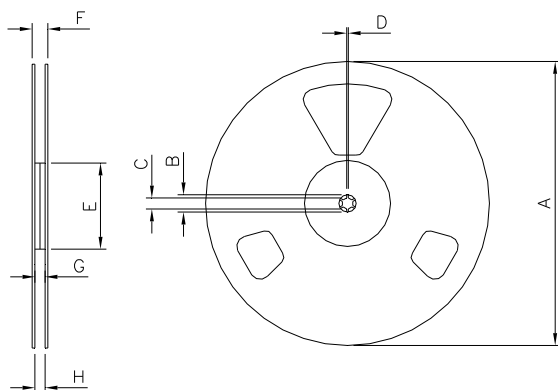
Please see DirectFET® application note AN-1035 for all details regarding the assembly of DirectFET®. This includes all recommendations for stencil and substrate designs.



| CODE | METRIC |      | IMPERIAL |       |
|------|--------|------|----------|-------|
|      | MIN    | MAX  | MIN      | MAX   |
| A    | 9.05   | 9.15 | 0.356    | 0.360 |
| B    | 6.85   | 7.10 | 0.270    | 0.280 |
| C    | 5.90   | 6.00 | 0.232    | 0.236 |
| D    | 0.55   | 0.65 | 0.022    | 0.026 |
| E    | 0.58   | 0.62 | 0.023    | 0.024 |
| F    | 1.18   | 1.22 | 0.046    | 0.048 |
| G    | 0.98   | 1.02 | 0.039    | 0.040 |
| H    | 0.73   | 0.77 | 0.029    | 0.030 |
| J    | 0.38   | 0.42 | 0.015    | 0.017 |
| K    | 1.35   | 1.45 | 0.053    | 0.057 |
| L    | 2.55   | 2.65 | 0.100    | 0.104 |
| L1   | 5.35   | 5.45 | 0.211    | 0.215 |
| M    | 0.68   | 0.74 | 0.027    | 0.029 |
| P    | 0.09   | 0.17 | 0.003    | 0.007 |
| R    | 0.02   | 0.08 | 0.001    | 0.003 |

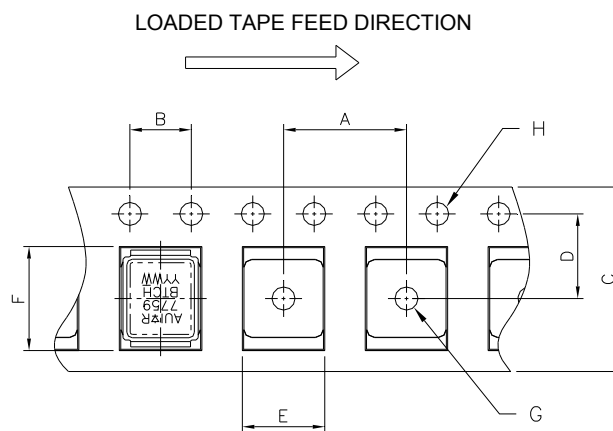
**DirectFET® Part Marking**


Note: For the most current drawing please refer to IR website at <http://www.irf.com/package/>

**DirectFET® Tape & Reel Dimension (Showing component orientation)**


NOTE: Controlling dimensions in mm  
Std reel quantity is 4000 parts, ordered as AUIRF7759L2TR.

| REEL DIMENSIONS            |        |        |          |       |
|----------------------------|--------|--------|----------|-------|
| STANDARD OPTION (QTY 4000) |        |        |          |       |
| CODE                       | METRIC |        | IMPERIAL |       |
|                            | MIN    | MAX    | MIN      | MAX   |
| A                          | 330.00 | N.C    | 12.992   | N.C   |
| B                          | 20.20  | N.C    | 0.795    | N.C   |
| C                          | 12.80  | 13.20  | 0.504    | 0.520 |
| D                          | 1.50   | N.C    | 0.059    | N.C   |
| E                          | 99.00  | 100.00 | 3.900    | 3.940 |
| F                          | N.C    | 22.40  | N.C      | 0.880 |
| G                          | 16.40  | 18.40  | 0.650    | 0.720 |
| H                          | 15.90  | 19.40  | 0.630    | 0.760 |



NOTE: CONTROLLING  
DIMENSIONS IN MM

| DIMENSIONS |        |       |          |       |
|------------|--------|-------|----------|-------|
| CODE       | METRIC |       | IMPERIAL |       |
|            | MIN    | MAX   | MIN      | MAX   |
| A          | 11.90  | 12.10 | 4.69     | 0.476 |
| B          | 3.90   | 4.10  | 0.154    | 0.161 |
| C          | 15.90  | 16.30 | 0.623    | 0.642 |
| D          | 7.40   | 7.60  | 0.291    | 0.299 |
| E          | 7.20   | 7.40  | 0.283    | 0.291 |
| F          | 9.90   | 10.10 | 0.390    | 0.398 |
| G          | 1.50   | N.C   | 0.059    | N.C   |
| H          | 1.50   | 1.60  | 0.059    | 0.063 |

Note: For the most current drawing please refer to IR website at <http://www.irf.com/package/>

**Qualification Information**

|                                   |                      |                                                                                                                                                                               |      |
|-----------------------------------|----------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------|
| <b>Qualification Level</b>        |                      | Automotive<br>(per AEC-Q101)                                                                                                                                                  |      |
|                                   |                      | Comments: This part number(s) passed Automotive qualification. Infineon's Industrial and Consumer qualification level is granted by extension of the higher Automotive level. |      |
| <b>Moisture Sensitivity Level</b> |                      | DFET2 Large Can                                                                                                                                                               | MSL1 |
| <b>ESD</b>                        | Machine Model        | Class M4 (+/- 800V) <sup>†</sup><br>AEC-Q101-002                                                                                                                              |      |
|                                   | Human Body Model     | Class H2 (+/- 6000V) <sup>†</sup><br>AEC-Q101-001                                                                                                                             |      |
|                                   | Charged Device Model | N/A<br>AEC-Q101-005                                                                                                                                                           |      |
| <b>RoHS Compliant</b>             |                      | Yes                                                                                                                                                                           |      |

† Highest passing voltage.

**Revision History**

| Date      | Comments                                                                                                                                                                                  |
|-----------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 10/5/2015 | <ul style="list-style-type: none"> <li>Updated datasheet with corporate template</li> <li>Corrected ordering table on page 1.</li> <li>Updated Tape and Reel option on page 10</li> </ul> |

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