

TOSHIBA BiCD Integrated Circuit Silicon Monolithic

## TBD62064APG, TBD62064AFG

### 4channel high current sink type DMOS transistor array

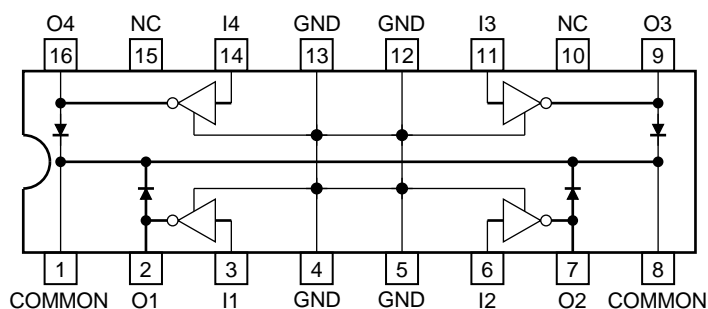
TBD62064A series are DMOS transistor array with 4 circuits. It has a clamp diode for switching inductive loads built-in in each output. Please be careful about thermal conditions during use.

#### Features

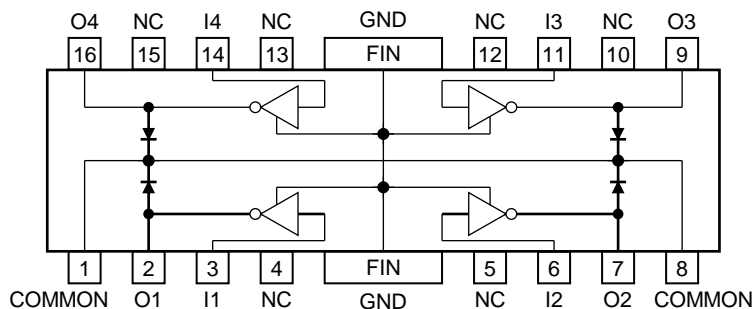
- 4 circuits built-in
- High voltage :  $V_{OUT} = 50\text{ V (MAX)}$
- High current :  $I_{OUT} = 1.5\text{ A/ch (MAX)}$
- Input voltage(output on) :  $2.5\text{ V (MIN)}$
- Input voltage(output off) :  $0.6\text{ V (MAX)}$
- Package : PG type DIP16-P-300-2.54A  
FG type HSOP16-P-300-1.00

#### Pin connection (top view)

##### TBD62064APG

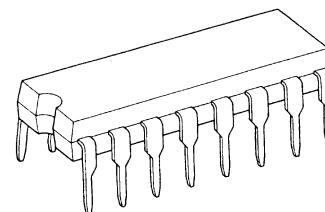


##### TBD62064AFG



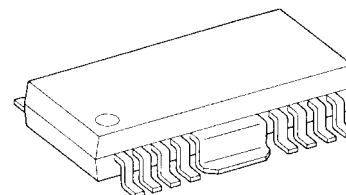
Pin connection may be simplified for explanatory purpose.

##### TBD62064APG



DIP16-P-300-2.54A

##### TBD62064AFG



HSOP16-P-300-1.00

#### Weight

DIP16-P-300-2.54A : 1.11 g (Typ.)

HSOP16-P-300-1.00 : 0.50 g (Typ.)

## Pin explanations

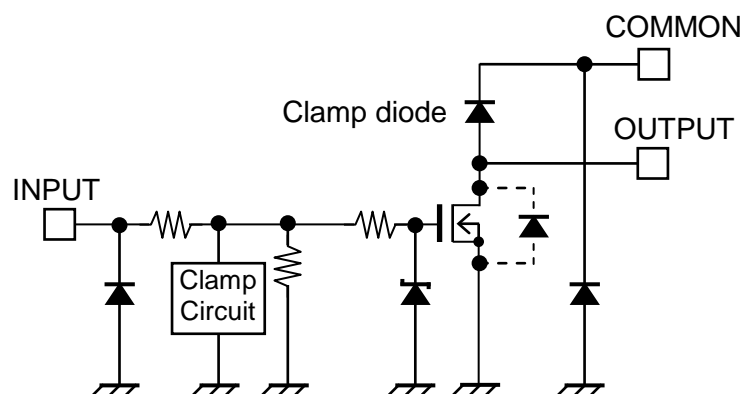
### TBD62064APG

Pin No.	Pin name	Function
1	COMMON	Common pin
2	O1	Output pin 1
3	I1	Input pin 1
4	GND	GND pin
5	GND	GND pin
6	I2	Input pin 2
7	O2	Output pin 2
8	COMMON	Common pin
9	O3	Output pin 3
10	NC	Non-connection pin
11	I3	Input pin 3
12	GND	GND pin
13	GND	GND pin
14	I4	Input pin 4
15	NC	Non-connection pin
16	O4	Output pin 4

### TBD62064AFG

Pin No.	Pin name	Function
1	COMMON	Common pin
2	O1	Output pin 1
3	I1	Input pin 1
4	NC	Non-connection pin
FIN	GND	GND pin
5	NC	Non-connection pin
6	I2	Input pin 2
7	O2	Output pin 2
8	COMMON	Common pin
9	O3	Output pin 3
10	NC	Non-connection pin
11	I3	Input pin 3
12	NC	Non-connection pin
FIN	GND	GND pin
13	NC	Non-connection pin
14	I4	Input pin 4
15	NC	Non-connection pin
16	O4	Output pin 4

## Equivalent circuit



Equivalent circuit may be simplified for explanatory purpose.

## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Output voltage	V <sub>OUT</sub>	50	V
COMMON pin voltage	V <sub>COM</sub>	−0.5 to 50	V
Output current	I <sub>OUT</sub>	1.5	A/ch
Input voltage	V <sub>IN</sub>	−0.5 to 30	V
Clamp diode reverse voltage	V <sub>R</sub>	50	V
Clamp diode forward current	I <sub>F</sub>	1.5	A
Power dissipation	PG	1.47 (Note 1) / 2.7 (Note 2) 0.9 (Note 3) / 1.4 (Note 4)	W
	FG		
Operating temperature	T <sub>opr</sub>	−40 to 85	°C
Storage temperature	T <sub>stg</sub>	−55 to 150	°C

Note1: Device alone. When Ta exceeds 25°C, it is necessary to do the derating with 11.8 mW/°C.

Note2: On PCB (Size: 50 mm × 50 mm × 1.6 mm, Cu area: 50%, single-side glass epoxy).

When Ta exceeds 25°C, it is necessary to do the derating with 21.6 mW/°C.

Note3: Device alone. When Ta exceeds 25°C, it is necessary to do the derating with 7.2 mW/°C.

Note4: On PCB (Size: 60 mm × 30 mm × 1.6 mm, Cu area: 30%, single-side glass epoxy).

When Ta exceeds 25°C, it is necessary to do the derating with 11.2 mW/°C.

## Operating Ranges (Ta = −40 to 85°C)

Characteristics		Symbol	Condition	Min	Typ.	Max	Unit	
Output voltage		V <sub>OUT</sub>	—	—	—	50	V	
COMMON pin voltage		V <sub>COM</sub>	—	0	—	50	V	
Output current	PG (Note 1)	I <sub>OUT</sub>	1 circuit ON, Ta = 25°C		0	—	1250	mA/ch
			t <sub>pw</sub> = 25 ms 4 circuits ON Ta = 85°C T <sub>j</sub> = 120°C	Duty = 10%	0	—	1250	
				Duty = 50%	0	—	610	
	FG (Note 2)		1 circuit ON, Ta = 25°C		0	—	1250	
			t <sub>pw</sub> = 25 ms 4 circuits ON Ta = 85°C T <sub>j</sub> = 120°C	Duty = 10%	0	—	990	
				Duty = 50%	0	—	440	
Input voltage (Output on)		V <sub>IN (ON)</sub>	I <sub>OUT</sub> = 100 mA or upper, V <sub>OUT</sub> = 2 V	2.5	—	25	V	
Input voltage (Output off)		V <sub>IN (OFF)</sub>	I <sub>OUT</sub> = 100 μA or less, V <sub>OUT</sub> = 2 V	0	—	0.6	V	
Clamp diode forward current		I <sub>F</sub>	—	—	—	1.25	A	

Note1: On PCB (Size: 50 mm × 50 mm × 1.6 mm, Cu area: 50%, single-side glass epoxy).

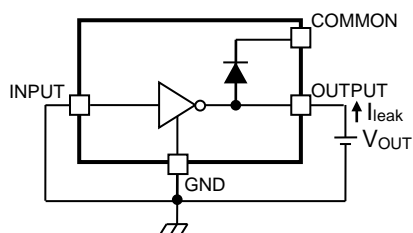
Note2: On PCB (Size: 60 mm × 30 mm × 1.6 mm, Cu area: 30%, single-side glass epoxy).

**Electrical Characteristics (Ta = 25°C unless otherwise noted)**

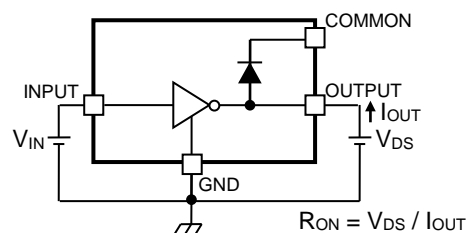
Characteristics	Symbol	Test Circuit	Condition	Min	Typ.	Max	Unit
Output leakage current	I <sub>leak</sub>	1	V <sub>IN</sub> = 0 V, V <sub>OUT</sub> = 50V Ta = 85°C	—	—	1.0	μA
Output voltage (Output ON-resistance)	V <sub>DS</sub> (R <sub>ON</sub> )	2	I <sub>OUT</sub> = 1.25 A, V <sub>IN</sub> = 5.0 V	—	0.56 (0.45)	1.25 (1.0)	V (Ω)
			I <sub>OUT</sub> = 0.75 A, V <sub>IN</sub> = 5.0 V	—	0.32 (0.43)	0.75 (1.0)	
Input current (Output on)	I <sub>IN</sub> (ON)	3	V <sub>IN</sub> = 2.5 V	—	—	0.2	mA
Input current (Output off)	I <sub>IN</sub> (OFF)	4	V <sub>IN</sub> = 0 V, Ta = 85°C	—	—	1.0	μA
Input voltage (Output on)	V <sub>IN</sub> (ON)	5	I <sub>OUT</sub> = 100 mA or upper, V <sub>OUT</sub> = 2 V	—	—	2.5	V
Clamp diode reverse current	I <sub>R</sub>	6	V <sub>R</sub> = 50 V, Ta = 85°C	—	—	1.0	μA
Clamp diode forward voltage	V <sub>F</sub>	7	I <sub>F</sub> = 1.25 A	—	—	2.0	V
Turn-on delay	t <sub>ON</sub>	8	V <sub>OUT</sub> = 50 V R <sub>L</sub> = 42 Ω C <sub>L</sub> = 15 pF	—	1.0	—	μs
Turn-off delay	t <sub>OFF</sub>			—	1.7	—	

## Test circuit

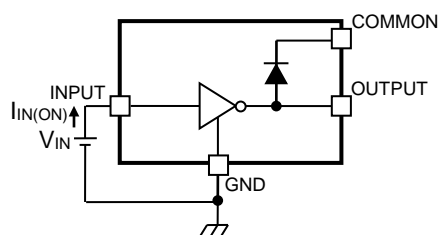
### 1. $I_{leak}$



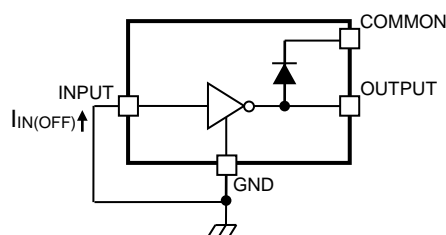
### 2. $V_{DS} (R_{ON})$



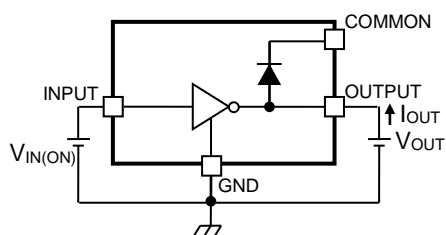
### 3. $I_{IN} (ON)$



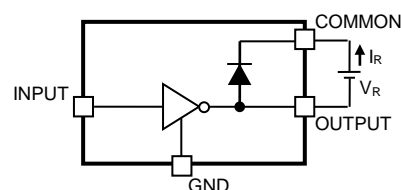
### 4. $I_{IN} (OFF)$



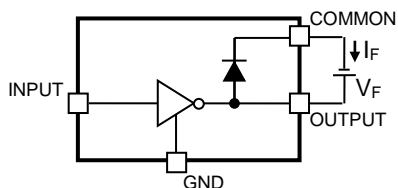
### 5. $V_{IN} (ON)$



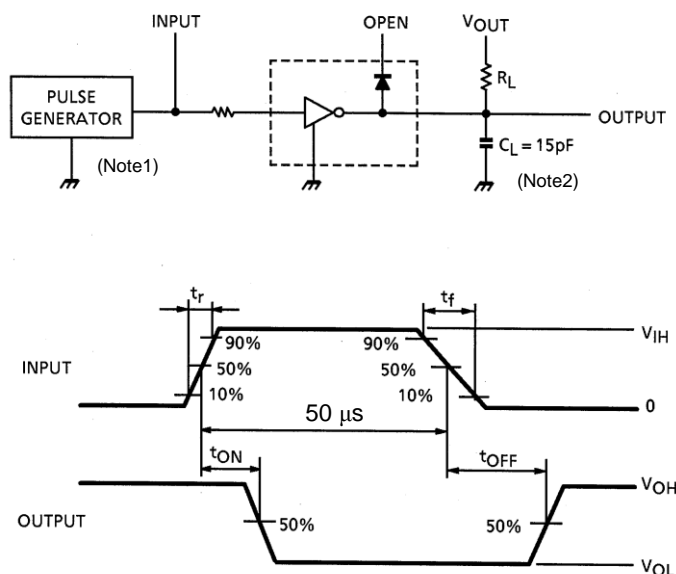
### 6. $I_R$



### 7. $V_F$



Test circuit and timing chart may be simplified for explanatory purpose.

8.  $t_{ON}$ ,  $t_{OFF}$ 

Note 1: Pulse width  $50\text{ }\mu\text{s}$ , Duty cycle 10%

Output impedance  $50\text{ }\Omega$ ,  $t_r \leq 5\text{ ns}$ ,  $t_f \leq 10\text{ ns}$ ,  $V_{IH} = 5.0\text{V}$

Note 2:  $C_L$  includes the probe and the test board capacitance.

Test circuit and timing chart may be simplified for explanatory purpose.

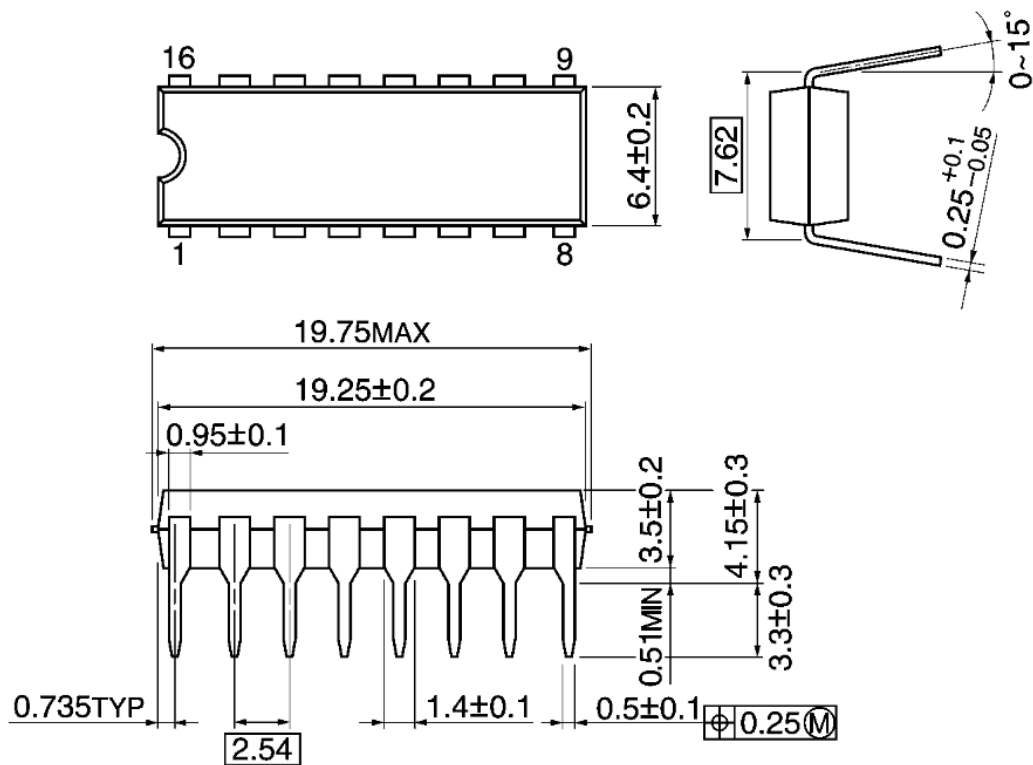
## Precautions for Using

- (1) This IC does not include built-in protection circuits for excess current or overvoltage. Therefore, if the short-circuit between adjacent pins or between outputs, the short-to-power or ground fault has occurred, the current or voltage beyond the absolute maximum rating is impressed, and IC destroys.  
When designing, please consider enough in power supply line, output line and GND line. In addition, so as not to continue to flow a current that exceeds the absolute maximum rating of the IC, please insert the appropriate fuse in the power supply line.
- (2) This IC is being used to drive an inductive load (such as a motor, solenoid or relay), Toshiba recommends that the diodes (pins 1 and 8) be connected to the secondary power supply pin so as to absorb the counter electromotive force generated by the load. Please adhere to the device's absolute maximum ratings.  
Toshiba recommends that zener diodes be connected between the diodes (pins 1 and 8) and the secondary power supply pin (as the anode) so as to enable rapid absorption of the counter electromotive force. Again, please adhere to the device's absolute maximum ratings.

Package Dimensions

DIP16-P-300-2.54A

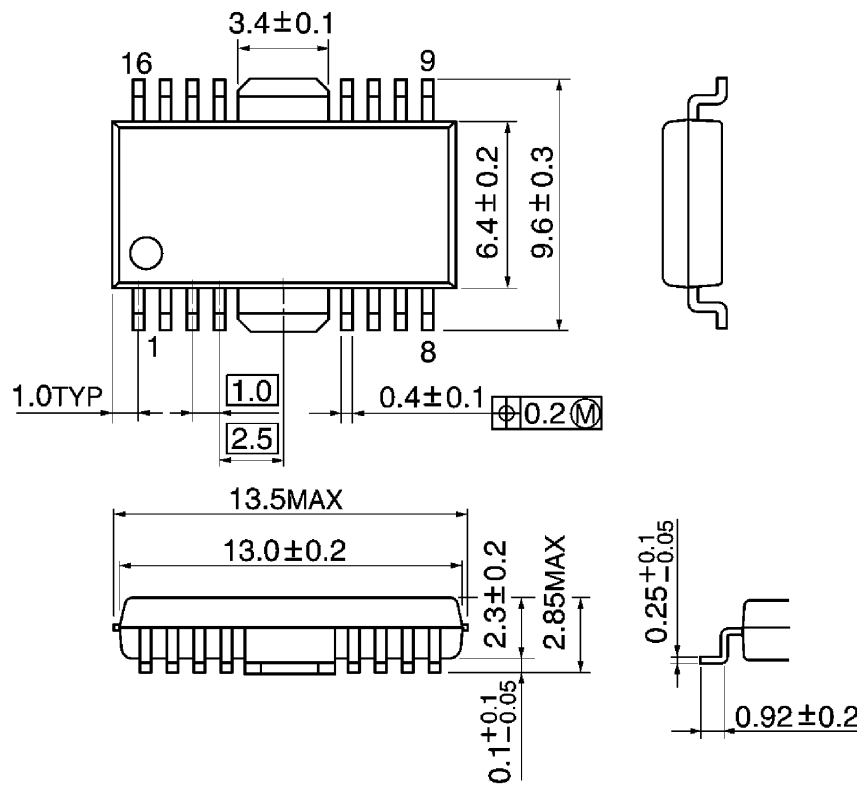
Unit: mm



Weight: 1.11 g (Typ.)

HSOP16-P-300-1.00

Unit: mm



Weight: 0.50 g (Typ.)

## Notes on Contents

### 1. Pin connection

Pin connection may be simplified for explanatory purpose.

### 2. Equivalent Circuits

Equivalent circuit may be simplified for explanatory purpose.

### 3. Timing chart

Timing charts may be simplified for explanatory purposes.

### 4. Test circuit

Test circuit may be simplified for explanatory purpose.

## IC Usage Considerations

### Notes on handling of ICs

- (1) The absolute maximum ratings of a semiconductor device are a set of ratings that must not be exceeded, even for a moment. Do not exceed any of these ratings. Exceeding the rating(s) may cause device breakdown, damage or deterioration, and may result in injury by explosion or combustion.
- (2) Do not insert devices in the wrong orientation or incorrectly. Make sure that the positive and negative terminals of power supplies are connected properly. Otherwise, the current or power consumption may exceed the absolute maximum rating, and exceeding the rating(s) may cause device breakdown, damage or deterioration, and may result in injury by explosion or combustion. In addition, do not use any device inserted in the wrong orientation or incorrectly to which current is applied even just once.
- (3) Use an appropriate power supply fuse to ensure that a large current does not continuously flow in the case of overcurrent and/or IC failure. The IC will fully break down when used under conditions that exceed its absolute maximum ratings, when the wiring is routed improperly or when an abnormal pulse noise occurs from the wiring or load, causing a large current to continuously flow and the breakdown can lead to smoke or ignition. To minimize the effects of the flow of a large current in the case of breakdown, appropriate settings, such as fuse capacity, fusing time and insertion circuit location, are required.
- (4) If your design includes an inductive load such as a motor coil, incorporate a protection circuit into the design to prevent device malfunction or breakdown caused by the current resulting from the inrush current at power ON or the negative current resulting from the back electromotive force at power OFF. IC breakdown may cause injury, smoke or ignition. Use a stable power supply with ICs with built-in protection functions. If the power supply is unstable, the protection function may not operate, causing IC breakdown. IC breakdown may cause injury, smoke or ignition.
- (5) Carefully select external components (such as inputs and negative feedback capacitors) and load components (such as speakers), for example, power amp and regulator. If there is a large amount of leakage current such as from input or negative feedback condenser, the IC output DC voltage will increase. If this output voltage is connected to a speaker with low input withstand voltage, overcurrent or IC failure may cause smoke or ignition. (The overcurrent may cause smoke or ignition from the IC itself.) In particular, please pay attention when using a Bridge Tied Load (BTL) connection-type IC that inputs output DC voltage to a speaker directly.

## Points to remember on handling of ICs

### Heat Radiation Design

When using an IC with large current flow such as power amp, regulator or driver, design the device so that heat is appropriately radiated, in order not to exceed the specified junction temperature (T<sub>J</sub>) at any time or under any condition. These ICs generate heat even during normal use. An inadequate IC heat radiation design can lead to decrease in IC life, deterioration of IC characteristics or IC breakdown. In addition, when designing the device, take into consideration the effect of IC heat radiation with peripheral components.

### Back-EMF

When a motor rotates in the reverse direction, stops or slows abruptly, current flows back to the motor's power supply owing to the effect of back-EMF. If the current sink capability of the power supply is small, the device's motor power supply and output pins might be exposed to conditions beyond the absolute maximum ratings. To avoid this problem, take the effect of back-EMF into consideration in system design.



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Телефон: 8 (812) 309-75-97 (многоканальный)

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Электронная почта: [ocean@oceanchips.ru](mailto:ocean@oceanchips.ru)

Web: <http://oceanchips.ru/>

Адрес: 198099, г. Санкт-Петербург, ул. Калинина, д. 2, корп. 4, лит. А