



RF360 Europe GmbH

A Qualcomm – TDK Joint Venture

SAW Components

SAW Rx filter

Automotive telematics

Series/type:	B4302
Ordering code:	B39212B4302F210
Date:	December 19, 2013
Version:	2.4

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SAW Components

SAW Rx filter

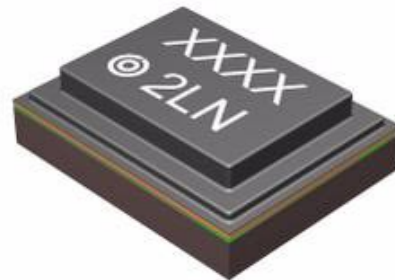
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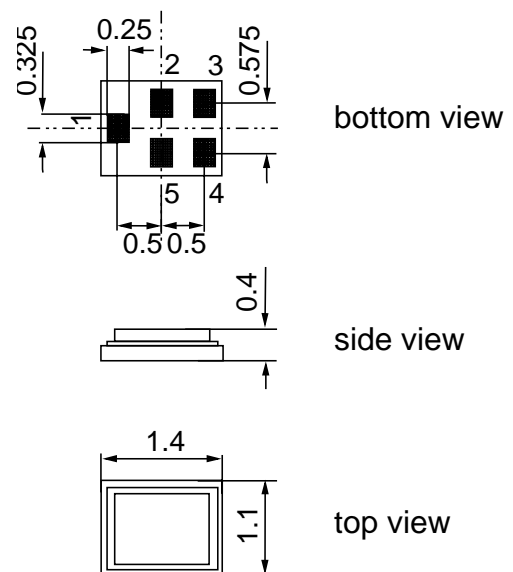
Data sheet


Application

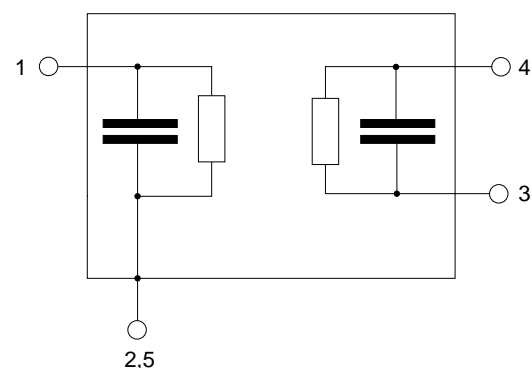
- Low-loss RF filter for mobile telephone WCDMA systems, receive path (RX)
- Impedance transformation from 50 Ω to 150 Ω
- Unbalanced to balanced operation
- Very low insertion attenuation
- Very high Tx-suppression
- Low amplitude ripple
- Very low ripple over any 3.84MHz as well as 5.0MHz within the passband
- Usable passband 60 MHz


Features

- Package size 1.4 x 1.1 x 0.4 mm³
- Package code QCS5M
- RoHS compatible
- Approximate weight 0.003 g
- Package for **Surface Mount Technology (SMT)**
- Ni, gold-plated terminals
- AEC-Q200 qualified component family (operable temperature range -40°C to +85°C)
- **Electrostatic Sensitive Device (ESD)**


Pin configuration

- 1 Input
- 3,4 Output balanced
- 2,5 To be grounded



Data sheet


Characteristics

Temperature range for specification: $T = -10\text{ °C to }+85\text{ °C}$
 Terminating source impedance: $Z_S = 50\ \Omega$
 Terminating load impedance: $Z_L = 150\ \Omega \parallel 18\text{ nH (balanced)}$

				min.	typ. @ 25 °C	max.	
Center frequency			f_C	—	2140.0	—	MHz
Maximum insertion attenuation			α_{\max}	—	1.9	2.5	dB
	2110.0	...	2170.0 MHz				
Amplitude ripple (p-p)			$\Delta\alpha$	—	0.7	1.4	dB
	2110.0	...	2170.0 MHz				
VSWR							
Input	2110.0	...	2170.0 MHz	—	2.0	2.4	
Output	2110.0	...	2170.0 MHz	—	2.0	2.4	
CMRR ($S_{21}-S_{31} / S_{21}+S_{31}$)							
	2110.0	...	2170.0 MHz	17 ¹⁾	22	—	
Attenuation			α				
	10.0	...	1920.0 MHz	35	41	—	dB
	1920.0	...	1980.0 MHz	44	49	—	dB
	1980.0	...	2025.0 MHz	30	34	—	dB
	2025.0	...	2050.0 MHz	18	32	—	dB
	2230.0	...	2300.0 MHz	18	25	—	dB
	2300.0	...	2360.0 MHz	22	28	—	dB
	2360.0	...	4220.0 MHz	28	33	—	dB
	4220.0	...	4340.0 MHz	35	54	—	dB
	4340.0	...	6000.0 MHz	30	46	—	dB

1) A CMRR of 19.6 dB corresponds to a phase imbalance of +/-10° together with an amplitude imbalance of +/- 1.0 dB.

Data sheet


Characteristics

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		min.	typ. @ 25 °C	max.	
Center frequency	f_C	—	2140.0	—	MHz
Maximum insertion attenuation	α_{\max}	—	1.9	3.5	dB
2110.0 ... 2170.0 MHz					
Amplitude ripple (p-p)	$\Delta\alpha$	—	0.7	2.4	dB
2110.0 ... 2170.0 MHz					
VSWR					
Input	2110.0 ... 2170.0 MHz	—	2.0	2.6	
Output	2110.0 ... 2170.0 MHz	—	2.0	2.6	
CMRR ($S_{21}-S_{31} / S_{21}+S_{31}$)					
2110.0 ... 2170.0 MHz		17 ¹⁾	22	—	
Attenuation	α				
10.0 ... 1920.0 MHz		35	41	—	dB
1920.0 ... 1980.0 MHz		44	49	—	
1980.0 ... 2025.0 MHz		30	34	—	
2025.0 ... 2050.0 MHz		18	32	—	
2230.0 ... 2300.0 MHz		18	25	—	dB
2300.0 ... 2360.0 MHz		22	28	—	
2360.0 ... 4220.0 MHz		28	33	—	
4220.0 ... 4340.0 MHz		35	54	—	
4340.0 ... 6000.0 MHz		30	46	—	

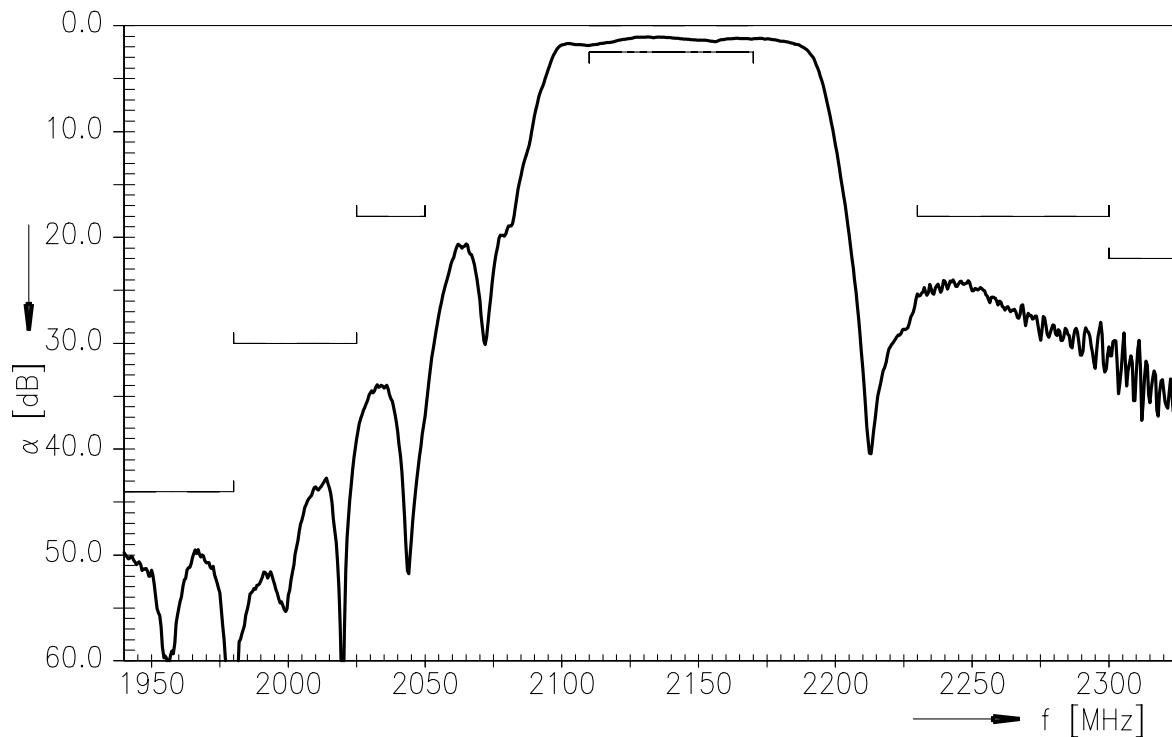
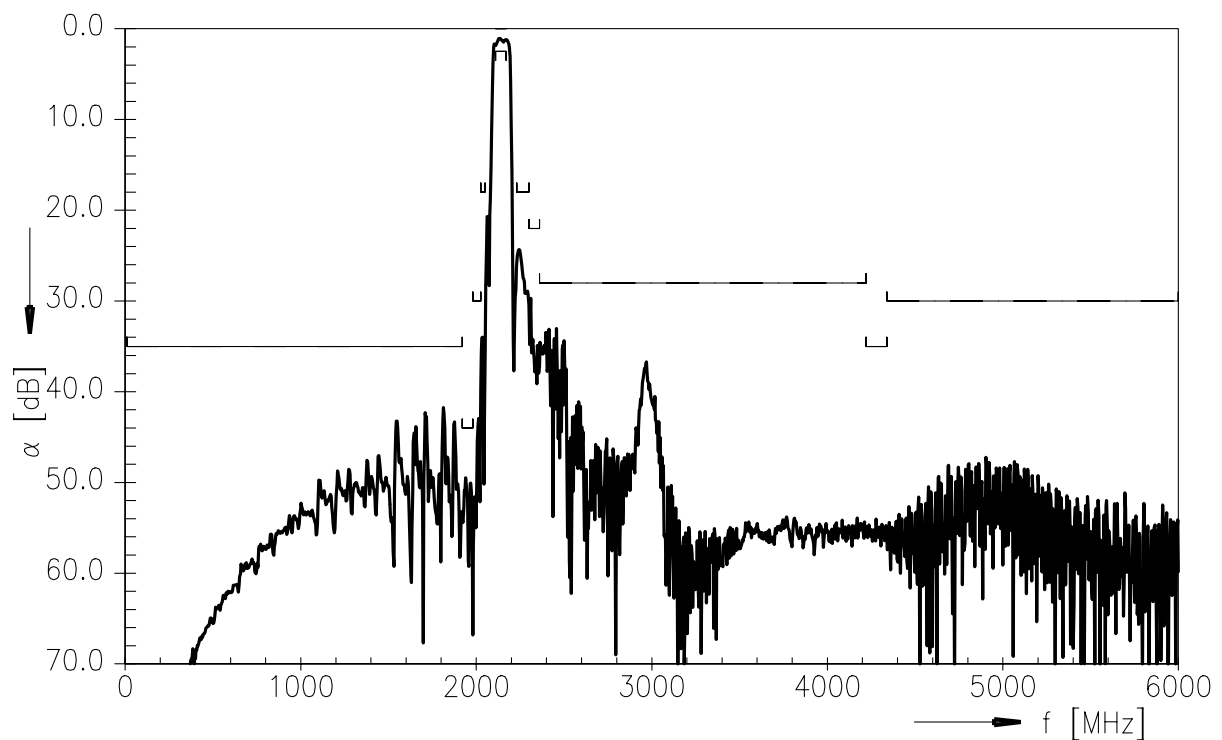
1) A CMRR of 19.6 dB corresponds to a phase imbalance of +/-10° together with an amplitude imbalance of +/- 1.0 dB.



Operable temperature range	T	-40/+85	°C	
Storage temperature range	T _{stg}	-40/+85	°C	
DC voltage	V _{DC}	0	V	
ESD voltage	V _{ESD}	50 ¹⁾	V	machine model, 10 pulses
Source Power	P _s	10	dBm	
Input power at				
GSM850, GSM900	P _{IN}	20	dBm	effective power in the on-state, duty cycle 4:8
GSM1800, GSM1900	P _{IN}	19	dBm	
Tx bands				

1) acc. to JESD22-A115A (machine model), 10 negative & 10 positive pulses.

Data sheet

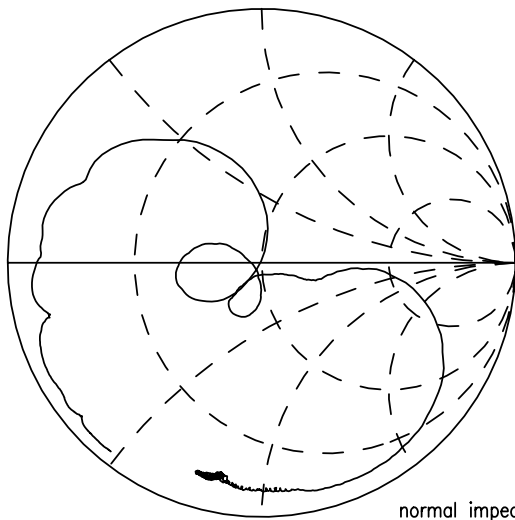

Frequency response (narrowband)

Frequency response (wideband)


Data sheet

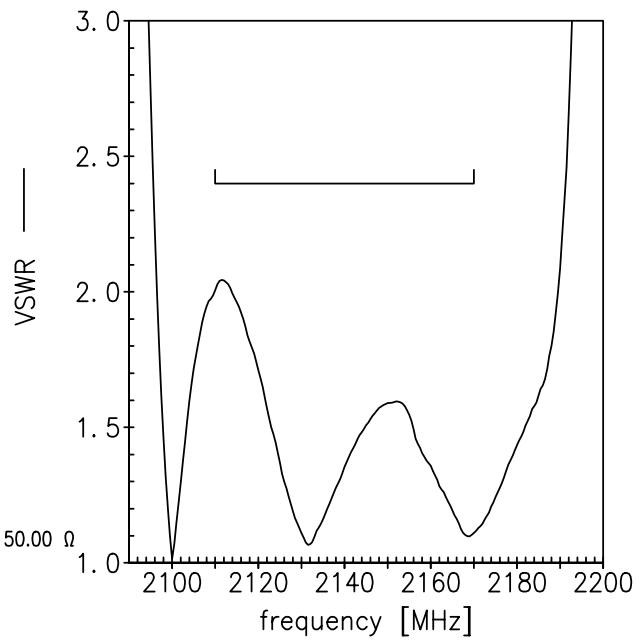


Smith chart

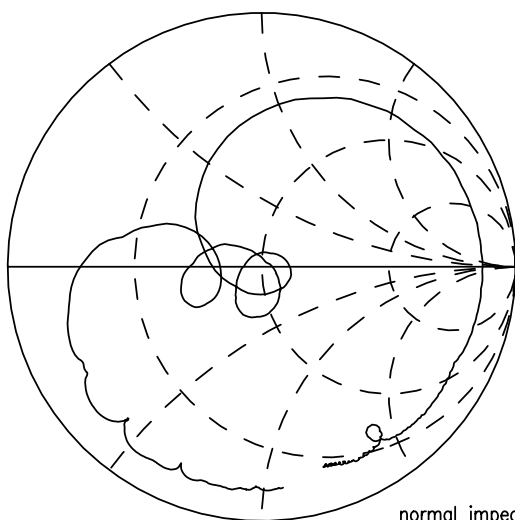
S₁₁ function



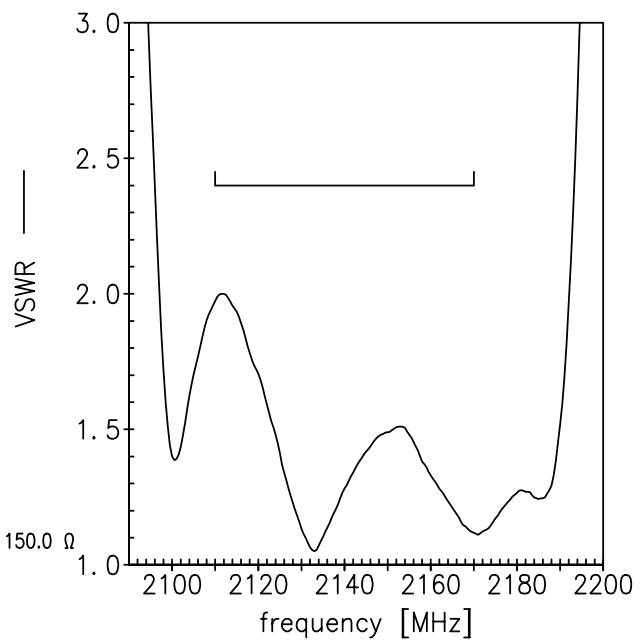
normal impedance: 50.00 Ω



S₂₂ function



normal impedance: 150.0 Ω



Data sheet



ESD protection of SAW filters

SAW filters are **E**lectro **S**tatic **D**ischarge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies have to be applied.

In general, “ESD matching” has to be ensured at that filter port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore only the input matching of the SAW filter has to be designed to short circuit or to block the ESD pulse.

Below three figures show recommended “ESD matching” topologies.

For wideband filters the high-pass ESD matching structure needs to be at least of 3rd order to ensure a proper matching for any impedance value of antenna and SAW filter input. The required component values have to be determined from case to case.

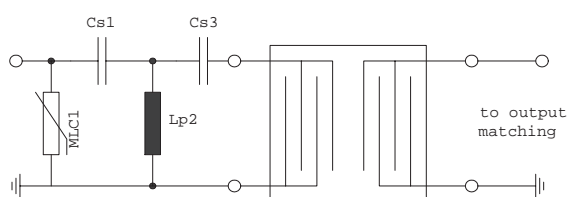


Fig. 1 MLC varistor plus ESD matching

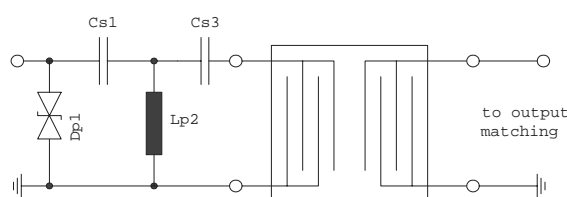


Fig. 2 Suppressor diode plus ESD matching

In cases where minor ESD occur, following simplified “ESD matching” topologies can be used alternatively.

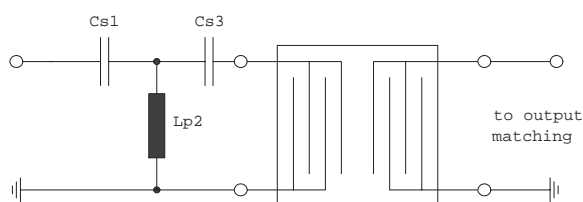


Fig. 3 3rd order high-pass structure for basic ESD protection

In all three figures the shunt inductor Lp2 could be replaced by a shorted microstrip with proper length and width. If this configuration is possible depends on the operating frequency and available pcb space.

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements

For further information, please refer to EPCOS Application report:

“ESD protection for SAW filters”.

This report can be found under www.epcos.com/rke. Click on “Applications Notes”.

Data sheet


References

Type	B4302
Ordering code	B39212B4302F210
Marking and package	C61157-A8-A8
Packaging	F61074-V8212-Z000
Date codes	L_1126
S-parameters	B4302_NB.s3p, B4302_WB.s3p See file header for port/pin assignment table.
Soldering profile	S_6001
RoHS compatible	RoHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8 th , 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.
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Matching coils	See Inductor pdf-catalog http://www.tdk.co.jp/tefe02/coil.htm#aname1 and Data Library for circuit simulation http://www.tdk.co.jp/etvcl/index.htm

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