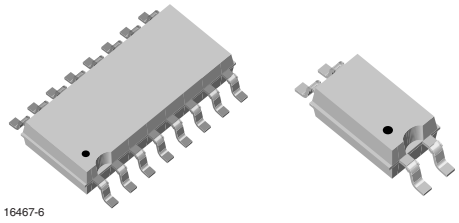
Optocoupler, Phototransistor Output, AC Input, Single/Quad Channel, Half Pitch Mini-Flat Package



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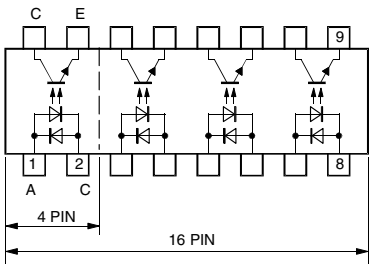


FEATURES

- Low profile package (half pitch)
- AC isolation test voltage 3750 V_{RMS}
- Low coupling capacitance of typical 0.3 pF
- Low temperature coefficient of CTR
- Wide ambient temperature range
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

Pb-free

e3

RoHS COMPLIANT

APPLICATIONS

- Programmable logic controllers

AGENCY APPROVALS

- UL1577, file no. E76222 system code M, double protection
- cUL CSA 22.2 bulletin 5A
- DIN EN 60747-5-2 (VDA 0884)
  DIN EN 60747-5-5 (pending)
- FIMKO: FI EN 60950-1:2006
- BSI: BS EN60065:2002
  BS EN60950-1:2006

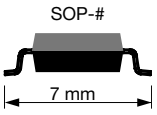
DESCRIPTION

The low profile miniflat package includes an optocoupler with AC Input and transistor output. It is available in single channel (4 pin) TCMT1600 or quad channel (16 pin) TCMT4600.

ORDERING INFORMATION

T	C	M	T	#	6	0	#

PART NUMBER



AGENCY CERTIFIED/PACKAGE	CTR (%)		
	SINGLE CHANNEL	QUAD CHANNEL	
UL, cUL, FIMKO, BSI, VDE	80 to 300	80 to 300	100 to 300
SOP-4	TCMT1600	-	-
SOP-4	TCMT1600T3 (1)	-	-
SOP-16	-	TCMT4600	TCMT4606
SOP-16	-	TCMT4600T0 (1)	-

Notes

- Available only on tape and reel.

(1) Product is rotated 180° in tape and reel cavity.

ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Forward current		I_F	± 60	mA
Forward surge current	$t_p \leq 10$ µs	I_{FSM}	± 1.5	A
Power dissipation		P_{diss}	100	mW
Junction temperature		T_j	125	°C

ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25 °C, unless otherwise specified)				
PARAMETER	**TEST CONDITION**	**SYMBOL**	**VALUE**	**UNIT**
OUTPUT				
Collector emitter voltage		V_{CEO}	70	V
Emitter collector voltage		V_{ECO}	7	V
Collector current		I_C	50	mA
Collector peak current	t_p/T = 0.5, $t_p \leq$ 10 ms	I_{CM}	100	mA
Power dissipation		P_{diss}	150	mW
Junction temperature		T_j	125	°C
COUPLER				
AC isolation test voltage (RMS)		V_{ISO}	3750	V_{RMS}
Total power dissipation		P_{tot}	250	mW
Operating ambient temperature range		T_{amb}	- 40 to + 100	°C
Storage temperature range		T_{stg}	- 40 to + 125	°C
Soldering temperature [1]		T_{sld}	260	°C

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

(2) Wave soldering three cycles are allowed. Also refer to “Assembly Instructions” (www.vishay.com/doc?80054).

ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)						
PARAMETER	**TEST CONDITION**	**SYMBOL**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
INPUT						
Forward voltage	I_F = ± 50 mA	V_F		1.25	1.6	V
Junction capacitance	V_R = 0 V, f = 1 MHz	C_j		50		pF
OUTPUT						
Collector emitter voltage	I_C = 100 µA	V_{CEO}	70			V
Emitter collector voltage	I_E = 100 µA	V_{ECO}	7			V
Collector dark current	V_{CE} = 20 V, I_F = 0	I_{CEO}			100	nA
COUPLER						
Collector emitter saturation voltage	I_F = ± 10 mA, I_C = 1 mA	V_{CEsat}			0.3	V
Cut-off frequency	V_{CE} = 5 V, I_F = ± 10 mA, R_L = 100 Ω	f_c		100		kHz
Capacitance (input to output)	f = 1 MHz	C_{IO}		0.3		pF

Note

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T_{amb} = 25 °C, unless otherwise specified)							
PARAMETER	**TEST CONDITION**	**PART**	**SYMBOL**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
I_C/I_F	V_{CE} = 5 V, I_F = ± 5 mA	TCMT1600	CTR	80		300	%
		TCMT4600	CTR	80		300	%
		TCMT4606	CTR	100		300	%

SWITCHING CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)						
PARAMETER	**TEST CONDITION**	**SYMBOL**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Delay time	V_S = 5 V, I_C = 2 mA, R_L = 100 Ω (see figure 1)	t_d		3		µs
Rise time	V_S = 5 V, I_C = 2 mA, R_L = 100 Ω (see figure 1)	t_r		3		µs
Fall time	V_S = 5 V, I_C = 2 mA, R_L = 100 Ω (see figure 1)	t_f		4.7		µs
Storage time	V_S = 5 V, I_C = 2 mA, R_L = 100 Ω (see figure 1)	t_s		0.3		µs
Turn-on time	V_S = 5 V, I_C = 2 mA, R_L = 100 Ω (see figure 1)	t_{on}		6		µs
Turn-off time	V_S = 5 V, I_C = 2 mA, R_L = 100 Ω (see figure 1)	t_{off}		5		µs
Turn-on time	V_S = 5 V, I_F = ± 10 mA, R_L = 1 kΩ (see figure 2)	t_{on}		9		µs
Turn-off time	V_S = 5 V, I_F = ± 10 mA, R_L = 1 kΩ (see figure 2)	t_{off}		18		µs

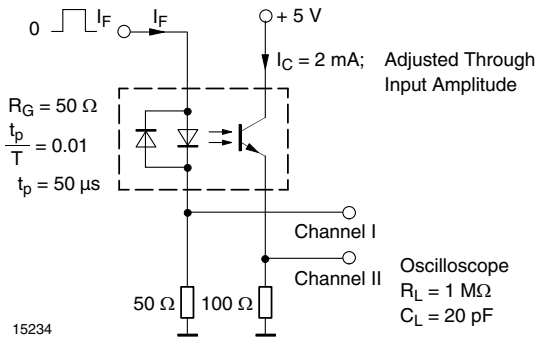


Fig. 1 - Test Circuit, Non-Saturated Operation

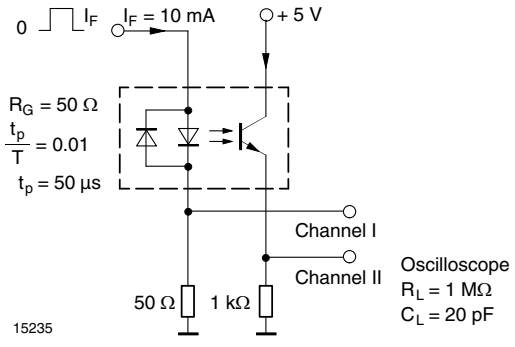


Fig. 2 - Test Circuit, Saturated Operation

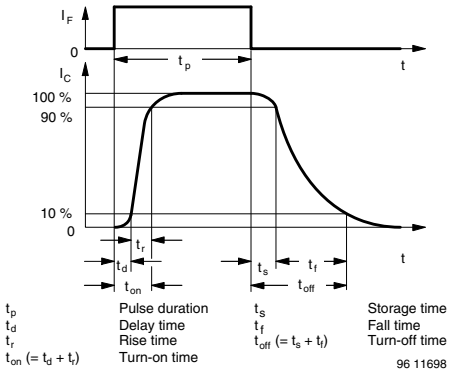


Fig. 3 - Switching Times

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

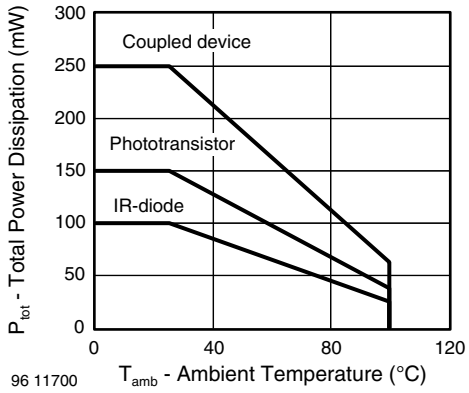


Fig. 4 - Total Power Dissipation vs. Ambient Temperature

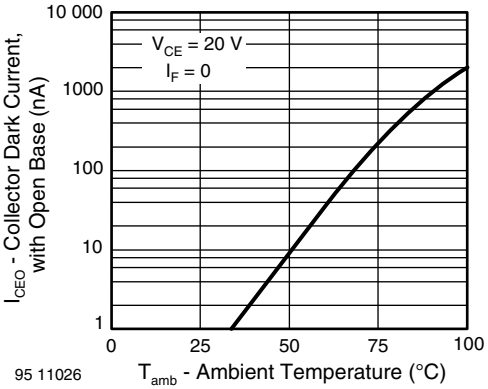


Fig. 7 - Collector Dark Current vs. Ambient Temperature

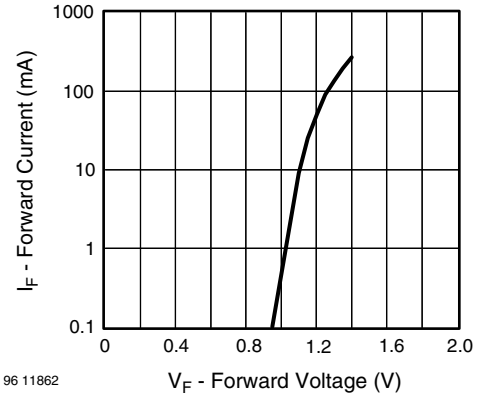


Fig. 5 - Forward Current vs. Forward Voltage

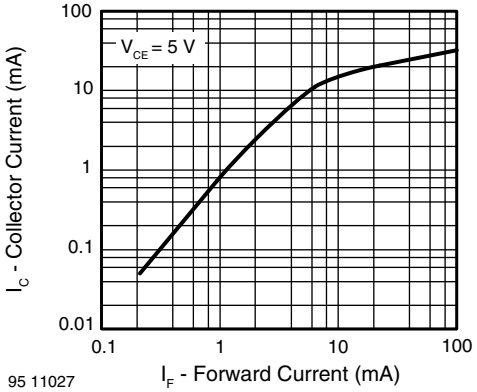


Fig. 8 - Collector Current vs. Forward Current

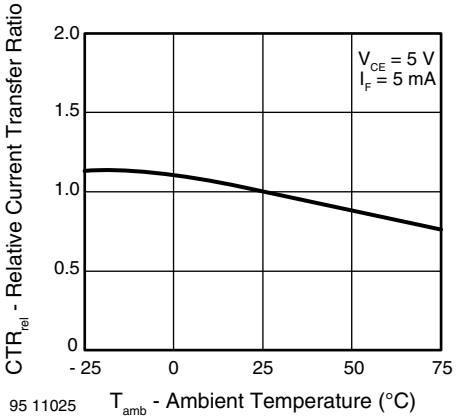


Fig. 6 - Relative Current Transfer Ratio vs. Ambient Temperature

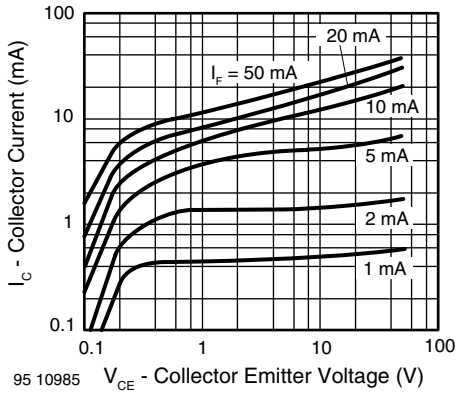


Fig. 9 - Collector Current vs. Collector Emitter Voltage

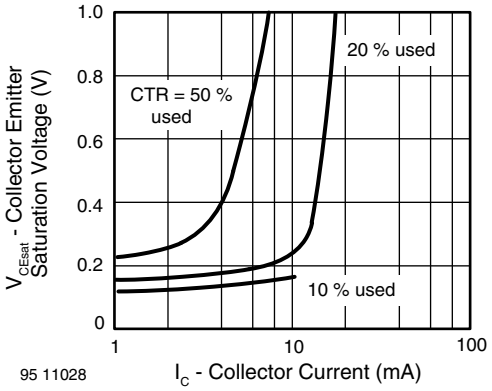


Fig. 10 - Collector Emitter Saturation Voltage vs. Collector Current

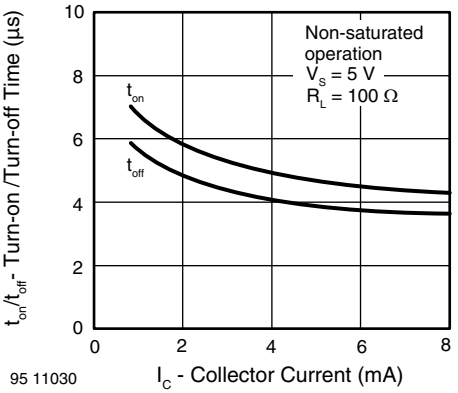


Fig. 13 - Turn-on/Turn-off Time vs. Collector Current

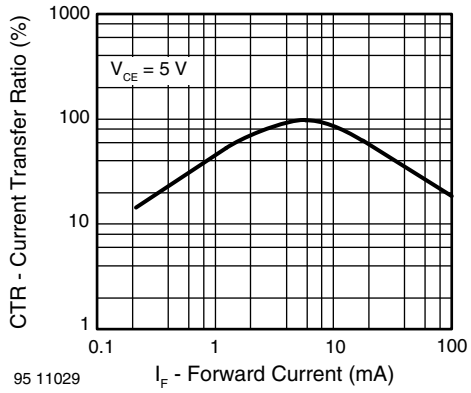


Fig. 11 - Current Transfer Ratio vs. Forward Current

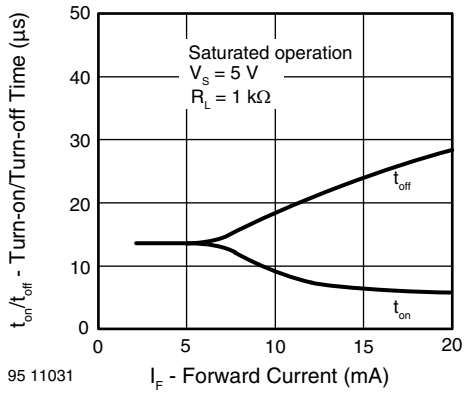
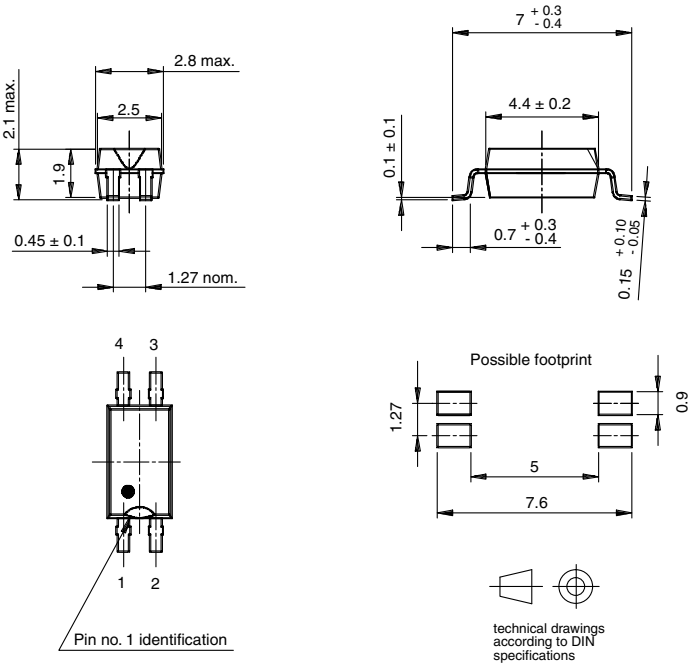


Fig. 12 - Turn-on/Turn-off Time vs. Forward Current

PACKAGE DIMENSIONS in millimeters



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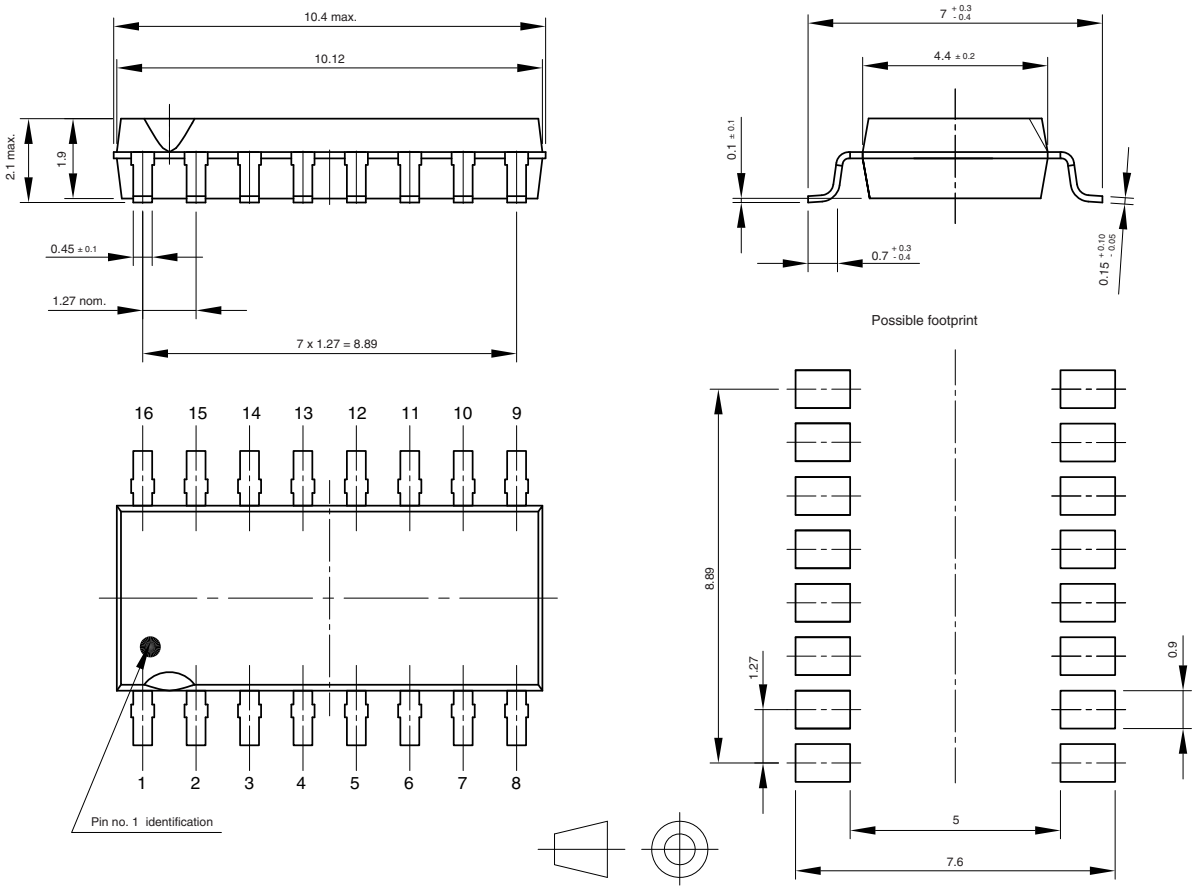

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Issue: 1; 04.04.00
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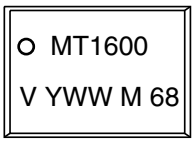
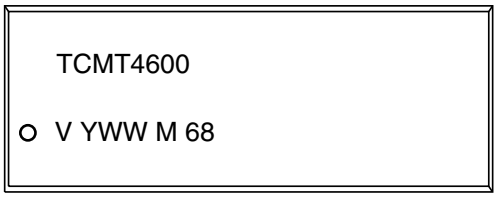
PACKAGE MARKING





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