

## V.92 ISOMODEM<sup>®</sup> WITH GLOBAL DAA

### Features

- This data sheet applies to Si2493 Revision D
- Data modem formats
  - ITU-T, Bell
  - 300 bps up to 56 kbps
  - V.21, V.22, V.29 Fast Connect
  - V.44, V.42, V.42bis, MNP2-5
  - Automatic rate negotiation
  - V.92 PCM upstream
  - V.92 Quick connect
  - V.92 Modem on hold
- Type I and II caller ID decode
- No external ROM or RAM required
- UART, SPI, or parallel interface
- Flexible clock options
  - Low-cost 32.768 kHz oscillator
  - 4.915 MHz oscillator
  - 27 MHz clock input
- Integrated DAA
  - Over 6000 V capacitive isolation
  - Parallel phone detect
  - Globally-compliant line interface
  - Overcurrent detection
- AT command set support
- SMS / MMS support
- Firmware upgradeable
- EEPROM interface
- Lead-free, RoHS-compliant packages
- Commercial or industrial temperature range
- DTMF detection/generation

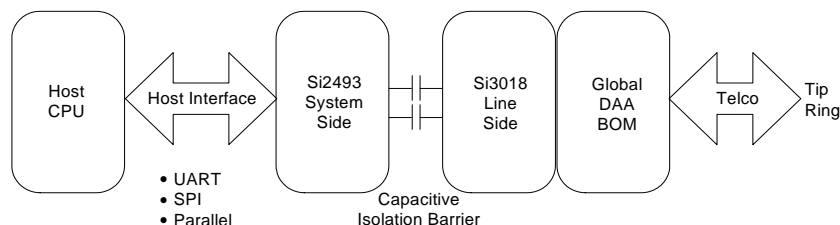
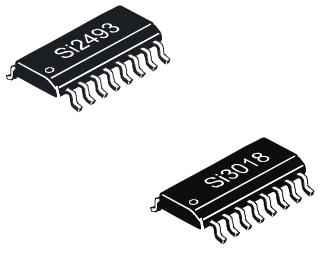
### Applications

- Set-top boxes
- Point-of-sale terminals
- Text / video telephones
- Digital video recorder
- Digital televisions
- Remote monitoring

### Description

The Si2493 ISModem is a complete, ITU V.92-compliant, full-featured modem that provides conventional data formats with connect rates of up to 56,000 bps, full-duplex, over the Public Switched Telephone Network (PSTN). Offered as a chipset with the Si2493 system-side device and the Si3018 line-side device, the ISModem utilizes Silicon Laboratories' patented Direct Access Arrangement (DAA) technology to provide a programmable telephone line interface with an unparalleled level of integration. This compact solution eliminates the need for a separate DSP, modem controller, codec, transformer, relay, opto-isolators, clocking crystal, and 2–4 wire hybrid. Available with a system-side packaging option of either a 16-pin SOIC or a 24-pin TSSOP, the Si2493 is ideal for embedded modem applications due to its flexibility, small footprint, and minimal external component count.

### System Block Diagram

### Ordering Information

This data sheet is valid only for those chipset combinations listed on page 51.

### Pin Assignments

| Si2493 (16-Pin Option) |    |
|------------------------|----|
| CLKIN/XTALI            | 1  |
| XTALO                  | 2  |
| RI                     | 3  |
| V <sub>D</sub>         | 4  |
| MISO/RXD               | 5  |
| MOSI/TXD               | 6  |
| SCLK/CTS               | 7  |
| RESET                  | 8  |
| SS/RTS                 | 16 |
| DCD                    | 15 |
| ESC                    | 14 |
| V <sub>A</sub>         | 13 |
| GND                    | 12 |
| INT                    | 11 |
| C1A                    | 10 |
| C2A                    | 9  |

| Si2493 (24-Pin Option) |    |
|------------------------|----|
| CLKIN/XTALI            | 1  |
| XTALO                  | 2  |
| CLKOUT/EECS/A0         | 3  |
| FSYNC/D6               | 4  |
| VD3.3                  | 5  |
| GND                    | 6  |
| VDA                    | 7  |
| SS/RTS/D7              | 8  |
| MISO/RXD/RD            | 9  |
| MOSI/TXD/WR            | 10 |
| SCLK/CTS/CS            | 11 |
| RESET                  | 12 |
| SDO/EECLK/D5           | 24 |
| DCD/D4                 | 23 |
| ESC/D3                 | 22 |
| VD3.3                  | 21 |
| GND                    | 20 |
| VDB                    | 19 |
| SDI/EESD/D2            | 18 |
| RI/D1                  | 17 |
| INT/DO                 | 16 |
| AOUT/INT               | 15 |
| C1A                    | 14 |
| C2A                    | 13 |

| Si3018 |    |
|--------|----|
| QE     | 1  |
| DCT    | 2  |
| RX     | 3  |
| IB     | 4  |
| C1B    | 5  |
| C2B    | 6  |
| VREG   | 7  |
| RNG1   | 8  |
| DCT2   | 16 |
| IGND   | 15 |
| DCT3   | 14 |
| QB     | 13 |
| QE2    | 12 |
| SC     | 11 |
| VREG2  | 10 |
| RNG2   | 9  |



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## 1. Electrical Specifications

**Table 1. Recommended Operating Conditions**

| Parameter <sup>1</sup>                      | Symbol | Test Condition     | Min <sup>2</sup> | Typ      | Max <sup>2</sup> | Unit |
|---|--------|--------------------|------------------|----------|------------------|------|
| Ambient Temperature                         | $T_A$  | F-grade<br>G-grade | 0<br>-40         | 25<br>25 | 70<br>85         | °C   |
| Si2493 Supply Voltage, Digital <sup>3</sup> | $V_D$  |                    | 3.0              | 3.3      | 3.6              | V    |

**Notes:**

- The Si2493 specifications are guaranteed when the typical application circuit (including component tolerance) and any Si2493 and any Si3018 are used. See "2. Typical Application Schematic" on page 11.
- All minimum and maximum specifications are guaranteed and apply across the recommended operating conditions. Typical values apply at nominal supply voltages and an operating temperature of 25 °C unless otherwise stated.
- The digital supply,  $V_D$ , operates from 3.0 to 3.6 V.

**Table 2. Loop Characteristics**

( $V_D = 3.0$  to  $3.6$  V,  $T_A = 0$  to  $70$  °C for F-grade,  $T_A = -40$  to  $85$  °C for G-grade)

| Parameter                        | Symbol   | Test Condition  | Min | Typ | Max | Unit      |
|----------------------------------|----------|---|-----|-----|-----|-----------|
| DC Termination Voltage           | $V_{TR}$ | $I_L = 20$ mA, $ILIM^1 = 0$<br>DCV = 00, MINI = 11, DCR = 0 | —   | —   | 6.0 | V         |
| DC Termination Voltage           | $V_{TR}$ | $I_L = 120$ mA, $ILIM = 0$<br>DCV = 00, MINI = 11, DCR = 0  | 9   | —   | —   | V         |
| DC Termination Voltage           | $V_{TR}$ | $I_L = 20$ mA, $ILIM = 0$<br>DCV = 11, MINI = 00, DCR = 0   | —   | —   | 7.5 | V         |
| DC Termination Voltage           | $V_{TR}$ | $I_L = 120$ mA, $ILIM = 0$<br>DCV = 11, MINI = 00, DCR = 0  | 9   | —   | —   | V         |
| DC Termination Voltage           | $V_{TR}$ | $I_L = 20$ mA, $ILIM = 1$<br>DCV = 11, MINI = 00, DCR = 0   | —   | —   | 7.5 | V         |
| DC Termination Voltage           | $V_{TR}$ | $I_L = 60$ mA, $ILIM = 1$<br>DCV = 11, MINI = 00, DCR = 0   | 40  | —   | —   | V         |
| DC Termination Voltage           | $V_{TR}$ | $I_L = 50$ mA, $ILIM = 1$<br>DCV = 11, MINI = 00, DCR = 0   | —   | —   | 40  | V         |
| On-Hook Leakage Current          | $I_{LK}$ | $V_{TR} = -48$ V  | —   | —   | 5   | μA        |
| Operating Loop Current           | $I_{LP}$ | MINI = 00, $ILIM = 0$                                       | 10  | —   | 120 | mA        |
| Operating Loop Current           | $I_{LP}$ | MINI = 00, $ILIM = 1$                                       | 10  | —   | 60  | mA        |
| DC Ring Current                  |          | DC current flowing through ring detection circuitry         | —   | 1.5 | 3   | μA        |
| Ring Detect Voltage <sup>2</sup> | $V_{RD}$ | RT = 0  | 12  | 15  | 18  | $V_{RMS}$ |
| Ring Detect Voltage <sup>2</sup> | $V_{RD}$ | RT = 1  | 18  | 21  | 25  | $V_{RMS}$ |
| Ring Frequency                   | $F_R$    |   | 15  | —   | 68  | Hz        |
| Ringer Equivalence Number        | REN      |   | —   | —   | 0.2 |           |

**Notes:**

- $ILIM = U67$ , bit 9; DCV = U67, bits 3:2; MINI = U67, bits 13:12; DCR = U67, bit 7; RT = U67, bit 0.
- The ring signal is guaranteed to not be detected below the minimum. The ring signal is guaranteed to be detected above the maximum.



Figure 1. Test Circuit for Loop Characteristics

Table 3. DC Characteristics,  $V_D = 3.0$  to  $3.6$  V

( $V_D = 3.0$  to  $3.6$  V,  $T_A = 0$  to  $70$  °C for F-grade,  $T_A = -40$  to  $85$  °C for G-grade)

| Parameter                           | Symbol   | Test Condition | Min | Typ | Max  | Unit       |
|-------------------------------------|----------|----------------|-----|-----|------|------------|
| High Level Input Voltage            | $V_{IH}$ |                | 2.0 | —   | —    | V          |
| Low Level Input Voltage             | $V_{IL}$ |                | —   | —   | 0.8  | V          |
| High Level Output Voltage           | $V_{OH}$ | $I_O = -2$ mA  | 2.4 | —   | —    | V          |
| Low Level Output Voltage            | $V_{OL}$ | $I_O = 2$ mA   | —   | —   | 0.35 | V          |
| Input Leakage Current               | $I_L$    |                | -10 | —   | 10   | $\mu$ A    |
| Pullup Resistance Pins              | $R_{PU}$ |                | 50  | 125 | 200  | k $\Omega$ |
| Total Supply Current*               | $I_D$    |                | —   | 17  | 35   | mA         |
| Total Supply Current, Wake-On-Ring* | $I_D$    |                | —   | 4.4 | —    | mA         |
| Total Supply Current, Powerdown*    | $I_D$    | PDN = 1        | —   | 80  | —    | $\mu$ A    |

\*Note: All inputs at 0 or  $V_D$ . All inputs held static except clock and all outputs unloaded (Static  $I_{OUT} = 0$  mA).

**Table 4. AC Characteristics**

( $V_D = 3.0$  to  $3.6$  V,  $T_A = 0$  to  $70$  °C for F-grade,  $F_s = 8$  kHz,  $T_A = -40$  to  $85$  °C for G-grade)

| Parameter                                       | Symbol     | Test Condition   | Min | Typ    | Max | Unit       |
|---|------------|--|-----|--------|-----|------------|
| Sample Rate                                     | $F_s$      |  | —   | 8      | —   | kHz        |
| Clock Input Frequency                           | $F_{XTL}$  | default  | —   | 4.9152 | —   | MHz        |
| Clock Input Frequency                           | $F_{XTL}$  | 27 MHz Mode <sup>1</sup>                                 | —   | 27     | —   | MHz        |
| Clock Input Frequency                           | $F_{XTL}$  | 32 kHz Mode <sup>1</sup>                                 | —   | 32.768 | —   | kHz        |
| Receive Frequency Response                      |            | Low -3 dBFS Corner, FILT = 0                             | —   | 5      | —   | Hz         |
| Receive Frequency Response                      |            | Low -3 dBFS Corner, FILT = 1                             | —   | 200    | —   | Hz         |
| Transmit Full Scale Level <sup>2</sup>          | $V_{FS}$   |  | —   | 1.1    | —   | $V_{PEAK}$ |
| Receive Full Scale Level <sup>2,3</sup>         | $V_{FS}$   |  | —   | 1.1    | —   | $V_{PEAK}$ |
| Dynamic Range <sup>4</sup>                      | DR         | ILIM = 0, DCV = 11, MINI = 00<br>DCR = 0, $I_L = 100$ mA | —   | 80     | —   | dB         |
| Dynamic Range <sup>4</sup>                      | DR         | ILIM = 0, DCV = 00, MINI = 11<br>DCR = 0, $I_L = 20$ mA  | —   | 80     | —   | dB         |
| Dynamic Range <sup>4</sup>                      | DR         | ILIM = 1, DCV = 11, MINI = 00<br>DCR = 0, $I_L = 50$ mA  | —   | 80     | —   | dB         |
| Transmit Total Harmonic Distortion <sup>5</sup> | THD        | ILIM = 0, DCV = 11, MINI = 00<br>DCR = 0, $I_L = 100$ mA | —   | -72    | —   | dB         |
| Transmit Total Harmonic Distortion <sup>5</sup> | THD        | ILIM = 0, DCV = 00, MINI = 11<br>DCR = 0, $I_L = 20$ mA  | —   | -78    | —   | dB         |
| Receive Total Harmonic Distortion <sup>5</sup>  | THD        | ILIM = 0, DCV = 00, MINI = 11<br>DCR = 0, $I_L = 20$ mA  | —   | -78    | —   | dB         |
| Receive Total Harmonic Distortion <sup>5</sup>  | THD        | ILIM = 1, DCV = 11, MINI = 00<br>DCR = 0, $I_L = 50$ mA  | —   | -78    | —   | dB         |
| Dynamic Range (Caller ID Mode)                  | $DR_{CID}$ | $V_{IN} = 1$ kHz, -13 dBm                                | —   | 50     | —   | dB         |

**Notes:**

1. Refer to “AN93: ISOModem® Chipset Family Designer's Guide” for configuring clock input reset strapping.
2. Measured at TIP and RING with 600 Ω termination at 1 kHz, as shown in Figure 1 on page 5.
3. Receive full scale level produces -0.9 dBFS at DTX.
4.  $DR = 20 \times \log |V_{in}| + 20 \times \log (\text{rms signal/rms noise})$ . Applies to both transmit and receive paths.  $V_{in} = 1$  kHz, -3 dBFS.
5.  $V_{in} = 1$  kHz, -3 dBFS.  $THD = 20 \times \log (\text{rms distortion/rms signal})$ .

Table 5. Absolute Maximum Ratings

| Parameter                                | Symbol     | Value                 | Unit |
|--|------------|-----------------------|------|
| DC Supply Voltage                        | $V_D$      | 4.1                   | V    |
| Input Current, Si2493 Digital Input Pins | $I_{IN}$   | $\pm 10$              | mA   |
| Digital Input Voltage                    | $V_{IND}$  | -0.3 to $(V_D + 0.3)$ | V    |
| CLKIN/XTALI Input Voltage                | $V_{XIND}$ | -0.3 to $(V_D + 0.3)$ | V    |
| Operating Temperature Range              | $T_A$      | -10 to 100            | °C   |
| Storage Temperature Range                | $T_{STG}$  | -40 to 150            | °C   |

**Note:** Permanent device damage may occur if the above absolute maximum ratings are exceeded. Functional operation should be restricted to the conditions as specified in the operational sections of this data sheet. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Table 6. Switching Characteristics<sup>1</sup>

( $V_D = 3.0$  to  $3.6$  V,  $T_A = 0$  to  $70$  °C for F-grade,  $T_A = -40$  to  $85$  °C for G-grade)

| Parameter  | Symbol     | Min     | Typ | Max    | Unit |
|--|------------|---------|-----|--------|------|
| <b>UART Timing Parameters</b>                                |            |         |     |        |      |
| CLKOUT Output Clock Frequency                                |            | 2.048   | —   | 49.152 | MHz  |
| Baud Rate Accuracy   | $t_{BD}$   | -1      | —   | 1      | %    |
| <b>Reset Timing Parameters</b>                               |            |         |     |        |      |
| $\overline{RESET} \downarrow$ to $\overline{RESET} \uparrow$ | $t_{RS}$   | $5.0^2$ | —   | —      | ms   |
| $\overline{RESET} \uparrow$ to 1st AT Command                | $t_{AT}$   | 300     | —   | —      | ms   |
| <b>Parallel Timing Parameters</b>                            |            |         |     |        |      |
| Address Setup  | $t_{AS}$   | 15      | —   | —      | ns   |
| Address Hold   | $t_{AH}$   | 0       | —   | —      | ns   |
| $\overline{WR}$ Low Pulse Width                              | $t_{WL}$   | 50      | —   | —      | ns   |
| Write Data Setup Time  | $t_{WDSU}$ | 20      | —   | —      | ns   |
| Write Cycle Time   | $t_{WC}$   | 120     | —   | —      | ns   |
| Chip Select Setup  | $t_{CSS}$  | 10      | —   | —      | ns   |
| Chip Select Hold   | $t_{CSH}$  | 0       | —   | —      | ns   |
| $\overline{RD}$ Low Pulse Width                              | $t_{RL}$   | 50      | —   | —      | ns   |
| $\overline{RD}$ Low to Data Driven Time                      | $t_{RLDD}$ | —       | —   | 20     | ns   |
| Data Hold  | $t_{DH}$   | 10      | —   | —      | ns   |
| $\overline{RD}$ High to Hi-Z Time                            | $t_{DZ}$   | —       | —   | 30     | ns   |

**Notes:**

- All timing is referenced to the 50% level of the waveform. Input test levels are  $V_{IH} = V_D - 0.4$  V,  $V_{IL} = 0.4$  V.
- With 32.768 kHz clocking, allow 500 to the reset low-to-high minimum pulse on power-up and wake-from-power-down conditions.

**Table 6. Switching Characteristics<sup>1</sup> (Continued)**

( $V_D = 3.0$  to  $3.6$  V,  $T_A = 0$  to  $70$  °C for F-grade,  $T_A = -40$  to  $85$  °C for G-grade)

| Parameter  | Symbol           | Min | Typ | Max | Unit |
|--|------------------|-----|-----|-----|------|
| Read Cycle Time  | $t_{RC}$         | 120 | —   | —   | ns   |
| Write to Read Cycle Time   | $t_{WRC}$        | 120 | —   | —   | ns   |
| <b>Serial Peripheral Interface (SPI) Timing Parameters</b>   |                  |     |     |     |      |
| SS Falling to First SCLK Edge  | $t_{SE}$         | 41  | —   | —   | ns   |
| Last SCLK Edge to SS Rising  | $t_{SD}$         | 41  | —   | —   | ns   |
| SS Rising to MISO High-Z   | $t_{SDZ}$        | —   | —   | 93  | ns   |
| SCLK High Time   | $t_{CKH}$        | 102 | —   | —   | ns   |
| SCLK Low Time  | $t_{CKL}$        | 102 | —   | —   | ns   |
| MOSI Valid to SCLK Sample Edge   | $t_{SIS}$        | 41  | —   | —   | ns   |
| SCLK Sample Edge to MOSI Change  | $t_{SIH}$        | 41  | —   | —   | ns   |
| SCLK Shift Edge to MISO Change   | $t_{SOH}$        | —   | —   | 93  | ns   |
| SCLK cycle time  | $t_{SCK}$        | 224 | —   | —   | ns   |
| Inactive time between SS actives   | $t_{NSS\_INACT}$ | 81  | —   | —   | ns   |
| <b>Notes:</b>  |                  |     |     |     |      |
| 1. All timing is referenced to the 50% level of the waveform. Input test levels are $V_{IH} = V_D - 0.4$ V, $V_{IL} = 0.4$ V.  |                  |     |     |     |      |
| 2. With 32.768 kHz clocking, allow 500 to the reset low-to-high minimum pulse on power-up and wake-from-power-down conditions. |                  |     |     |     |      |



**Figure 2. SPI Slave Timing**

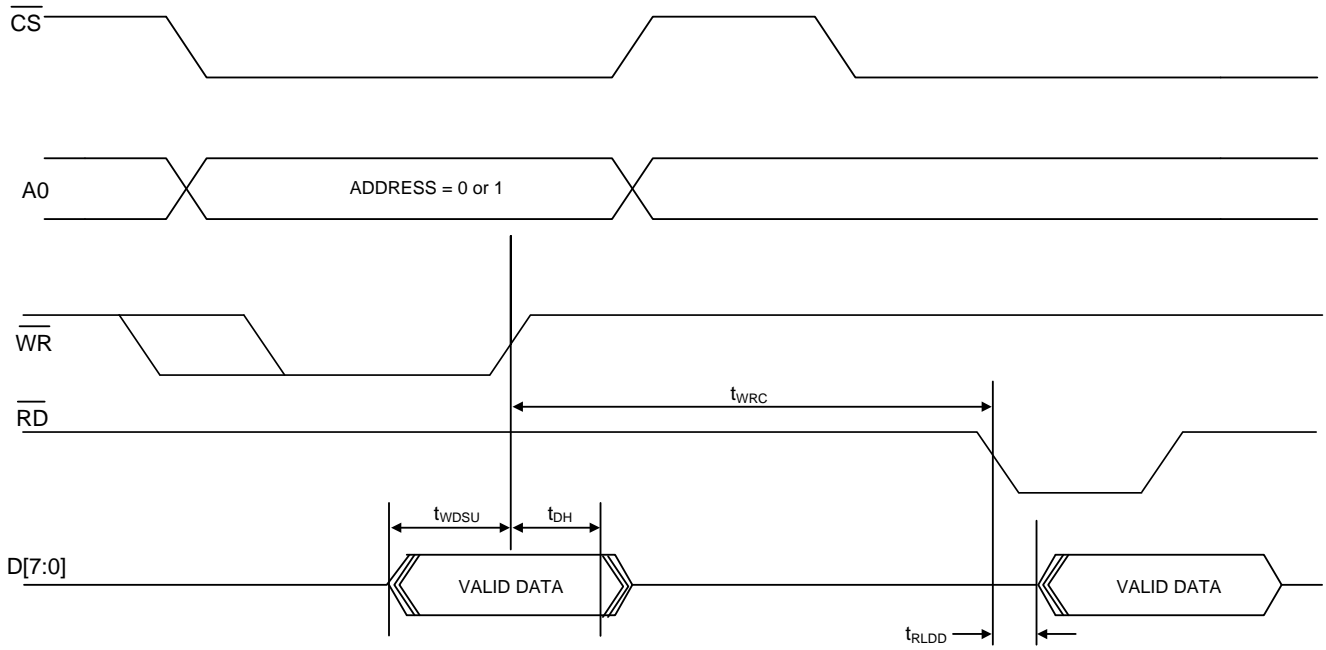




Figure 3. Parallel Interface Read Timing



Figure 4. Parallel Interface Write Timing



**Figure 5. Parallel Interface Write Followed by Read Timing**

## 2. Typical Application Schematic

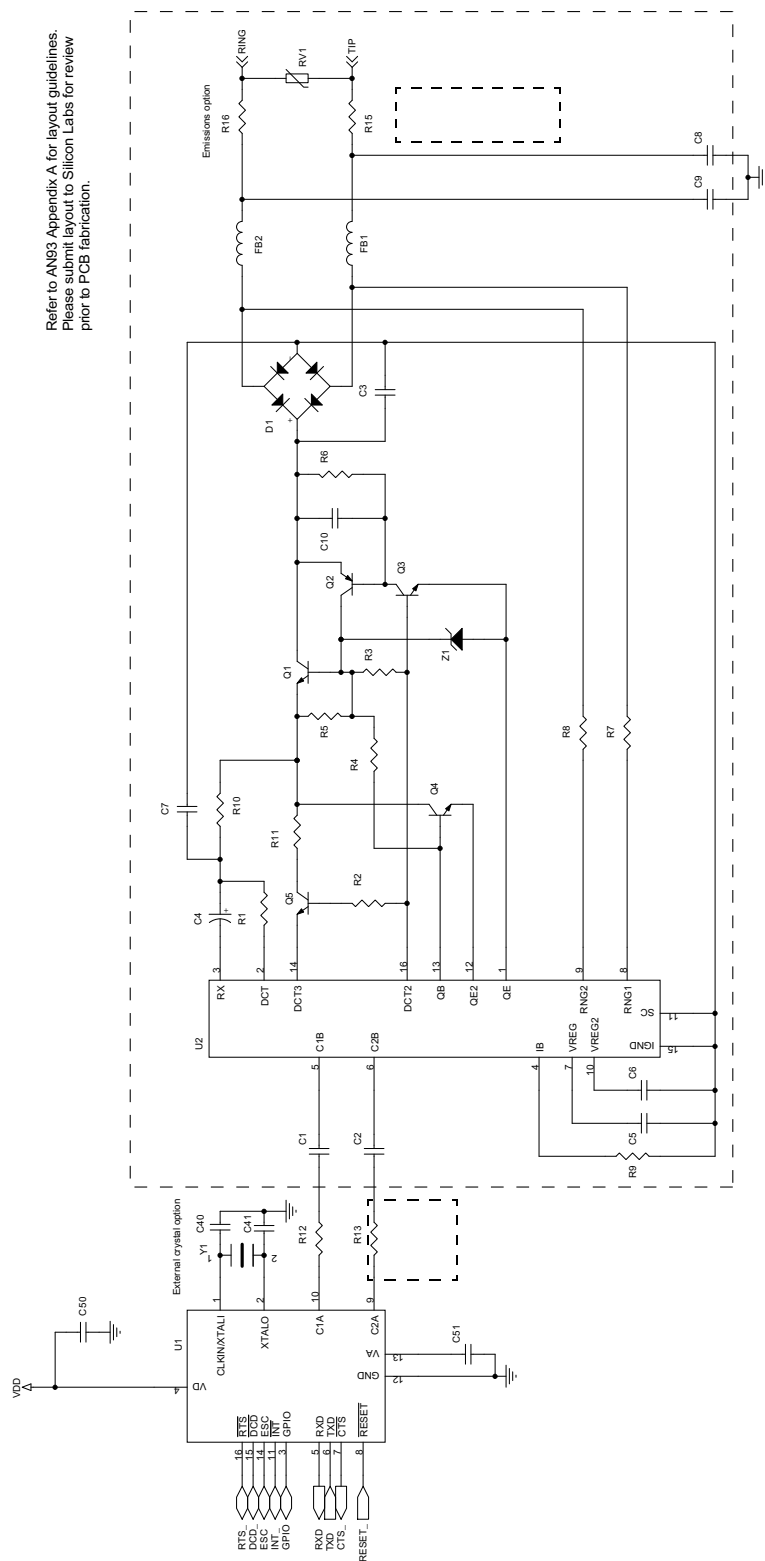


Figure 6. Typical Si2493 Schematic with 16-pin System-Side Option



Figure 7. Typical Si2493 Schematic with 24-pin System-Side Option

## 3. Bill of Materials: Si2493 Chipset

| Component                     | Value   | Supplier(s)               |
|-------------------------------|---|---------------------------|
| C1, C2                        | 33 pF, Y2, X7R, $\pm 20\%$                        | Panasonic, Murata, Vishay |
| C3                            | 10 nF, 250 V, X7R, $\pm 20\%$                     | Venkel, SMEC              |
| C4                            | 1.0 $\mu$ F, 50 V, Elec/Tant, $\pm 20\%$          | Panasonic                 |
| C5, C6, C50, C52 <sup>1</sup> | 0.1 $\mu$ F, 16 V, X7R, $\pm 20\%$                | Venkel, SMEC              |
| C7                            | 2.7 nF, 50 V, X7R, $\pm 20\%$                     | Venkel, SMEC              |
| C8, C9                        | 680 pF, Y2, X7R, $\pm 10\%$                       | Panasonic, Murata, Vishay |
| C10                           | 0.01 $\mu$ F, 16 V, X7R, $\pm 20\%$               | Venkel, SMEC              |
| C40<br>C41                    | 32.768 kHz: 18 pF, 16 V, NPO, $\pm 5\%$           | Venkel, SMEC              |
|                               | 4.9152 MHz: 33 pF, 16 V, NPO, $\pm 5\%$           |                           |
|                               | 27 MHz: Not Populated                             |                           |
| C51, C53 <sup>1</sup>         | 0.22 $\mu$ F, 16 V, X7R, $\pm 20\%$               | Venkel, SMEC              |
| D1, D2 <sup>2</sup>           | Dual Diode, 225 mA, 300 V, CMPD2004S              | Central Semiconductor     |
| FB1, FB2                      | Ferrite Bead, BLM21AG601SN1                       | Murata                    |
| Q1, Q3                        | NPN, 300 V, MMBTA42                               | OnSemi, Fairchild         |
| Q2                            | PNP, 300 V, MMBTA92                               | OnSemi, Fairchild         |
| Q4, Q5                        | NPN, 80 V, 330 mW, MMBTA06                        | OnSemi, Fairchild         |
| RV1                           | Sidactor, 275 V, 100 A                            | Teccor, Protek, ST Micro  |
| R1                            | 1.07 k $\Omega$ , 1/2 W, 1%                       | Venkel, SMEC, Panasonic   |
| R2                            | 150 $\Omega$ , 1/16 W, 5%                         | Venkel, SMEC, Panasonic   |
| R3                            | 3.65 k $\Omega$ , 1/2 W, 1%                       | Venkel, SMEC, Panasonic   |
| R4                            | 2.49 k $\Omega$ , 1/2 W, 1%                       | Venkel, SMEC, Panasonic   |
| R5, R6                        | 100 k $\Omega$ , 1/16 W, 5%                       | Venkel, SMEC, Panasonic   |
| R7, R8                        | 20 M $\Omega$ , 1/16 W, 5%                        | Venkel, SMEC, Panasonic   |
| R9                            | 1 M $\Omega$ , 1/16 W, 1%                         | Venkel, SMEC, Panasonic   |
| R10                           | 536 $\Omega$ , 1/4 W, 1%                          | Venkel, SMEC, Panasonic   |
| R11                           | 73.2 $\Omega$ , 1/2 W, 1%                         | Venkel, SMEC, Panasonic   |
| R12, R13                      | 56 $\Omega$ , 1/16 W, 1%                          | Venkel, SMEC, Panasonic   |
| R15, R16 <sup>3</sup>         | 0 $\Omega$ , 1/16 W                               | Venkel, SMEC, Panasonic   |
| U1                            | Si2493  | Silicon Labs              |
| U2                            | Si3018  | Silicon Labs              |
| Y1 <sup>4</sup>               | 32.768 kHz, 12 pF, 100 ppm, 35 k $\Omega$ max ESR | ECS Inc., Siward          |
|                               | 4.9152 MHz, 20 pF, 100 ppm, 150 $\Omega$ ESR      |                           |
|                               | 27 MHz (from external clock)                      |                           |
| Z1                            | Zener Diode, 43 V, 1/2 W, BZT84C43                | On Semi                   |

## Notes:

1. C52 and C53 should not be populated with the Si2493 16-pin package option.
2. Several diode bridge configurations are acceptable. For example, a single DF04S or four 1N4004 diodes may be used.
3. Murata BLM21AG601SN1 may be substituted for R15–R16 (0  $\Omega$ ) to decrease emissions.
4. To ensure compliance with ITU specifications, frequency tolerance must be less than 100 ppm including initial accuracy, 5-year aging, 0 to 70 °C, and capacitive loading. For optimal V.92 PCM upstream performance, the recommended crystal accuracy is  $\pm 25$  ppm.



Table 7. Protocol Characteristics (Continued)

| Item  | Specification  |
|---|--|
| Data Rate<br>33.6 kbps<br>31.2 kbps<br>28.8 kbps<br>26.4 kbps<br>24.0 kbps<br>21.6 kbps<br>19.2 kbps<br>16.8 kbps<br>14.4 kbps<br>12.0 kbps<br>9600 bps<br>7200 bps<br>4800 bps<br>2400 bps<br>1200 bps<br>300 bps<br>300 bps | ITU-T V.34<br>ITU-T V.34<br>ITU-T V.34<br>ITU-T V.34<br>ITU-T V.34<br>ITU-T V.34<br>ITU-T V.34<br>ITU-T V.34<br>ITU-T V.34<br>ITU-T V.34 or V.32bis<br>ITU-T V.34 or V.32bis<br>ITU-T V.34 or V.32bis<br>ITU-T V.34 or V.32bis<br>ITU-T V.34 or V.32bis<br>ITU-T V.34, IV.32 bis, or V.22bis<br>ITU-T V.22bis, V.23, or Bell 212A<br>ITU-T V.21<br>Bell 103  |
| Data Format<br>Bit asynchronous   | Selectable 8, 9, 10, or 11 bits per character, which includes the start, stop, and parity bits.  |
| Compatibility   | ITU-T V.92, V.90, V.34, V.32bis, V.32, V.23, V.22bis, V.22, V.21, Bell 212A, and Bell 103  |
| Operating Mode<br>Switched network  | Two-wire full duplex   |
| Data Modulation<br>28 to 56 kbps (downstream)<br>24 to 48 kbps (upstream)<br>2.4 to 33.6 kbps<br>14.4 kbps<br>12.0 kbps<br>9600 kbps<br>9600 kbps<br>7200 kbps<br>4800 kbps<br>2400 kbps<br>1200 kbps<br>0 to 300 kbps        | V.90 as specified by ITU-T<br>V.92 as specified by ITU-T<br>V.34 as specified by ITU-T<br>128-level TCM/2400 Baud $\pm 0.01\%$<br>64-level TCM/2400 Baud $\pm 0.01\%$<br>32-level TCM/2400 Baud $\pm 0.01\%$<br>16-level QAM/2400 Baud $\pm 0.01\%$<br>16-level TCM/2400 Baud $\pm 0.01\%$<br>4-level QAM/2400 Baud $\pm 0.01\%$<br>16-level QAM/600 Baud $\pm 0.01\%$<br>4-level PSK/600 Baud $\pm 0.01\%$<br>FSK 0–300 Baud $\pm 0.01\%$ |
| Answer Tone<br>ITU-T V.32bis, V.32, V.22bis, V.22, and V.21 modes<br>Bell 212A and 103 modes  | 2100 Hz $\pm 3$ Hz<br><br>2225 Hz $\pm 3$ Hz   |

Table 7. Protocol Characteristics (Continued)

| Item   | Specification  |
|--|--|
| Transmit Carrier<br>V.92<br>V.90<br>V.34<br>ITU-T V.32bis<br>ITU-T V.32<br>ITU-T V.29<br>ITU-T V.22, V.22bis/Bell 212A<br>Originate mode<br>Answer mode<br>ITU-T V.21<br>Originate mode<br>Answer mode<br>Bell 103<br>Originate mode<br>Answer mode    | As specified by ITU-T<br>As specified by ITU-T<br>As specified by ITU-T<br>1800 Hz $\pm$ 0.01%<br>1800 Hz $\pm$ 0.01%<br>1700 Hz $\pm$ 1 Hz<br><br>1200 Hz $\pm$ 0.5 Hz<br>2400 Hz $\pm$ 1 Hz<br><br>Mark (980 Hz $\pm$ 12 Hz) Space (1180 Hz $\pm$ 12 Hz)<br>Mark (1650 Hz $\pm$ 12 Hz) Space (1850 Hz $\pm$ 12 Hz)<br><br>Mark (1070 Hz $\pm$ 12 Hz) Space (1270 Hz $\pm$ 12 Hz)<br>Mark (2025 Hz $\pm$ 12 Hz) Space (2225 Hz $\pm$ 12 Hz) |
| Output Level<br>Permissive—Switched network  | -9 dBm maximum   |
| Receive Carrier<br>ITU-T V.90<br>ITU-T V.34<br>ITU-T V.32bis<br>ITU-T V.32<br>ITU-T V.29<br>ITU-T V.22, V.22bis/Bell 212A<br>Originate mode<br>Answer mode<br>ITU-T V.21<br>Originate mode<br>Answer mode<br>Bell 103<br>Originate mode<br>Answer mode | As specified by ITU-T<br>As specified by ITU-T<br>1800 Hz $\pm$ 7 Hz<br>1800 Hz $\pm$ 7 Hz<br>1800 Hz $\pm$ 7 Hz<br><br>2400 Hz $\pm$ 7 Hz<br>1200 Hz $\pm$ 7 Hz<br><br>Mark (1650 Hz $\pm$ 12 Hz) Space (1850 Hz $\pm$ 12 Hz)<br>Mark (1650 Hz $\pm$ 12 Hz) Space (1850 Hz $\pm$ 12 Hz)<br><br>Mark (2025 Hz $\pm$ 12 Hz) Space (2225 Hz $\pm$ 12 Hz)<br>Mark (1070 Hz $\pm$ 12 Hz) Space (1270 Hz $\pm$ 12 Hz)                             |
| Carrier Detect (level for ITU-T V.22bis, V.22, V.21, 212, 103) in Switched Network   | Acquisition (-43 dBm)<br>Release (-48 dBm)   |
| Hysteresis   | 2 dBm minimum  |
| ITU-T V.90, V.34, V.32/V.32bis are echo canceling protocols that use signal quality as criteria for maintaining connection. They also provide for self-training detection to force disconnect.   |  |
| DTE Interface  | EIA/TIA-232-E (ITU-T V.24/V.28/ISO 2110)   |
| Line Equalization  | Automatic Adaptive   |
| Connection Options   | Loss of Carrier in ITU-T V.22bis and lower   |



Table 7. Protocol Characteristics (Continued)

| Item                  | Specification   |
|-----------------------|---|
| Phone Types           | 500 (rotary dial), 2500 (DTMF dial)   |
| Dialing               | Pulse and Tone  |
| DTMF Output Level     | Per Part 68   |
| Pulse Dial Ratio      | Make/Break: 39/61%  |
| Ring Cadence          | On 2 seconds; Off 4 seconds   |
| Call Progress Monitor | BUSY<br>CONNECT (rate)<br>NO ANSWER<br>NO CARRIER<br>NO DIALTONE<br>OK<br>RING<br>RINGING |

## 4. Functional Description



**Figure 8. Functional Block Diagram**

The Si2493 ISModem<sup>®</sup> is a complete embedded modem chipset with integrated direct access arrangement (DAA) that provides a programmable line interface to meet global telephone line requirements. Available in two small packages, this solution includes a DSP data pump, modem controller, on-chip RAM and ROM, codec, DAA, analog output, and 27 MHz clock input.

The Si2493 accepts standard modem AT commands and provides connect rates up to 56/33.6/14.4/2.4 kbps full-duplex over the Public Switched Telephone Network (PSTN). The Si2493 features a complete set of modem protocols including all ITU-T standard formats up to V.92.

To provide the most flexibility, the Si2493 ISModem system-side device is offered in either a 24-pin TSSOP or a 16-pin SOIC package. The 16-pin version is footprint-compatible with the Si2401 ISModem and is recommended for most applications. The 16-pin version does not support the parallel, EEPROM or voice codec interface. If these features are required, customers should use the 24-pin version.

The ISModem provides numerous additional features for embedded modem applications. The modem includes full type I and type II caller ID detection and decoding for global standards. Call progress monitoring is supported through standard result codes. The modem is also programmable to meet global settings. Because the Si2493 ISModem integrates the DAA, analog features, such as parallel phone detect, overcurrent detection, and global PTT compliance with a single design, are included.

This device is ideal for embedded modem applications due to its small board space, low power consumption, and global compliance. The Si2493 solution includes a silicon DAA using Silicon Laboratories' proprietary third-generation DAA technology. This highly-integrated DAA can be programmed to meet worldwide PTT specifications for ac termination, dc termination, ringer impedance, and ringer threshold. In addition, the Si2493 has been designed to meet the most stringent worldwide requirements for out-of-band energy, billing-tone immunity, surge immunity, and safety requirements.

The Si2493 allows for rapid integration into existing modem applications by providing a serial interface that can directly communicate to either a microcontroller via a UART interface or a PC via an RS-232 port. This interface allows for PC evaluation of the modem immediately upon powerup via the AT commands using standard terminal software.

### 4.1. Host Interface

The Si2493 interfaces to the host processor through either an asynchronous serial interface, a synchronous Serial Peripheral Interface (SPI), or a parallel interface. The default is asynchronous serial communication. Selection of either SPI or parallel interface is done on power-up with reset strapping. Please refer to "AN93: ISModem<sup>®</sup> Chipset Family Designer's Guide" for details.

#### 4.1.1. Asynchronous Serial Interface

The Si2493 supports asynchronous serial communication with data terminal equipment (DTE) at rates up to 307.2 kbps with the standard serial UART format. Upon powerup, the UART baud rate is automatically detected using the autobaud feature.

#### 4.1.2. Serial Peripheral Interface (SPI)

The serial peripheral interface (SPI) provides a flexible, full-duplex synchronous serial bus for host processor and Si2493 ISModem communication. When the Si2493 is powered up with SPI mode enabled the modem becomes an SPI slave, and the pins are configured to SS (slave select input, active low), MOSI (serial data input to modem), MISO (serial data output from modem) and SCLK (serial data clock input). Each SPI operation consists of a control-and-address byte and a data byte.

#### 4.1.3. Parallel Interface (24-Pin Version Only)

The Si2493 can also communicate via a parallel interface when using the 24-pin version. The parallel interface is an 8-bit data bus with a single bit address to memory mapped registers.

## 4.2. Command Mode

Upon reset, the ISOModem<sup>®</sup> is in command mode and accepts “AT” commands. An outgoing modem call can be made using the “ATDT#” (tone dial) or “ATDP#” (pulse dial) command after the device is configured. If the handshake is successful, the modem responds with the response codes detailed in Table 12 on page 40 and enters data mode.

## 4.3. Data Mode

The Si2493 ISOModem is in data mode while it has a telephone line connection to another modem or is in the process of establishing a connection.

Data protocols are available to provide error correction to improve reliability (V.42 and MNP2-4) and data compression to increase throughput (V.44, V.42bis and MNP5).

Each connection between two modems in data mode begins with a handshaking sequence. During this sequence, the modems determine the line speed, data protocol, and related parameters for the data link. Configuration through AT commands determines the range of choices available to the modem during the negotiation process.

## 4.4. Fast Connect

The Si2493 supports a fast connect mode of operation to reduce the time of a connect sequence in originate mode. The Fast Connect modes can be enabled for V.21, V.22, Bell103, and V.29 modulations. See AN93 for details.

## 4.5. V.80 Synchronous Access Mode

The Si2493 supports a V.80 synchronous access mode of operation, which operates with an asynchronous DTE and a synchronous DCE. See “AN93: ISOModem<sup>®</sup> Chipset Family Designer's Guide”.

## 4.6. Clocking

The Si2493 contains an on-chip phase-locked loop (PLL) and clock generator to derive all necessary internal system clocks from a single clock input. A 32.768 kHz or 4.9152 MHz crystal can be used across XTALI and XTALO pins to form the master clock ( $\pm 100$  ppm max,  $\pm 25$  ppm recommended) for the ISOModem. The 32.768 kHz option can provide lower BOM costs and smaller footprint. Alternatively, a clock input of 27 MHz, 4.9152 MHz, or 32.768 kHz can be provided to XTALI if that clock source is available in the system. A 4.9152 MHz clock input is the default clock option. Other clock options are selected at power-up through reset strapping. Refer to AN93 for details.

## 4.7. Low-Power Modes

The Si2493 provides multiple low power modes. Using the S24 S-register, the Si2493 can be set to automatically enter sleep mode after a pre-programmed time of inactivity with either the DTE or the remote modem. The sleep mode is entered after (S24) seconds have passed since the last DTE activity, after the transmit FIFO is empty, and after the last data are received from the remote modem.

Additionally, the Si2493 can be placed in wake-on-ring-mode using the command, AT&Z. In either mode, the ISOModem remains in the sleep state until one of the following occurs:

- A 1-to-0 transition on TXD (UART mode).
- A 1-to-0 transition on SS (SPI mode).
- A 1-to-0 transition on CS (parallel mode).
- An incoming ring is detected.
- A parallel telephone is picked up.
- Line polarity reversal

The Si2493 may also be placed in a complete powerdown mode. Once the Si2493 completely powers down, it can only be powered back on via the RESET pin.

## 4.8. Data Compression

The modem can achieve DTE (host-to-ISOModem) speeds greater than the maximum DCE (modem-to-modem) speed through the use of a data compression protocol. The compression protocols available are the ITU-T V.44, V.42bis, and MNP5 protocols. Data compression attempts to increase throughput by compressing the data before actually sending it. Thus, the modem is able to transmit more data in a given period of time.

## 4.9. Error Correction

The Si2493 ISOModem can employ error correction (reliable) protocols to ensure error-free delivery of asynchronous data sent between the host and the remote end. The Si2493 supports V.42 and MNP2-4 error correction protocols. V.42 (LAPM) is most commonly used and is enabled by default.

## 4.10. Wire Mode

Wire mode is used to communicate with standard non-error correcting modems. When optioned with \N3, the Si2493 falls back to wire mode if it fails in an attempt to negotiate a V.42 link with the remote modem. Error correction and data compression are not active in wire mode.

## 4.11. V.92 PCM Upstream

The Si2493 supports the ITU-V.92 PCM upstream data protocol. This protocol allows the ISModem to connect at speed up to 48 kbps upstream. Previously the upstream connection rate was limited to 33.6 kbps. The PCM upstream mode is enabled by default; to disable, issue the AT command +PIG=1 (see Table 8 on page 23). To view both downstream and upstream connect speeds in the connect result message, issue the command "AT\V4" or "AT+MR".

## 4.12. V.92 Quick Connect

The Si2493 supports the ITU-V.92 quick connect protocol. Quick connect enables the modem to save and reuse line condition parameters to reduce startup negotiation time.

The quick connect feature is enabled by default in the Si2493. For information on changing the quick connect settings, see the +PSS and +PQC commands shown in Table 8 on page 23.

## 4.13. V.92 Modem-on-Hold

The modem-on-hold (MOH) feature allows the modem user to answer an incoming call while connected online without dropping the internet connection. The modem will remain "on hold" for a period of time determined by the host and the ISP. There are four AT commands that control the operation of MOH. The commands are as follows: +PCW, +PMH, +PMHT, +PMHR. By changing these parameters, the user can enable/disable call waiting and MOH, set the MOH request timeout, and set the MOH initiate timeout. For further details and syntax on these commands see Table 8 on page 23. The MOH feature is most useful when the Si2493 is connected to a central office that allows call waiting.

## 4.14. Caller ID Operation

The Si2493 supports full type I and type II caller ID detection and decode. Caller ID is supported for the US Bellcore, European ETSI, UK, and Japanese protocols and is enabled via the +VCID, +VCDT, and +PCW commands.

## 4.15. Parallel Phone Detection

The ISModem<sup>®</sup> is able to detect when another telephone, modem, or other device is using the phone line. This allows the host to avoid interrupting another phone call when the phone line is already in use and to intelligently handle an interruption when the ISModem is using the phone line.

### 4.15.1. On-Hook Line-in-use Detection

When the ISModem is sharing the telephone line with other devices, it is important that it not interrupt a call in progress. To detect whether another device is using the shared telephone line, the host can use the ISModem to monitor the TIP-RING dc voltage with the line voltage sense (LVS) register (U6C, bits 15:8). The LVS bits have a resolution of 1 V per bit with an accuracy of approximately  $\pm 10\%$ . Bits 0 through 6 of this 8-bit signed twos complement number indicate the value of the line voltage, and the sign bit (bit 7) indicates the polarity of TIP and RING. The ISModem can also monitor the TIP-RING dc voltage using the LVCS register (U79, bits 4:0). See Figure 9 on page 21. See also the %Vn commands for automatic line-in-use detection.

### 4.15.2. Off-Hook Intrusion Detection

When the ISModem is off-hook, an algorithm is implemented in the ISModem to automatically monitor the TIP-RING loop current via the LVCS register. During the off-hook state, the LVCS register switches from representing the TIP-RING voltage to representing the TIP-RING current. See Figure 10 on page 21. Upon detecting an intrusion, the ISModem alerts the host of the condition via the INT pin.



Figure 9. Loop Voltage



Figure 10. Loop Current

## 4.16. Overcurrent Detection

The Si2493 includes an overcurrent detection feature that measures the loop current at a programmable time after the Si2493 goes off-hook. This allows the Si2493 to detect if it is connected to an improper telephone line. The overcurrent detection feature may be enabled by setting the OCDM bit (U70, bit 11). OHT (U77, bits 8:0) sets the delay after off-hook until the loop current is measured. See “AN93: ISModem<sup>®</sup> Chipset Family Designer’s Guide” for details.

## 4.17. Global Operation

The Si2493 chipset contains an integrated silicon direct access arrangement (Silicon DAA) that provides a programmable line interface to meet international telephone line interface requirements. “AN93: Modem Designer’s Guide” gives the DAA register settings required to meet international PTT standards.

Additionally, the user-access registers (via the AT:U and AT:R commands) may be programmed for country-specific settings, such as dial tone, ring, ringback, and busy tone. See AN93 for complete details.

## 4.18. Firmware Upgrades

The Si2493 contains an on-chip program ROM that includes the firmware required for the features listed in this data sheet. In addition, the Si2493 contains on-chip program RAM to accommodate minor changes to the ROM firmware. This allows Silicon Labs to provide future firmware updates to optimize the characteristics of new modem designs and those already deployed in the field. See AN93 for complete details.

## 4.19. DTMF Detection / Generation

The Si2493 provides comprehensive DTMF tone generation and detection. The ISModem can generate single tones or DTMF tones using the +VTS command. DTMF tones may also be generated during dialing using the “ATDT” command. DTMF detection is only available in voice mode (FCLASS = 8). DTMF digits are reported from the modem to the host using <DLE> shielding.

## 4.20. SMS/MMS Support

Short Message Service (SMS) is a service that allows text messages to be sent and received from one telephone to another via an SMS service center. Multimedia Messaging Service (MMS) extends the core SMS capability to send messages that include multimedia content. The Fax ISModem provides an interface that offers a great deal of flexibility in handling multiple SMS standards. This flexibility is possible because most of the differences between standards are handled by the host using the raw data itself. The Si2493 performs the necessary modulation/demodulation of the data and provides two options for message packet structure (Protocol 1 and Protocol 2, as defined in ETSI ES 201 912). The rest of the data link layer and transfer layer are defined by the host system. The content of the message is entirely up to the host including any checksum or CRC. ETSI ES 201 912 describes two standard data and transfer layers that are commonly used. SMS typically relies on caller identification information to determine if the call should be answered using an SMS device or not.

See “6.4. SMS Support” in AN93 for more information on how to configure the modem for SMS support.

## 4.21. Codec Interface (24-Pin Version Only)

In order to support a full range of voice and data applications, the Si2493 includes an optional serial interface that connects to an external voice codec (Si3000). See AN93 for complete details.

## 4.22. EEPROM Interface (24-Pin Version Only)

The Si2493 supports an optional serial peripheral interface (SPI) bus serial EEPROM Mode 3 with a 16-bit (8–64 kbit range) address. Upon powerup, if a pulldown resistor  $\leq 10$  k $\Omega$  is placed between D6 and GND, the ISModem attempts to detect an EEPROM. The EEPROM is intended first for setting custom defaults, second for automatically loading firmware upgrades, and third to allow for user-defined AT command macros for use in custom AT commands or country codes. See AN93 for complete details.

## 4.23. AT Commands

At powerup, the Si2493 is in the AT command mode. In command mode, the modem monitors the input (serial or parallel) checking constantly for a valid command (AT commands are described in Table 8.)

Table 8. Basic AT Command Set (Command Defaults in Bold)

| Command   | Action   |   |
|-----------|--|---|
| \$        | Display AT command mode settings.  |   |
| A         | Answer incoming call   |   |
| A/        | Re-execute last command. This is the only command not preceded by “AT” or followed by a <CR>.  |   |
| Dn        | Dial<br>The dial command, followed by 1 or more dial command modifiers, manually dials a phone number:   |   |
|           | <b>Modifier</b>  | <b>Function</b>   |
|           | ! or &   | Flash hook switch for FHT (U4F) ms (default: 500 ms)  |
|           | , or <   | Pause before continuing for S8 seconds (default: 2 seconds)   |
|           | ;  | Return to AT command mode   |
|           | @  | Wait for silence.   |
|           | G  | Polarity reversal detect. By placing the “G” character in the dial string (i.e. ATDTG1), the Si2493 will monitor the telephone line for polarity reversals. If a busy tone is detected, the Si2493 will report “POLARITY REVERSAL” if a polarity reversal was detected or “NO POLARITY REVERSAL” if a polarity reversal was not detected. In each case, the result code is followed by “OK”. If the S7 timeout occurs before a busy tone is detected, the Si2493 will report “NO CARRIER”. Polarity reversal monitoring begins after the last digit is dialed and ends when the busy tone is detected or S7 timeout occurs.<br><b>Note:</b> It is not possible to establish a modem connection when using this command. |
|           | L  | Redial last number.   |
|           | P  | Pulse (rotary) dialing—pulse digits: 0, 1, 2, 3, 4, 5, 6, 7, 8, 9   |
|           | T  | Tone (DTMF) dialing—DTMF digits: *, #, A, B, C, D, 0, 1, 2, 3, 4, 5, 6, 7, 8, 9.  |
| W         | Wait for dial tone before continuing for S14 seconds (default: 12 seconds). Blind dialing modes X0, X1 and X3 do not affect the W command.   |   |
|           | If the DOP bit (U7A, bit 7) is set, the “ATDTW” command will cause the ISOModem® to pause dialing and either report an “OK” if a dialtone is detected or “NO DIALTONE” if a dial tone is not detected. |   |
| En        | Local DTE echo   |   |
| E0        | Disable  |   |
| <b>E1</b> | <b>Enable</b>  |   |
| Hn        | Hook switch.   |   |
| H0        | Go on-hook (hang up modem).  |   |

**Table 8. Basic AT Command Set (Command Defaults in Bold) (Continued)**

| Command   | Action   |
|-----------|--|
| H1        | Go off-hook.   |
| In        | Identification and checksum.   |
| I0        | Display Si2493 revision code.<br>B: Revision B<br>C: Revision C, etc.                                |
| I1        | Display Si2493 firmware revision code (numeric).   |
| I3        | Display line-side revision code.<br>18C = Si3018 revision C  |
| I6        | Display the ISOModem <sup>®</sup> model number.<br>"2493" = Si2493                                   |
| I7        | Diagnostic results 1. See "AN93: ISOModem <sup>®</sup> Chipset Family Designer's Guide" for details. |
| I8        | Diagnostic results 2. See AN93 for details.  |
| Ln        | Speaker volume operation   |
| L1        | Low speaker volume   |
| L2        | Medium speaker volume  |
| L3        | High speaker volume  |
| Mn        | Speaker operation (via AOUT).  |
| <b>M0</b> | <b>Speaker is always off.</b>  |
| M1        | Speaker is on while dialing and handshaking; off in data mode.                                       |
| M2        | Speaker is always on.  |
| M3        | Speaker is off while dialing, on during handshaking and retraining.                                  |
| On        | Return to data mode from Command mode operation.   |
| O0        | Return to data mode.   |
| O1        | Return to data mode and perform a full retrain (at any speed except 300 bps).                        |
| O2        | Return to data mode and perform rate renegotiation.  |
| Qn        | Response mode.   |
| <b>Q0</b> | <b>Enable result codes (see Table 12 on page 40)</b>   |
| Q1        | Disable result codes (enable quiet mode).  |
| R         | Initiate V.23 reversal.  |
| Sn        | S-register operation (see Table 13 on page 42).  |
| S\$       | List contents of all S registers.  |
| Sn?       | Display contents of S-register n.  |
| Sn=x      | Set S-register n to value x (where n and x are decimal values).                                      |
| Vn        | Result code type (see Table 12 on page 40).  |
| V0        | Numeric result codes.  |



Table 8. Basic AT Command Set (Command Defaults in Bold) (Continued)

| Command   | Action  |
|-----------|---|
| <b>V1</b> | <b>Verbal result codes</b>  |
| Xn        | Call Progress Monitor (CPM)—This command controls which CPM signals are monitored and reported to the host from the Si2493. (See Table 12 on page 40.)  |
| X0        | Basic results; disable CPM—Blind dial (does not wait for dial tone). CONNECT message does not include speed.  |
| X1        | Extended results; disable CPM—Blind dial. CONNECT message includes speed.   |
| X2        | Extended results and detect dial tone only—Add dial tone detection to X1 mode. Does not blind dial.   |
| X3        | Extended results and detect busy only—Add busy tone detection to X1 mode.   |
| <b>X4</b> | <b>Extended results, full CPM—Full CPM enabled, CONNECT message includes speed.</b>   |
| X5        | Extended results—Full CPM enabled including ringback detection. Adds ringback detection to X4 mode.   |
| Yn        | Long space disconnect—Modem hangs up after 1.5 seconds or more of continuous space while on-line.   |
| <b>Y0</b> | <b>Disable.</b>   |
| Y1        | Enable.   |
| Z         | Hard Reset—This command is functionally equivalent to pulsing the RESET pin low. (See $t_{AT}$ in Table 6 on page 7.)   |
| :E        | Read from serial EEPROM.  |
| :I        | Interrupt Read—This command causes the ISOModem <sup>®</sup> to report the lower 8 bits of the interrupt register I/O Control 0 (U70). The CID, OCD, PPD, and RI bits also are cleared, and the INT pin (INT bit in parallel mode) is deactivated on this read.   |
| :M        | Write to serial EEPROM.   |
| :P        | Program RAM Write—This command is used to upload firmware supplied by Silicon Labs to the Si2493. The format for this command is AT:Paaaa,xxxx,yyyy,... where aaaa is the first address in hexadecimal and xxxx,yyyy,... is data in hexadecimal. Only one :P command is allowed per AT command line. No other commands can be concatenated in the :P command line. This command is <i>only</i> for use with special files provided by Silicon Laboratories. Do not attempt to use this command for any other purpose. |
| :R        | User-Access Register Read—This command allows the user to read from the user-access registers (see "6. User-Access Registers (U-Registers)" on page 45). The format is "AT:Raa", where:<br>aa = user-access address in hexadecimal.<br>The "AT:R" command causes all the U- registers to be displayed.  |
| :U        | User-Access Register Write—This command allows the user to write to the 16-bit user-access registers. (See page page 45.) The format is "AT:Uaa,xxxx,yyyy,zzzz,..." where<br>aa = user-access address in hexadecimal.<br>xxxx = Data in hexadecimal to be written to location aa.<br>yyyy = Data in hexadecimal to be written to location (aa + 1).<br>zzzz = Data in hexadecimal to be written to location (aa + 2).<br>etc.   |

Table 8. Basic AT Command Set (Command Defaults in Bold) (Continued)

| Command   | Action  |
|---|---|
| +DR=X   | Data compression reporting.<br><u>X</u> <u>Mode</u><br><b>0</b> <b>Disabled</b><br>1      Enabled<br>If enabled, the intermediate result code is transmitted at the point after error control negotiation. The format of this result code is as follows:<br><u>Result code</u> <u>Mode</u><br>+DR:NONE        Data compression is not in use<br>+DR:V42B        Rec. V.42bis is in use in both directions<br>+DR:V42B RD    Rec. V.42bis is in use in receive direction only<br>+DR:V42B TD    Rec. V.42bis is in use in transmit directions only       |
| +DS Options<br>+DS = A<br>+DS = A,B<br>+DS = A,B,C<br>+DS = A,B,C,D | Controls V.42bis data compression function.<br>A    Direction<br>0    No compression<br>1    Transmit only<br>2    Receive only<br><b>3 Both Directions</b><br>B    Compression_negotiation<br><b>0 Do not disconnect if V.42 is not negotiated.</b><br>1    Disconnect if V.42 is not negotiated.<br>C    Maximum dictionary size<br><b>512</b><br>D    Maximum string size<br>6 to 250 (28 default)   |
| +DS44 Options<br>+DS44 = A<br>+DS44 = A,B<br>+DS44 = A,B,C          | Controls V.44 data compression function.<br>A    Direction<br>0    No compression<br>1    Transmit only<br>2    Receive only<br><b>3 Both Directions</b><br>B    Compression_negotiation<br><b>0 Do not disconnect if V.44 is not negotiated.</b><br>1    Disconnect if Rec. V.42 is not negotiated.<br>C    Capability<br>0    Stream method   |
| +ES Options<br>+ES = A<br>+ES = A,,C                                | Enable synchronous access mode.<br>A    Specifies the mode of operation when initiating a modem connection.<br>D    Disable synchronous assess mode.<br>6    Enable synchronous access mode when connection is completed and data state is entered.<br>B    Specifies fallback mode of operation.<br>This parameter should not be used.<br>C    Specifies the mode of operation when answer a modem connection.<br>D    Disable synchronous assess mode.<br>8    Enable synchronous access mode when connection is completed and data state is entered. |

Table 8. Basic AT Command Set (Command Defaults in Bold) (Continued)

| Command  | Action   |
|--|--|
| +ESA Options<br>+ESA = A<br>+ESA = A,B<br>+ESA = A,B,C<br>+ESA = A,B,C,E<br>+ESA = A,B,C,E,F<br>+ESA = A,B,C,E,F,G | Synchronous access mode control options<br>A Specifies action taken if an underrun condition occurs during transparent sub-mode.<br>0 Modem transmits 8-bit SYN sequences on idle.<br>B Specifies action taken if an underrun condition occurs after a flag during framed sub-mode<br>0 Modem transmits 8-bit HDLC flags on idle.<br>C Specifies action taken if an underrun or overrun condition occurs after a non-flag during framed sub-mode.<br>0 Modem transmits abort on underrun in middle of frame.<br>1 Modem transmits flag on underrun in middle of frame and notifies host of underrun or overrun.<br>D Specifies V.34 half duplex operation.<br>This parameter should not be used.<br>E Specifies CRC polynomial used while in framed sub-mode.<br>0 CRC generation checking disable.<br>1 16-bit CRC generation and checking is performed by the modem.<br>F Specifies NRZI encoding and decoding.<br>0 NRZI encoding and decoding disabled.<br>G Specifies SYN.<br>255 |
| +FCLASS = X  | Class 1 Mode Enable for V.29 fast connect.<br><u>X</u> <u>Mode</u><br><b>0</b> <b>Off</b><br>1 Enables support for Class 1 mode for use in the V.29 fast connect mode.   |
| +FRM = X   | Class 1 Receive Carrier for V.29 fast connect.<br><u>X</u> <u>Mode</u><br>2 Transmit V.21 (980 Hz) tone for longer than 100 ms, then send answer tone (2100/2225 Hz) for 200 ms.<br>95 V.29 short synchronous.<br>96 V.29 full synchronous.  |
| +FTM = X   | Class 1 Transmit Carrier for V.29 fast connect.<br><u>X</u> <u>Mode</u><br>2 Transmit V.21 (980 Hz) tone.<br>53 Same as &T4, but transmit V.29 7200 bps. Data pattern set by S40 register. AT + FCLASS = 0 must be sent to restore the ISOmodem <sup>®</sup> to normal operation after test.<br>54 Same as &T4, but transmit V.29 9600 bps. Data pattern set by S40 register. AT + FCLASS = 0 must be sent to restore the ISOmodem to normal operation after test.<br>95 V.29 short synchronous.<br>96 V.29 full synchronous.  |

Table 8. Basic AT Command Set (Command Defaults in Bold) (Continued)

| Command  | Action   |
|----------|--|
|          | Country settings - Automatically configure all registers for a particular country.   |
|          | <u>X</u> <u>Country</u>  |
|          | 0      Japan   |
|          | 9      Australia   |
|          | A      Austria   |
|          | F      Belgium   |
|          | 16     Brazil  |
|          | 1B     Bulgaria  |
|          | 20     Canada  |
|          | 26     China   |
|          | 27     Columbia  |
|          | 2E     Czech Republic  |
|          | 31     Denmark   |
|          | 35     Ecuador   |
|          | 3C     Finland   |
|          | 3D     France  |
|          | 42     Germany   |
|          | 46     Greece  |
|          | 50     Hong Kong   |
|          | 51     Hungary   |
|          | 53     India   |
|          | 57     Ireland   |
|          | 58     Israel  |
| +GCI = X | 59     Italy   |
|          | 61     South Korea   |
|          | 69     Luxembourg  |
|          | 6C     Malaysia  |
|          | 73     Mexico  |
|          | 7B     Netherlands   |
|          | 7E     New Zealand   |
|          | 82     Norway  |
|          | 87     Paraguay  |
|          | 89     Philippines   |
|          | 8A     Poland  |
|          | 8B     Portugal  |
|          | 9C     Singapore   |
|          | 9F     South Africa  |
|          | A0     Spain   |
|          | A5     Sweden  |
|          | A6     Switzerland   |
|          | B8     Russia  |
|          | B4     United Kingdom  |
|          | <b>B5</b> <b>United States</b>   |
|          | FE     Taiwan  |
|          | <b>Note:</b> U-registers are configured to Silicon Laboratories' recommended values. The +GCI command resets the U-registers and the S7 and S6 S-registers to default values before setting country-specific values. Changes may be made by writing individual registers after sending the AT+GCI command. Refer to "AN93: ISOModem <sup>®</sup> Chipset Family Designer's Guide" for details. |
| +GCI?    | List current country code setting (response is: + GCI:<setting>)   |
| +GCI = ? | List all possible country code settings.   |

Table 8. Basic AT Command Set (Command Defaults in Bold) (Continued)

| Command   | Action   |
|---|--|
| +IFC Options<br>+IFC = A<br>+IFC = A,B  | Specifies the flow control to be implemented.<br>A Specifies the flow control method used by the host to control data from the modem<br>0 None<br>1 Local XON/OFF flow control. Does not pass XON/XOFF character to the remote modem.<br>2 Hardware flow control (RTS)<br>B Specifies the flow control method used by the modem to control data from the host<br>0 None<br>1 Local XON/OFF flow control.<br>2 Hardware flow control (CTS).   |
| +ITF Options<br>+ITF = A<br>+ITF = A,B<br>+ITF = A,B,C  | Transmit flow control threshold.<br>A Threshold above which the modem will generate a flow off signal<br><0 to 511> bytes<br>B Threshold below which the modem will generate a flow on signal<br><0 to 511> bytes<br>C Polling interval for <EM><BNUM> indicator<br>0 to 300 in 10 msec units.   |
| +MR = X   | Modulation reporting control.<br><u>X</u> <u>Mode</u><br><b>0 Disabled</b><br>1 Enabled<br>If enabled, the intermediate result code is transmitted at the point during connect negotiation.<br>The format of this result code is as follows:<br>+MCR: <carrier> e.g. +MCR: V32B<br>+MRR: <rate> e.g. +MRR: 14400   |
| +MS Options<br>+MS = A<br>+MS = A,B<br>+MS = A,B,C<br>+MS = A,B,C,D<br>+MS = A,B,C,D,E<br>+MS = A,B,C,D,E,<br>F | Modulation Selection.<br>A Preferred modem carrier<br>V21 ITU-T V.21<br>V22 ITU-T V.22<br>V22B ITU-T V.22bis<br>V32 ITU-T V.32<br>V32B ITU-T V.32bis<br>V34 ITU-T V.34<br>V90 ITU-T V.90<br>V92 ITU-T V.92<br>B Automatic modulation negotiation<br>0 Disabled<br><b>1 Enabled</b><br>C,D Min TX rate/Max TX rate are optional numeric values that specify the lowest value at which the DCE may establish a connection. If unspecified (set to 0), they are determined by the carrier and automode settings.<br>E,F Min RX rate/Max RX rate are optional numeric values which specify the highest value at which the DCE may establish a connection. If unspecified (set to 0), they are determined by the carrier and automode settings. |

Table 8. Basic AT Command Set (Command Defaults in Bold) (Continued)

| Command  | Action  |
|----------|---|
| +PCW = X | Controls the action to be taken upon detection of call waiting.<br><u>X</u> <u>Mode</u><br>0    Toggle $\overline{RI}$ and collect type II Caller ID if enabled by +VCID.<br>1    Hang up.<br><b>2</b> <b>Ignore call waiting.</b>  |
| +PIG=X   | Controls the use of PCM upstream in a V.92 DCE.<br><u>X</u> <u>Mode</u><br><b>0</b> <b>Enable PCM upstream.</b><br>1    Disable PCM upstream.   |
| +PMH=X   | Controls the modem-on-hold procedures.<br><u>X</u> <u>Mode</u><br><b>0</b> <b>Enables V.92 MOH.</b><br>1    Disables V.92 MOH.  |
| +PMHF=X  | V.92 MOH hook flash. This command causes the DCE to go on-hook and then return off-hook. If this command is initiated and the modem is not On Hold, Error is returned.  |
| +PMHR=X  | Initiate MOH. Requests the DCE to initiate or to confirm a MOH procedure. Valid only if MOH is enabled.<br><u>X</u> <u>Mode</u><br>0    V.92 MOH request denied or not available.<br>1    MOH with 10 s timeout granted.<br>2    MOH with 20 s timeout granted.<br>3    MOH with 30 s timeout granted.<br>4    MOH with 40 s timeout granted.<br>5    MOH with 1 min. timeout granted.<br>6    MOH with 2 min. timeout granted.<br>7    MOH with 3 min. timeout granted.<br>8    MOH with 4 min. timeout granted.<br>9    MOH with 6 min. timeout granted.<br>10    MOH with 8 min. timeout granted.<br>11    MOH with 12 min. timeout granted.<br>12    MOH with 16 min. timeout granted.<br>13    MOH with indefinite timeout granted.<br>14    MOH request denied. Future request will also be denied. |

Table 8. Basic AT Command Set (Command Defaults in Bold) (Continued)

| Command   | Action   |
|-----------|--|
| +PMHT=X   | Controls access to MOH request and sets the timeout value.<br><u>X</u> <u>Mode</u><br>0    Deny V.92 MOH request.<br>1    Grant MOH with 10 s timeout.<br>2    Grant MOH with 20 s timeout.<br>3    Grant MOH with 30 s timeout.<br>4    Grant MOH with 40 s timeout.<br>5    Grant MOH with 1 min. timeout.<br>6    Grant MOH with 2 min. timeout.<br>7    Grant MOH with 3 min. timeout.<br>8    Grant MOH with 4 min. timeout.<br>9    Grant MOH with 6 min. timeout.<br>10   Grant MOH with 8 min. timeout.<br>11   Grant MOH with 12 min. timeout.<br>12   Grant MOH with 16 min. timeout.<br>13   Grant MOH with indefinite timeout. |
| +PQC=X    | V.92 Phase 1 and Phase 2 Control.<br><u>X</u> <u>Mode</u><br><b>0</b> <b>Enable Short Phase 1 and Short Phase 2.</b><br>1    Enable Short Phase 1.<br>2    Enable Short Phase 2.<br>3    Disable Short Phase 1 and Short Phase 2.  |
| +PSS=X    | Selection of full or short startup procedures.<br><u>X</u> <u>Mode</u><br><b>0</b> <b>The DCEs decide to use short startup procedures.</b><br>1    Forces the use of short startup procedures on next and subsequent connections.<br>2    Forces the use of full startup procedures on next and subsequent connections.  |
| +VCDT = X | Caller ID Type.<br><u>X</u> <u>Mode</u><br><b>0</b> <b>After ring only</b><br>1    Always on (Recommended Setting)<br>2    UK<br>3    Japan  |
| +VCID = X | Caller ID Enable.<br><u>X</u> <u>Mode</u><br><b>0</b> <b>Off</b><br>1    On—formatted<br>2    On—raw data format   |
| +VCIDR?   | Type II caller ID information — The Si2493 will display “+VCDIR:” followed by raw caller ID information including checksum.  |

## 4.24. Extended AT Commands

The extended AT commands are supported by the Si2493 and are described in Tables 9 through 11.

**Table 9. Extended AT& Command Set (Command Defaults in Bold)**

| Command | Action   |
|---------|--|
| &\$     | Display AT& current settings.  |
| &D0     | ESC is not used  |
| &D1     | <b>ESC escapes to command mode from data mode if also enabled by HES U70, bit 15.</b>  |
| &D2     | ESC assertion during a modem connection causes the modem to go on-hook and return to command mode.   |
| &D3     | ESC assertion causes ATZ command (reset and return OK result code).  |
| &F      | Restore factory default settings.  |
| &Gn     | Line connection rate limit—This command sets an upper limit on the line speed that the Si2493 can connect. Note that the &Hn commands may limit the line speed as well (&Gn not used for &H0 or &H1). Not all modulations support rates given by &G. Any improper setting will be ignored. |
| &G3     | 1200 bps max   |
| &G4     | 2400 bps max   |
| &G5     | 4.8 kbps max   |
| &G6     | 7.2 kbps max   |
| &G7     | 9.6 kbps max   |
| &G8     | 12 kbps max  |
| &G9     | 14.4 kbps max  |
| &G10    | 16.8 kbps max  |
| &G11    | 19.2 kbps max  |
| &G12    | 21.6 kbps max  |
| &G13    | 24 kbps max  |
| &G14    | 26.4 kbps max  |
| &G15    | 28.8 kbps max  |
| &G16    | 31.2 kbps max  |
| &G17    | 33.6 kbps max  |
| &Hn     | Switched network handshake mode—&Hn commands must be on a separate command line from ATD, ATA, or ATO commands.  |
| &H0     | V.90 with automatic fallback (56 kbps to 300 bps)  |
| &H1     | V.90 only (56 kbps to 28 kbps)   |



Table 9. Extended AT&amp; Command Set (Command Defaults in Bold) (Continued)

| Command | Action  |
|---------|---|
| &H2     | V.34 with automatic fallback (33.6 kbps to 300 bps)   |
| &H3     | V.34 only (33.6 kbps to 2400 bps)   |
| &H4     | ITU-T V.32bis with automatic fallback (14.4 kbps to 300 bps)  |
| &H5     | ITU-T V.32bis only (14.4 kbps to 4800 bps)  |
| &H6     | ITU-T V.22bis only (2400 bps or 1200 bps)   |
| &H7     | ITU-T V.22 only (1200 bps)  |
| &H8     | Bell 212 only (1200 bps)  |
| &H9     | Bell 103 only (300 bps)   |
| &H10    | ITU-T V.21 only (300 bps)   |
| &H11    | V.23 (1200/75 bps)  |
| &H12    | <b>V.92 with automatic fallback</b>   |
| &Pn     | Japan pulse dialing   |
| &P0     | Configure Si2493 for 10 pulse-per-second pulse dialing. For Japan.  |
| &P1     | Configure Si2493 for 20 pulse-per-second pulse dialing. For Japan.  |
| &Tn     | Test mode   |
| &T0     | Cancel test mode (Escape to command mode to issue AT&T0). This command will also report the number of bit errors encountered on the previous &T4 or &T5 test.   |
| &T2     | Initiate ITU-T V.54 (ANALOOP) test. Modulation set by &H AT command. Test loop is through the DSP (Si2493 device) only. ISModem <sup>®</sup> echoes data from TX pin (Register 0 in parallel mode) back to RX pin (Register 0 in parallel mode).  |
| &T3     | Initiate ITU-T V.54 (ANALOOP) test. Modulation set by &H AT command. Test loop is through the DSP (Si2493), DAA interface section (Si2493), DAA interface (Si3018), and analog hybrid circuit (Si3018). ISModem echoes data from TX pin (Register 0 in parallel mode) back to RX pin (Register 0 in parallel mode). Phone line termination required as in Figure 1. To test only the link operation, the hybrid and AFE codec can be removed from the test loop by setting the DL bit (U62, bit 1). |
| &T4     | Initiate transmit as originating modem with automatic data generation. Modulation, data rate, and symbol rate are set by &H, &G, and S41. Data pattern is set by the S40 register. Continues until the ATH command is sent after an escape into command mode. Data is also demodulated as in ANALOOP, and any bit errors are counted to be displayed after the test using &T0.  |
| &T5     | Initiate transmit as answering modem with automatic data generation. Modulation, data rate, and symbol rate are set by &H, &G, and S41. Data pattern is set by the S40 register. Continues until the ATH command is sent after an escape into command mode. Data is also demodulated as in ANALOOP, and any bit errors are counted to be displayed after the test using &T0.  |
| &T6     | Compute checksum for firmware-upgradeable section of program memory. If no firmware upgrade is installed, &T6 returns 0x4474.   |
| &Xn     | Automatic determination of telephone line type.   |

Table 9. Extended AT&amp; Command Set (Command Defaults in Bold) (Continued)

| Command | Action   |
|---------|--|
| &X0     | Abort &x1 or &x2 command.  |
| &X1     | Automatic determination of telephone line type.<br>Result code: WXYZn<br>W: 0 = line supports DTMF dialing.<br>1 = line is pulse dial only.<br>X: 0 = line supports 20 pps dialing.<br>1 = line supports 10 pps dialing only.<br>Y: 0 = extension network present (PBX).<br>1 = outside line (PSTN) connected directly.<br>Z: 0 = continuous dialtone.<br>1 = make-break dialtone.<br>n: 0–9 (number required for outside line if Y = 0).<br><b>Note:</b> The initial number attempted for an outside line is controlled in S51. |
| &X2     | Same as &X1, but Y result (PBX) is not tested.   |
| *Y2A    | Produce a constant answer tone (ITU-T) and return to command mode. The answer tone continues until the ATH command is received or the S7 timer expires.  |
| &Z      | Enter low-power wake-on-ring mode.   |

Table 10. Extended AT% Command Set (Command Defaults in Bold)

| Command                    | Action  |                     |               |                         |                                      |                            |  |                  |   |
|----------------------------|---|---------------------|---------------|-------------------------|--------------------------------------|----------------------------|--|------------------|---|
| %%\$                       | Display AT% command settings.   |                     |               |                         |                                      |                            |  |                  |   |
| %B                         | Report blacklist. See also S42 register.  |                     |               |                         |                                      |                            |  |                  |   |
| %Cn                        | Data compression  |                     |               |                         |                                      |                            |  |                  |   |
| %C0                        | Disable V.42bis and MNP5 data compression   |                     |               |                         |                                      |                            |  |                  |   |
| <b>%C1</b>                 | <b>Enable V.42bis in transmit and receive paths. If MNP is selected (N2), then %C1 enables MNP5 in transmit and receive paths.</b>  |                     |               |                         |                                      |                            |  |                  |   |
| %C2                        | Enable V.42bis in transmit path only.   |                     |               |                         |                                      |                            |  |                  |   |
| %C3                        | Enable V.42bis in receive path only.  |                     |               |                         |                                      |                            |  |                  |   |
| %On                        | Answer mode.  |                     |               |                         |                                      |                            |  |                  |   |
| <b>%O1</b>                 | <b>Si2493 will auto-answer a call in answer mode.</b>   |                     |               |                         |                                      |                            |  |                  |   |
| %O2                        | Si2493 will auto-answer a call in originate mode.   |                     |               |                         |                                      |                            |  |                  |   |
| %Vn                        | Automatic Line Status Detection.<br>After the %V1 and %V2 commands are issued, the Si2493 will automatically check the telephone connection for whether or not a line is present. If a line is present, the Si2493 will automatically check if the line is already in use. Finally, the Si2493 will check line status both before going off-hook and again before dialing. %V1 uses the fixed method, and %V2 uses the adaptive method. %V0 (default) disables this feature.  |                     |               |                         |                                      |                            |  |                  |   |
| <b>%V0</b>                 | <b>Disable automatic line-in-use detection.</b>   |                     |               |                         |                                      |                            |  |                  |   |
| %V1                        | Automatic Line Status Detection—Fixed Method.<br>Description: Before going off-hook with the ATD, ATO, or ATA commands, the Si2493 compares the line voltage (via LVCS) to registers NOLN (U83) and LIUS (U84):<br><br><table border="0"> <thead> <tr> <th><u>Loop Voltage</u></th> <th><u>Action</u></th> </tr> </thead> <tbody> <tr> <td><math>0 \leq LVCS \leq NOLN</math></td> <td>Report "NO LINE" and remain on-hook.</td> </tr> <tr> <td><math>NOLN \leq LVCS \leq LIUS</math></td> <td>Report "LINE IN USE" and remain on-hook.</td> </tr> <tr> <td><math>LIUS \leq LVCS</math></td> <td>Go off-hook and establish a modem connection.</td> </tr> </tbody> </table><br>Once the call has begun, the off-hook intrusion algorithm (described in "4.15.2. Off-Hook Intrusion Detection" on page 20) operates normally. In addition, the Si2493 will report "NO LINE" if the telephone line is completely disconnected. If the HOI bit (U77, bit 11) is set, "LINE IN USE" is reported upon intrusion. | <u>Loop Voltage</u> | <u>Action</u> | $0 \leq LVCS \leq NOLN$ | Report "NO LINE" and remain on-hook. | $NOLN \leq LVCS \leq LIUS$ | Report "LINE IN USE" and remain on-hook. | $LIUS \leq LVCS$ | Go off-hook and establish a modem connection. |
| <u>Loop Voltage</u>        | <u>Action</u>   |                     |               |                         |                                      |                            |  |                  |   |
| $0 \leq LVCS \leq NOLN$    | Report "NO LINE" and remain on-hook.  |                     |               |                         |                                      |                            |  |                  |   |
| $NOLN \leq LVCS \leq LIUS$ | Report "LINE IN USE" and remain on-hook.  |                     |               |                         |                                      |                            |  |                  |   |
| $LIUS \leq LVCS$           | Go off-hook and establish a modem connection.   |                     |               |                         |                                      |                            |  |                  |   |

**Table 10. Extended AT% Command Set (Command Defaults in Bold) (Continued)**

| Command   | Action   |                     |               |   |                                      |   |  |                             |   |
|---|--|---------------------|---------------|---|--------------------------------------|---|--|-----------------------------|---|
| <p>%V2</p>  | <p>Automatic Line Status Detection—Adaptive Method.<br/>           Description: Before going off-hook with the ATD, ATO, or ATA commands, the Si2493 compares the line voltage (via LVCS) to the NLIU (U85) register:</p> <table border="0"> <thead> <tr> <th data-bbox="305 407 779 436"><u>Loop Voltage</u></th> <th data-bbox="779 407 1472 436"><u>Action</u></th> </tr> </thead> <tbody> <tr> <td data-bbox="305 436 779 466"><math>0 \leq LVCS \leq (0.0625 \times NLIU)</math></td> <td data-bbox="779 436 1472 466">Report "NO LINE" and remain on-hook.</td> </tr> <tr> <td data-bbox="305 466 779 495"><math>(0.0625 \times NLIU) &lt; LVCS \leq (0.85 \times NLIU)</math></td> <td data-bbox="779 466 1472 495">Report "LINE IN USE" and remain on-hook.</td> </tr> <tr> <td data-bbox="305 495 779 525"><math>(0.85 \times NLIU) &lt; LVCS</math></td> <td data-bbox="779 495 1472 525">Go off-hook and establish a modem connection.</td> </tr> </tbody> </table> <p>The NLIU register is updated every 1 ms with the minimum non-zero value of LVCS in the last 30 ms. This allows the Si2493 to eliminate errors due to 50/60 Hz interference and also adapt to relatively slow change in the on-hook dc reference value on the telephone line. This algorithm does not allow any non-zero values for NLIU below 0x0007. The host may also initialize NLIU prior to issuing the %V2 command. Once the call has begun, the off-hook intrusion algorithm (described in "4.15.2. Off-Hook Intrusion Detection" on page 20) operates normally. In addition, the Si2493 will report "NO LINE" if the telephone line is completely disconnected. If the HOI (U77, bit 11) bit is set, "LINE IN USE" is reported upon intrusion.</p> | <u>Loop Voltage</u> | <u>Action</u> | $0 \leq LVCS \leq (0.0625 \times NLIU)$ | Report "NO LINE" and remain on-hook. | $(0.0625 \times NLIU) < LVCS \leq (0.85 \times NLIU)$ | Report "LINE IN USE" and remain on-hook. | $(0.85 \times NLIU) < LVCS$ | Go off-hook and establish a modem connection. |
| <u>Loop Voltage</u>                                   | <u>Action</u>  |                     |               |   |                                      |   |  |                             |   |
| $0 \leq LVCS \leq (0.0625 \times NLIU)$               | Report "NO LINE" and remain on-hook.   |                     |               |   |                                      |   |  |                             |   |
| $(0.0625 \times NLIU) < LVCS \leq (0.85 \times NLIU)$ | Report "LINE IN USE" and remain on-hook.   |                     |               |   |                                      |   |  |                             |   |
| $(0.85 \times NLIU) < LVCS$                           | Go off-hook and establish a modem connection.  |                     |               |   |                                      |   |  |                             |   |

Table 11. Extended AT\ Command Set (Command Defaults in Bold)

| Command  | Action  |
|--|---|
| \\$  | Display AT\ command settings.   |
| \Bn  | Character length will be automatically set in autobaud mode   |
| \B0  | 6N1—six data bits, no parity, one stop bit, one start bit, eight bits total (\N0 only) <sup>1</sup>   |
| \B1  | 7N1—seven data bits, no parity, one stop bit, one start bit, nine bits total (\N0 only) <sup>1</sup>  |
| \B2  | 7P1—seven data bits, parity optioned by \P, one stop bit, one start bit, 10 bits total  |
| <b>\B3</b>   | <b>8N1—eight data bits, no parity, one stop bit, one start bit, 10 bits total</b>   |
| \B5  | 8P1—eight data bits, parity optioned by \P, one stop bit, one start bit, 11 bits total (\N0 only)   |
| \B6  | 8X1—eight data bits, one escape bit, one stop bit, one start bit, 11 bits total (enables ninth-bit escape mode)   |
| \Nn  | Asynchronous protocol   |
| \N0  | Wire mode (no error correction, no compression)   |
| \N2  | MNP reliable mode. The Si2493 attempts to connect with the MNP protocol. If unsuccessful, the call is dropped.  |
| <b>\N3</b>   | <b>V.42 auto-reliable—The Si2493 attempts to connect with the V.42 protocol. If unsuccessful, the MNP protocol is attempted. If unsuccessful, wire mode is attempted.</b> |
| \N4  | V.42 (LAPM) reliable mode (or drop call)—Same as \N3 except that the Si2493 drops the call instead of connecting in MNP or wire mode.                                     |
| \N5  | V.42 and MNP reliable mode—The Si2493 attempts to connect with V.42. If unsuccessful, MNP is attempted. If MNP is unsuccessful, the call is dropped.                      |
| \Pn  | Parity type will be automatically set in autobaud mode  |
| <b>\P0</b>   | <b>Even</b>   |
| \P1  | Space <sup>1</sup>  |
| \P2  | Odd   |
| \P3  | Mark <sup>1</sup>   |
| \Qn  | Modem-to-DTE flow control   |
| \Q0  | Disable all flow control—This may only be used if the DTE speed and the VF speed are guaranteed to match throughout the call.   |
| <b>\Q2</b>   | <b>Use CTS only</b>   |
| <b>Notes:</b>  |   |
| <ol style="list-style-type: none"> <li>1. When in autobaud mode, \B0, \B1, and \P1 will not be detected automatically. The combination of \B2 and \P3 will be detected. This is compatible with seven data bits, no parity, two stop bits. Seven data bits, no parity, one stop bit may be forced by sending AT\T17\B1.</li> <li>2. The autobaud feature does not detect this rate.</li> <li>3. Default is \T9 if a pulldown resistor is connected to the autobaud strap pin; otherwise, the default is \T16.</li> </ol> |   |

**Table 11. Extended AT Command Set (Command Defaults in Bold) (Continued)**

| Command     | Action   |
|-------------|--|
| \Q3         | Use RTS/CTS  |
| \Q4         | Use XON/XOFF flow control for modem-to-DTE interface. Does not enable modem-to-modem flow control.   |
| \Tn         | DTE rate—Change DTE rate. When the Si2493 is configured in autobaud mode (default), \T0 through \T15 will lock the new baud rate and disable autobaud. When the ISOModem <sup>®</sup> is not in autobaud mode (the autobaud strap pin low on powerup), the result code “OK” is sent at the old DTE rate. Subsequent commands must be sent at the new rate. |
| \T0         | 300 bps  |
| \T1         | 600 bps  |
| \T2         | 1200 bps   |
| \T3         | 2400 bps   |
| \T4         | 4800 bps   |
| \T5         | 7200 bps   |
| \T6         | 9600 bps   |
| \T7         | 12.0 kbps <sup>2</sup>   |
| \T8         | 14.4 kbps  |
| \T9         | 19.2 kbps <sup>3</sup>   |
| \T10        | 38.4 kbps  |
| \T11        | 57.6 kbps  |
| \T12        | 115.2 kbps   |
| \T13        | 230.4 kbps   |
| \T14        | 245.760 kbps <sup>2</sup>  |
| \T15        | 307.200 kbps   |
| <b>\T16</b> | <b>Autobaud on<sup>3</sup></b>   |
| \T17        | Autobaud off; lock at current baud rate.   |

**Notes:**

1. When in autobaud mode, \B0, \B1, and \P1 will not be detected automatically. The combination of \B2 and \P3 will be detected. This is compatible with seven data bits, no parity, two stop bits. Seven data bits, no parity, one stop bit may be forced by sending AT\T17\B1.
2. The autobaud feature does not detect this rate.
3. Default is \T9 if a pulldown resistor is connected to the autobaud strap pin; otherwise, the default is \T16.

Table 11. Extended AT Command Set (Command Defaults in Bold) (Continued)

| Command  | Action  |
|--|---|
| \U   | Serial mode—causes a low pulse (25 ms) on $\overline{RI}$ and $\overline{DCD}$ . $\overline{INT}$ to be the inverse of ESC. $\overline{RTS}$ to be inverse of $\overline{CTS}$ .<br>Parallel mode—causes a low pulse (25 ms) on $\overline{INT}$ .<br>This command terminates with a $\overline{RESET}$ . |
| \Vn  | Connect message type  |
| <b>\V0</b>   | <b>Report connect message and protocol message</b>  |
| \V2  | Report connect message only (exclude protocol message)  |
| \V4  | Report connect and protocol message with both upstream and downstream connect rates.  |
| <b>Notes:</b> <ol style="list-style-type: none"> <li>1. When in autobaud mode, \B0, \B1, and \P1 will not be detected automatically. The combination of \B2 and \P3 will be detected. This is compatible with seven data bits, no parity, two stop bits. Seven data bits, no parity, one stop bit may be forced by sending AT\T17\B1.</li> <li>2. The autobaud feature does not detect this rate.</li> <li>3. Default is \T9 if a pulldown resistor is connected to the autobaud strap pin; otherwise, the default is \T16.</li> </ol> |   |

Table 12. Result Codes

| Numeric | Meaning                                 | Verbal Response                            | X0 | X1 | X2 | X3 | X4 | X5 |
|---------|---|--|----|----|----|----|----|----|
| 0       | Command was successful                  | OK   | X  | X  | X  | X  | X  | X  |
| 1       | Link established at 300 bps or higher   | CONNECT                                    | X  | X  | X  | X  | X  | X  |
| 2       | Incoming ring detected                  | RING                                       | X  | X  | X  | X  | X  | X  |
| 3       | Link dropped                            | NO CARRIER                                 | X  | X  | X  | X  | X  | X  |
| 4       | Command failed                          | ERROR                                      | X  | X  | X  | X  | X  | X  |
| 5       | Link establish at 1200                  | CONNECT 1200                               |    | X  | X  | X  | X  | X  |
| 6       | Dial tone not present                   | NO DIALTONE                                |    |    | X  |    | X  | X  |
| 7       | Line busy                               | BUSY                                       |    |    |    | X  | X  | X  |
| 8       | Remote not answering                    | NO ANSWER                                  | X  | X  | X  | X  | X  | X  |
| 9       | Ringback detected                       | RINGING                                    |    |    |    |    |    | X  |
| 10      | Link established at 2400                | CONNECT 2400                               |    | X  | X  | X  | X  | X  |
| 11      | Link established at 4800                | CONNECT 4800                               |    | X  | X  | X  | X  | X  |
| 12      | Link established at 9600                | CONNECT 9600                               |    | X  | X  | X  | X  | X  |
| 14      | Link established at 19200               | CONNECT 19200                              |    | X  | X  | X  | X  | X  |
| 15      | Link established at 7200                | CONNECT 7200                               |    | X  | X  | X  | X  | X  |
| 16      | Link established at 12000               | CONNECT 12000                              |    | X  | X  | X  | X  | X  |
| 17      | Link established at 14400               | CONNECT 14400                              |    | X  | X  | X  | X  | X  |
| 18      | Link established at 16800               | CONNECT 16800                              |    | X  | X  | X  | X  | X  |
| 19      | Link established at 21600               | CONNECT 21600                              |    | X  | X  | X  | X  | X  |
| 20      | Link established at 24000               | CONNECT 24000                              |    | X  | X  | X  | X  | X  |
| 21      | Link established at 26400               | CONNECT 26400                              |    | X  | X  | X  | X  | X  |
| 22      | Link established at 28800               | CONNECT 28800                              |    | X  | X  | X  | X  | X  |
| 23      | Link established at 31200               | CONNECT 31200                              |    | X  | X  | X  | X  | X  |
| 24      | Link established at 33600               | CONNECT 33600                              |    | X  | X  | X  | X  | X  |
| 30      | Caller ID mark detected                 | CIDM                                       | X  | X  | X  | X  | X  | X  |
| 31      | Hookswitch flash detected               | FLASH                                      | X  | X  | X  | X  | X  | X  |
| 32      | UK CID State Tone Alert Signal detected | STAS                                       | X  | X  | X  | X  | X  | X  |
| 33      | Overcurrent condition                   | X*   | X  | X  | X  | X  | X  | X  |
| 40      | Blacklist is full                       | BLACKLIST FULL (enabled via S42 register)  | X  | X  | X  | X  | X  | X  |
| 41      | Attempted number is black-listed.       | BLACKLISTED (enabled via S42 register)     | X  | X  | X  | X  | X  | X  |
| 42      | No phone line present                   | NO LINE (enabled via %Vn commands)         | X  | X  | X  | X  | X  | X  |
| 43      | Telephone line is in use                | LINE IN USE (enabled via %Vn commands)     | X  | X  | X  | X  | X  | X  |
| 44      | A polarity reversal was detected        | POLARITY REVERSAL (enabled via G modifier) | X  | X  | X  | X  | X  | X  |

**\*Note:** X is the only verbal response code that does not follow the <CR><LF>Result Code<CR><LF> standard. There is no leading <CR><LF>.



Table 12. Result Codes (Continued)

| Numeric | Meaning                                       | Verbal Response                                  | X0                    | X1 | X2 | X3 | X4 | X5 |
|---------|---|--|-----------------------|----|----|----|----|----|
| 45      | A polarity reversal was NOT detected          | NO POLARITY REVERSAL<br>(enabled via G modifier) | X                     | X  | X  | X  | X  | X  |
| 52      | Link established at 56000                     | CONNECT 56000                                    |                       | X  | X  | X  | X  | X  |
| 60      | Link established at 32000                     | CONNECT 32000                                    |                       | X  | X  | X  | X  | X  |
| 61      | Link established at 48000                     | CONNECT 48000                                    |                       | X  | X  | X  | X  | X  |
| 63      | Link established at 28000                     | CONNECT 28000                                    |                       | X  | X  | X  | X  | X  |
| 64      | Link established at 29333                     | CONNECT 29333                                    |                       | X  | X  | X  | X  | X  |
| 65      | Link established at 30666                     | CONNECT 30666                                    |                       | X  | X  | X  | X  | X  |
| 66      | Link established at 33333                     | CONNECT 33333                                    |                       | X  | X  | X  | X  | X  |
| 67      | Link established at 34666                     | CONNECT 34666                                    |                       | X  | X  | X  | X  | X  |
| 68      | Link established at 36000                     | CONNECT 36000                                    |                       | X  | X  | X  | X  | X  |
| 69      | Link established at 37333                     | CONNECT 37333                                    |                       | X  | X  | X  | X  | X  |
| 70      | No protocol                                   | PROTOCOL: NONE                                   | Set with \V0 command. |    |    |    |    |    |
| 75      | Link established at 75                        | CONNECT 75                                       |                       | X  | X  | X  | X  | X  |
| 77      | V.42 protocol                                 | PROTOCOL: V42                                    | Set with \V0 command. |    |    |    |    |    |
| 79      | V.42bis protocol                              | PROTOCOL: V42bis                                 | Set with \V0 command. |    |    |    |    |    |
| 80      | MNP2 protocol                                 | PROTOCOL:<br>ALTERNATE, + CLASS 2                | Set with \V command.  |    |    |    |    |    |
| 81      | MNP3 protocol                                 | PROTOCOL:<br>ALTERNATE, + CLASS 3                | Set with \V command.  |    |    |    |    |    |
| 82      | MNP4 protocol                                 | PROTOCOL:<br>ALTERNATE, + CLASS 4                | Set with \V command.  |    |    |    |    |    |
| 83      | MNP5 protocol                                 | PROTOCOL:<br>ALTERNATE, + CLASS 5                |                       | X  | X  | X  | X  | X  |
| 90      | Link established at 38666                     | CONNECT 38666                                    |                       | X  | X  | X  | X  | X  |
| 91      | Link established at 40000                     | CONNECT 40000                                    |                       | X  | X  | X  | X  | X  |
| 92      | Link established at 41333                     | CONNECT 41333                                    |                       | X  | X  | X  | X  | X  |
| 93      | Link established at 42666                     | CONNECT 42666                                    |                       | X  | X  | X  | X  | X  |
| 94      | Link established at 44000                     | CONNECT 44000                                    |                       | X  | X  | X  | X  | X  |
| 95      | Link established at 45333                     | CONNECT 45333                                    |                       | X  | X  | X  | X  | X  |
| 96      | Link established at 46666                     | CONNECT 46666                                    |                       | X  | X  | X  | X  | X  |
| 97      | Link established at 49333                     | CONNECT 49333                                    |                       | X  | X  | X  | X  | X  |
| 98      | Link established at 50666                     | CONNECT 50666                                    |                       | X  | X  | X  | X  | X  |
| 99      | Link established at 52000                     | CONNECT 52000                                    |                       | X  | X  | X  | X  | X  |
| 100     | Link established at 53333                     | CONNECT 53333                                    |                       | X  | X  | X  | X  | X  |
| 101     | Link established at 54666                     | CONNECT 54666                                    |                       | X  | X  | X  | X  | X  |
| 102     | DTMF dial attempted on a pulse dial only line | UN-OBTAINABLE NUMBER                             | X                     | X  | X  | X  | X  | X  |

**\*Note:** X is the only verbal response code that does not follow the <CR><LF>Result Code<CR><LF> standard. There is no leading <CR><LF>.

## 5. S-Registers

The S command allows reading (Sn?) or writing (Sn = x) the S-registers. The S-registers store values for functions that typically are rarely changed, such as timers or counters, and the ASCII values of control characters, such as carriage return. Table 13 summarizes the S-register set.

**Table 13. S-Register Description**

| Definition           |   |                   |       |             |
|----------------------|---|-------------------|-------|-------------|
| S-Register (Decimal) | Function  | Default (Decimal) | Range | Units       |
| 0                    | Automatic answer—Number of rings the Si2493 must detect before answering a call. 0 disables auto answer.  | 0                 | 0–255 | Rings       |
| 1                    | Ring counter.   | 0                 | 0–255 | Rings       |
| 2                    | ESC code character.   | 43 (+)            | 0–255 | ASCII       |
| 3                    | Carriage return character.  | 13 (CR)           | 0–255 | ASCII       |
| 4                    | Linefeed character.   | 10 (LF)           | 0–255 | ASCII       |
| 5                    | Backspace character.  | 08 (BS)           | 0–255 | ASCII       |
| 6                    | Dial tone wait timer—Number of seconds the Si2493 waits before blind dialing. Only applicable if blind dialing is enabled (X0, X1, X3).   | 02                | 0–255 | seconds     |
| 7                    | Carrier wait timer—Number of seconds the Si2493 waits for carrier before timing out. This register also sets the number of seconds the modem waits for ring-back when originating a call before hanging up. This register also sets the number of seconds the answer tone will continue while using the AT*Y2A command. | 80                | 0–255 | seconds     |
| 8                    | Dial pause timer for , and < dial command modifiers.  | 02                | 0–255 | seconds     |
| 9                    | Carrier presence timer—Time after a loss of carrier that a carrier must be detected before reactivating DCD. S9 is referred to as “carrier loss debounce time.”   | 06                | 1–255 | 0.1 second  |
| 10                   | Carrier loss timer—Time the carrier must be lost before the Si2493 disconnects. Setting 255 disables disconnect entirely. If S10 is less than S9, even a momentary loss of carrier causes a disconnect.   | 14                | 1–255 | 0.1 second  |
| 12                   | Escape code guard timer—Minimum guard time required before and after “+++” for the Si2493 to recognize a valid escape sequence.   | 50                | 1–255 | 0.02 second |
| 14                   | Wait for dial tone delay value (in relation to the W dial modifier). Starts when “W” is executed in the dial string.  | 12                | 0–255 | seconds     |

Table 13. S-Register Description (Continued)

| Definition           |  |                   |       |         |
|----------------------|--|-------------------|-------|---------|
| S-Register (Decimal) | Function   | Default (Decimal) | Range | Units   |
| 24                   | Sleep Inactivity Time—Sets the time that the modem operates in normal power mode with no activity on the serial port, parallel port, or telephone line before entering low-power sleep mode. This feature is disabled if the timer is set to 0.  | 0                 | 0–255 | seconds |
| 30                   | Disconnect Activity Timer—Sets the length of time that the modem stays online before disconnecting with no activity on the serial port, parallel port, or telephone line (Ring, hookswitch flash, or caller ID). This feature is disabled if set to 0.   | 0                 | 0–255 | minutes |
| 38                   | Hang Up Delay Time—Maximum delay between receipt of ATH0 command and hang up. If time out occurs before all data can be sent, the NO CARRIER (3) result code is sent (operates in V.42 mode only). “OK” response is sent if all data is transmitted before timeout. S38 = 255 disables timeout and modem disconnects only if data is successfully sent or carrier is lost.   | 20                | 0–255 | seconds |
| 40                   | Data Pattern—Data pattern generated during &T4 and &T5 transmit tests.<br><b>0 = All spaces (0s)</b><br>1 = All marks (1s)<br>2 = Random data  | 0                 | 0–2   |         |
| 41                   | V.34 symbol rate - Symbol rate for V.34 when using the &T4 and &T5 commands.<br><b>0 = 2400 symbols/second</b><br>1 = 2743 symbols/second<br>2 = 2800 symbols/second<br>3 = 3000 symbols/second<br>4 = 3200 symbols/second<br>5 = 3429 symbols/second<br>A valid combination of symbol rate (S41) and data rate (&G) must be selected.<br><b><u>Symbol Rate</u>    <u>Allowable Data Rates</u></b><br>2400            2400–21600<br>2743            4800–26400<br>2800            4800–26400<br>3000            4800–28800<br>3200            4800–31200<br>3429            4800–33600 | 0                 | 0–5   |         |

Table 13. S-Register Description (Continued)

| Definition           |  |                   |       |         |
|----------------------|--|-------------------|-------|---------|
| S-Register (Decimal) | Function   | Default (Decimal) | Range | Units   |
| 42                   | Blacklisting—The Si2493 will not dial the same number more than two times in three minutes. An attempt to dial a third time within three minutes will result in a “BLACKLISTED” result code. If the blacklist memory is full, any dial to a new number will result in a “BLACKLIST FULL” result code. Numbers are added to the blacklist only if the modem connection fails. The %B command will list the numbers on the blacklists.<br><b>0 = disabled</b><br>1 = enabled | 0 (disabled)      | 0–1   |         |
| 43                   | Dial Attempts to Blacklist.<br>When blacklisting is enabled with S42, this value controls the number of dial attempts that will result in a number being blacklisted.  | 4                 | 0–4   | —       |
| 44                   | Blacklist Timer.<br>Period during which blacklisting is active.  | 180               | 0–255 | seconds |
| 50                   | Minimum on-hook time—Modem will remain on-hook for S50 seconds. Any attempt to go off-hook will be delayed until this timer expires.   | 3                 | 0–255 | seconds |
| 51                   | Number to start checking for an outside line on a PBX. See &X command for details.   | 1                 | 0–9   | —       |

## 6. User-Access Registers (U-Registers)

U-Registers are 16-bit registers written by the AT:Uaa command and read by the AT:R (read all U-Registers) or AT:Raa (read U-Register aa) commands (see the AT command list in Table 11 on page 37). Many aspects of the modem's and DAA's behavior can be enabled/disabled, configured, monitored, and/or modified through U-Registers; however, most of them will not be needed in normal use.

There are two types of U-Registers. The first represents a single 16-bit term, such as a filter coefficient, threshold, delay, or other quantity. These registers can be read from or written to as a single 16-bit value. The second type of U-Register is bit-mapped. Bit-mapped registers are written and/or read in hexadecimal, but each bit or combination of bits in the register represents an independent value or status information. These individual bits are used to enable or disable features and indicate states.

Some U-Registers are reserved and not available to the user. Therefore, there are gaps in the available U-Register address map. Additionally, some bits within available U-Registers are reserved. Any attempt to write a non-zero value to a reserved U-Register or bit may cause unpredictable modem operation.

With over 100 U-Registers, the Si2493 offers an unprecedented level of programmability for a controller-based modem. A detailed list of these registers and their use is beyond the scope of this data sheet. Refer to "AN93: ISModem<sup>®</sup> Chipset Family Designer's Guide" for details.

## 7. Pin Descriptions: Si2493 (16-Pin Option)



| Pin # | Pin Name       | Description  |
|-------|----------------|--|
| 1     | CLKIN/XTALI    | <b>XTALI—Crystal Oscillator Pin.</b><br>Provides support for parallel resonant AT cut crystals. A 4.9152 MHz or 32.768 kHz crystal or a 32.768 kHz or 4.9152 or 27 MHz clock on XTALI is required. |
| 2     | XTALO          | <b>XTALO—Crystal Oscillator Pin.</b><br>Serves as the output of the crystal amplifier.   |
| 3     | RI             | <b>Ring Indicator.</b><br>The RI on (active low) indicates the presence of an ON segment of a ring signal on the telephone line.   |
| 4     | V <sub>D</sub> | <b>Supply Voltage.</b><br>Provides the 3.3 V supply voltage to the Si2493.   |
| 5     | MISO/RXD       | <b>Master In Slave Out/Receive Data.</b><br>Serial data output from modem in SPI mode.<br>Data output to DTE RXD pin in UART mode.   |
| 6     | MOSI/TXD       | <b>Master Out Slave In/Transmit Data.</b><br>Serial data input to modem in SPI mode.<br>Data input from DTE TXD pin in UART mode.  |
| 7     | SCLK/CTS       | <b>Serial Data Clock/Clear to Send.</b><br>Signals that the Si2493 is ready to receive more digital data on the TXD pin.<br>Serial data clock input in SPI mode.                                   |
| 8     | RESET          | <b>Reset Input.</b><br>An active low input that is used to reset all control registers to a defined, initialized state.  |
| 9     | C2A            | <b>Isolation Capacitor 2A.</b><br>Connects to one side of the isolation capacitor C2.  |
| 10    | C1A            | <b>Isolation Capacitor 1A.</b><br>Connects to one side of the isolation capacitor C1.  |
| 11    | INT            | <b>Interrupt Output.</b><br>Active low interrupt output.   |

| Pin # | Pin Name                   | Description  |
|-------|----------------------------|--|
| 12    | GND                        | <b>Ground.</b><br>Connects to the system digital ground.   |
| 13    | V <sub>A</sub>             | <b>Regulator Voltage Reference.</b><br>Connects to an external capacitor and serves as the reference for the internal voltage regulator.                 |
| 14    | ESC                        | <b>Escape.</b><br>A positive edge on this pin causes the modem to go from online (connected) mode to the offline (command) mode.                         |
| 15    | $\overline{\text{DCD}}$    | <b>Carrier Detect.</b><br>Active low carrier detect.   |
| 16    | $\overline{\text{SS/RTS}}$ | <b>SPI Slave Select/Request to Send.</b><br>Active low slave select in SPI mode.<br>Active low request to send input used for flow control in UART mode. |

## 8. Pin Descriptions: Si2493 (24-Pin Option)



| Pin # | Pin Name           | Description  |
|-------|--------------------|--|
| 1     | CLKIN/XTALI        | <b>Clock Input/Crystal Oscillator Pin.</b><br>Provides support for parallel resonant AT cut crystals. A 4.9152 MHz or 32.768 kHz crystal or a 32.768 kHz or 4.9152 or 27 MHz clock on XTALI is required.                   |
| 2     | XTALO              | <b>Crystal Oscillator Pin.</b><br>Serves as the output of the crystal amplifier.   |
| 3     | CLKOUT/EECS/<br>A0 | <b>Clock Output/EEPROM Chip Select/Address Bit 0.</b><br>Clock output in serial mode. Active low read/write enable for SPI EEPROM in serial mode when pin 4 is pulled low during powerup. Address Enable in parallel mode. |
| 4     | FSYNC/D6           | <b>Frame Sync/Data Bit.</b><br>Frame Sync output to codec in serial mode. Bidirectional parallel bus data bit 6 in parallel mode.  |
| 5, 21 | VD3.3              | <b>Digital Supply Voltage.</b><br>Provides the 3.3 V supply voltage to the Si2493.   |
| 6, 20 | GND                | <b>Ground.</b><br>Connects to the system digital ground.   |
| 7, 19 | VDA, VDB           | <b>Regulator Voltage Reference.</b><br>Connects to an external capacitor and serves as the reference for the internal voltage regulator.   |
| 8     | SS/RTS/D7          | <b>SPI Slave Select/Request to Send/Data Bit.</b><br>Active low slave select in SPI mode.<br>Active low request to send input used for flow control in UART mode. Bidirectional parallel bus data bit 7 in parallel mode.  |



| Pin # | Pin Name   | Description   |
|-------|--|---|
| 9     | MISO/RXD/ $\overline{\text{RD}}$                       | <b>Master In Slave Out/Receive Data/Read Enable.</b><br>Serial data output from modem in SPI mode. Data output to DTE RXD pin in UART mode. Active low read enable pin in parallel mode.  |
| 10    | MOSI/TXD/ $\overline{\text{WR}}$                       | <b>Master Out Slave In/Transmit Data/Write Enable.</b><br>Serial data input to modem in SPI mode. Data input from DTE TXD pin in serial mode. Active low write enable pin in parallel mode.   |
| 11    | SCLK/ $\overline{\text{CTS}}$ / $\overline{\text{CS}}$ | <b>Serial Data Clock/Clear to Send/Chip Select.</b><br>Serial data clock input in SPI mode. Signals that the Si2493 is ready to receive more digital data on the TXD pin in data mode. Active low chip select in parallel mode.   |
| 12    | $\overline{\text{RESET}}$                              | <b>Reset Input.</b><br>An active low input that is used to reset all control registers to a defined initialized state.  |
| 13    | C2A  | <b>Isolation Capacitor 2A.</b><br>Connects to one side of the isolation capacitor, C2.  |
| 14    | C1A  | <b>Isolation Capacitor 1A.</b><br>Connects to one side of the isolation capacitor, C1.  |
| 15    | AOUT/ $\overline{\text{INT}}$                          | <b>Analog Output/Interrupt Output.</b><br>Analog output in serial mode. Active low interrupt output in parallel mode.   |
| 16    | $\overline{\text{INT}}$ /D0                            | <b>Interrupt Output/Data Bit.</b><br>Active low interrupt output in serial mode. Bidirectional parallel bus data bit 0 in parallel mode.  |
| 17    | $\overline{\text{RI}}$ /D1                             | <b>Ring Indicator/Data Bit.</b><br>The $\overline{\text{RI}}$ on (active low) indicates the presence of an ON segment of a ring signal on the telephone line. Bidirectional parallel bus data bit 1 in parallel mode.   |
| 18    | SDI/EESD/D2  | <b>Serial Data In/EEPROM Serial Data Input/Output/Data Bit.</b><br>Serial Data In (to codec) output in serial mode. Bidirectional Input/Output to SPI EEPROM in serial mode when pin 4 is pulled low during power up. Bidirectional parallel bus data bit 2 in parallel mode. |
| 22    | ESC/D3   | <b>Escape/Data Bit.</b><br>Hardware escape in serial mode. Bidirectional parallel bus data bit 3 in parallel mode.  |
| 23    | $\overline{\text{DCD}}$ /D4                            | <b>Carrier Detect/Data Bit.</b><br>Active low carrier detect in serial mode. Bidirectional parallel bus data bit 4 in parallel mode.  |
| 24    | SDO/EECLK/D5   | <b>Serial Data Out/EEPROM Clock/Data Bit 5.</b><br>Serial Data Out (from codec) input in serial mode. Clock output for SPI EEPROM in serial mode when pin 4 is pulled low during power up. Bidirectional parallel bus data bit 5 in parallel mode.                            |

## 9. Pin Descriptions: Si3018



**Table 14. Si3018 Pin Descriptions**

| Pin # | Pin Name | Description  |
|-------|----------|--|
| 1     | QE       | <b>Transistor Emitter.</b><br>Connects to the emitter of Q3.   |
| 2     | DCT      | <b>DC Termination.</b><br>Provides dc termination to the telephone network.  |
| 3     | RX       | <b>Receive Input.</b><br>Serves as the receive side input from the telephone network.  |
| 4     | IB       | <b>Internal Bias.</b><br>Provides a bias voltage to the device.  |
| 5     | C1B      | <b>Isolation Capacitor 1B.</b><br>Connects to one side of isolation capacitor C1 and communicates with the system side.                      |
| 6     | C2B      | <b>Isolation Capacitor 2B.</b><br>Connects to one side of isolation capacitor C2 and communicates with the system side.                      |
| 7     | VREG     | <b>Voltage Regulator.</b><br>Connects to an external capacitor to provide bypassing for an internal power supply.                            |
| 8     | RNG1     | <b>Ring 1.</b><br>Connects through a resistor to the RING lead of the telephone line. Provides the ring and caller ID signals to the Si2493. |
| 9     | RNG2     | <b>Ring 2.</b><br>Connects through a resistor to the TIP lead of the telephone line. Provides the ring and caller ID signals to the Si2493.  |
| 10    | VREG2    | <b>Voltage Regulator 2.</b><br>Connects to an external capacitor to provide bypassing for an internal power supply.                          |
| 11    | SC       | <b>SC Connection.</b><br>Enables external transistor network. Should be tied through a 0 Ω resistor to I <sub>GND</sub> .                    |
| 12    | QE2      | <b>Transistor Emitter 2.</b><br>Connects to the emitter of Q4.   |
| 13    | QB       | <b>Transistor Base.</b><br>Connects to the base of transistor Q4.  |
| 14    | DCT3     | <b>DC Termination 3.</b><br>Provides the dc termination to the telephone network.  |
| 15    | IGND     | <b>Isolated Ground.</b><br>Connects to ground on the line-side interface.  |
| 16    | DCT2     | <b>DC Termination 2.</b><br>Provides dc termination to the telephone network.  |

## 10. Ordering Guide

| Chipset | Max Speed | System-Side Package | System-Side | Line-Side   | Temp Range   |
|---------|-----------|---------------------|-------------|-------------|--------------|
| Si2493  | 56 kbps   | 24-pin TSSOP        | Si2493-D-FT | Si3018-F-FS | 0 to 70 °C   |
| Si2493  | 56 kbps   | 16-pin SOIC         | Si2493-D-FS | Si3018-F-FS | 0 to 70 °C   |
| Si2493  | 56 kbps   | 24-pin TSSOP        | Si2493-D-GT | Si3018-F-GS | -40 to 85 °C |

**Note:** Add an "R" at the end of the device to denote tape and reel option.

## 11. Package Markings (Top Markings)

Codes for the Si2493-D-FT and Si2493-D-FS top marks are as follows:

- YY = Current Year
- WW = Work Week
- R = Die Revision
- TTTTT = Trace Code
- XX = Assembly Country Code

### 11.1. Si2493-D-FT Top Marking



Figure 11. Si2493-D-FT Top Marking

### 11.2. Si2493-D-FS Top Marking



Figure 12. Si2493-D-FS Top Marking

### 11.3. Si3018-F-FS Top Marking



Figure 13. Si3018-F-FS Top Marking

## 12. Package Outline: 24-Pin TSSOP

Figure 14 illustrates the package details for the Si2493 24-pin packaging option. Table 15 lists the values for the dimensions shown in the illustration.



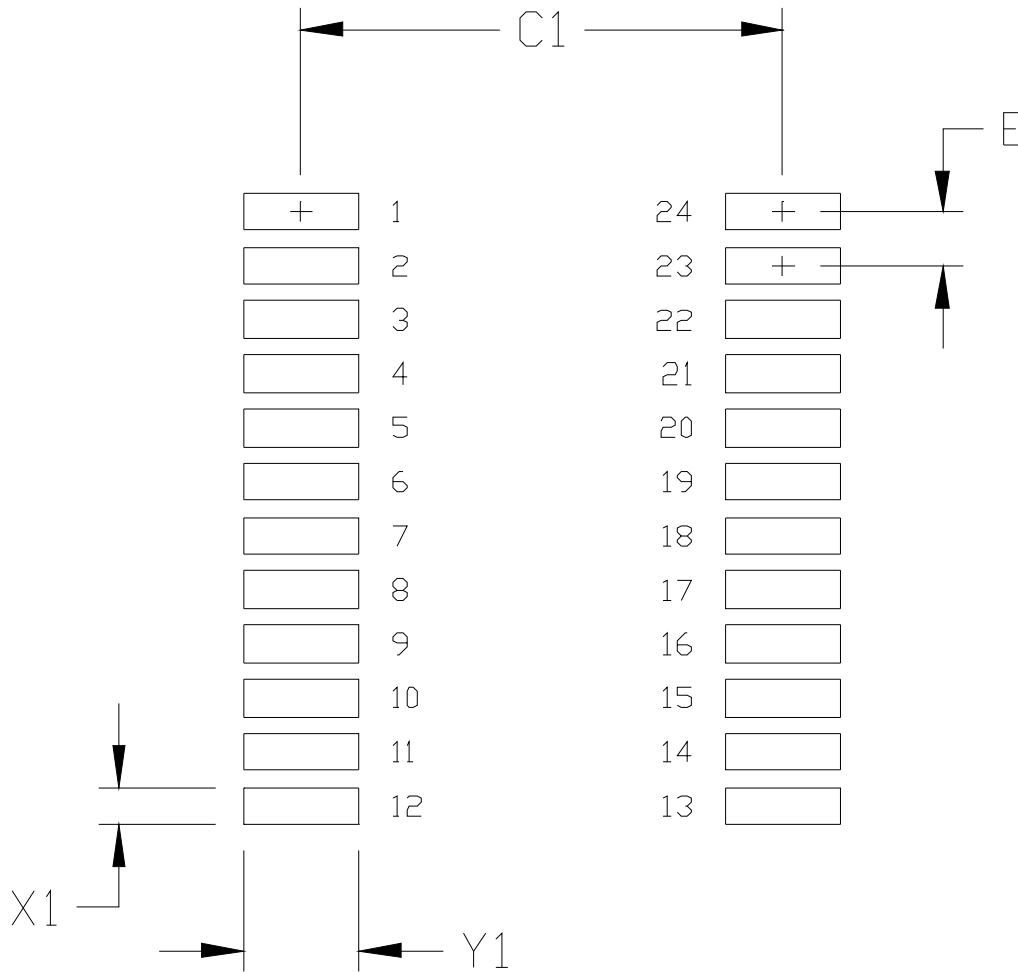
Figure 14. 24-Pin Thin Shrink Small Outline Package (TSSOP)

Table 15. Package Diagram Dimensions

| Dimension  | Min      | Nom  | Max  |
|--|----------|------|------|
| A  | —        | —    | 1.20 |
| A1   | 0.05     | —    | 0.15 |
| A2   | 0.80     | 1.00 | 1.05 |
| b  | 0.19     | —    | 0.30 |
| c  | 0.09     | —    | 0.20 |
| D  | 7.70     | 7.80 | 7.90 |
| E  | 6.40 BSC |      |      |
| E1   | 4.30     | 4.40 | 4.50 |
| e  | 0.65 BSC |      |      |
| L  | 0.45     | 0.60 | 0.75 |
| L2   | 0.25 BSC |      |      |
| $\theta$   | 0°       | —    | 8°   |
| aaa  | 0.10     |      |      |
| bbb  | 0.10     |      |      |
| ccc  | 0.20     |      |      |
| <b>Notes:</b>  |          |      |      |
| 1. All dimensions shown are in millimeters (mm) unless otherwise noted.                                    |          |      |      |
| 2. Dimensioning and Tolerancing per ANSI Y14.5M-1994.  |          |      |      |
| 3. This drawing conforms to the JEDEC Solid State Outline MO-153, Variation AD.                            |          |      |      |
| 4. Recommended card reflow profile is per the JEDEC/IPC J-STD-020 specification for Small Body Components. |          |      |      |

## 13. 24-Pin TSSOP Land Pattern

Figure 15 illustrates the recommended land pattern for the Si2493 24-Pin TSSOP. Table 16 lists the values for the dimensions shown in the illustration.



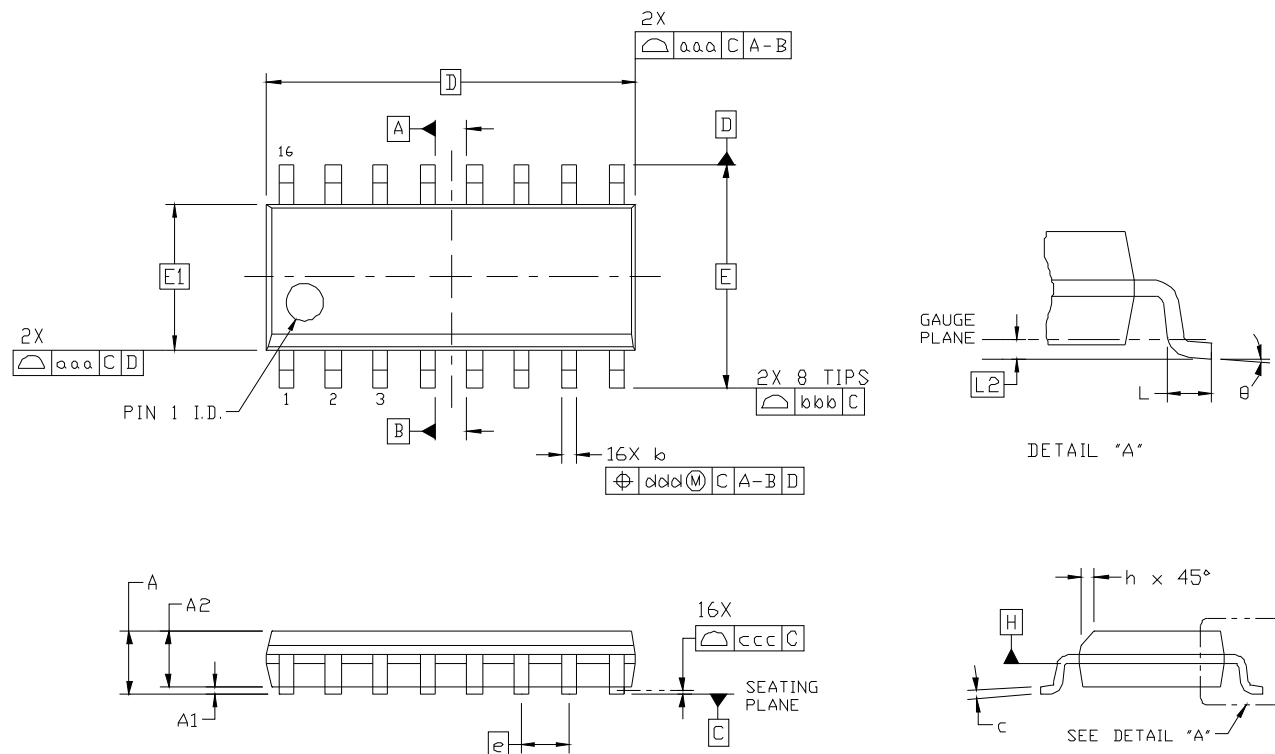
**Figure 15. 24-Pin TSSOP Land Pattern Diagram**

**Table 16. 24-Pin TSSOP PCB Land Pattern**

| Dimension   | Feature            | (mm) |
|---|--------------------|------|
| C1  | Pad Column Spacing | 5.80 |
| E   | Pad Row Pitch      | 0.65 |
| X1  | Pad Width          | 0.45 |
| Y1  | Pad Length         | 1.40 |
| <b>Notes:</b>   |                    |      |
| <ol style="list-style-type: none"> <li>1. This Land Pattern Design is based on IPC-7351 specifications for Density Level B (Median Land Protrusion).</li> <li>2. All feature sizes shown are at Maximum Material Condition (MMC) and a card fabrication tolerance of 0.05 mm is assumed.</li> </ol> |                    |      |

## 14. Package Outline: 16-Pin SOIC

Figure 16 illustrates the package details for the Si2493 16-pin packaging option. Table 17 lists the values for the dimensions shown in the illustration.



**Figure 16. 16-Pin Small Outline Integrated Circuit (SOIC) Package**

Table 17. Package Diagram Dimensions

| Dimension   | Min      | Max  |
|---|----------|------|
| A   | —        | 1.75 |
| A1  | 0.10     | 0.25 |
| A2  | 1.25     | —    |
| b   | 0.31     | 0.51 |
| c   | 0.17     | 0.25 |
| D   | 9.90 BSC |      |
| E   | 6.00 BSC |      |
| E1  | 3.90 BSC |      |
| e   | 1.27 BSC |      |
| L   | 0.40     | 1.27 |
| L2  | 0.25 BSC |      |
| h   | 0.25     | 0.50 |
| $\theta$  | 0°       | 8°   |
| aaa   | 0.10     |      |
| bbb   | 0.20     |      |
| ccc   | 0.10     |      |
| ddd   | 0.25     |      |
| <b>Notes:</b>   |          |      |
| <ol style="list-style-type: none"> <li>1. All dimensions shown are in millimeters (mm) unless otherwise noted.</li> <li>2. Dimensioning and Tolerancing per ANSI Y14.5M-1994.</li> <li>3. This drawing conforms to the JEDEC Solid State Outline MS-012, Variation AC.</li> <li>4. Recommended card reflow profile is per the JEDEC/IPC J-STD-020 specification for Small Body Components.</li> </ol> |          |      |



## 15. 16-Pin SOIC Land Pattern

Figure 17 illustrates the recommended land pattern for the Si2493 16-Pin SOIC. Table 18 lists the values for the dimensions shown in the illustration.



Figure 17. 16-Pin SOIC Land Pattern Diagram

Table 18. 16-Pin SOIC Land Pattern Dimensions

| Dimension | Feature            | (mm) |
|-----------|--------------------|------|
| C1        | Pad Column Spacing | 5.40 |
| E         | Pad Row Pitch      | 1.27 |
| X1        | Pad Width          | 0.60 |
| Y1        | Pad Length         | 1.55 |

**Notes:**

**General**

- All dimensions shown are in millimeters (mm) unless otherwise noted.
- Dimensioning and Tolerancing per ASME Y14.5M-1994.
- This Land Pattern Design is based on the IPC-7351 guidelines.
- All dimensions shown are at Maximum Material Condition (MMC). Least Material Condition (LMC) is calculated based on a Fabrication Allowance of 0.05 mm.

**Solder Mask Design**

- All metal pads are to be non-solder mask defined (NSMD). Clearance between the solder mask and the metal pad is to be 60  $\mu\text{m}$  minimum, all the way around the pad.

**Stencil Design**

- A stainless steel, laser-cut and electro-polished stencil with trapezoidal walls should be used to assure good solder paste release.
- The stencil thickness should be 0.125 mm (5 mils).
- The ratio of stencil aperture to land pad size should be 1:1.

**Card Assembly**

- A No-Clean, Type-3 solder paste is recommended.
- The recommended card reflow profile is per the JEDEC/IPC J-STD-020 specification for Small Body Components.

## DOCUMENT CHANGE LIST

### Revision 0.91 to Revision 1.0

- Updated "Features" and "Description" on page 1.
- Added Figure 5 typical schematic with 16-pin system-side option.
- Updated Table 3 to include 16-pin system-side parameters.
- Updated default register setting in Table 14.
- Added 16-pin system-side option.
- Updated "4. Functional Description" on page 18.
- Added "7. Pin Descriptions: Si2493 (16-Pin Option)" on page 46.
- Updated "10. Ordering Guide" on page 51 to reflect part revision.

### Revision 1.0 to Revision 1.1

- Added industrial temperature range devices.

### Revision 1.1 to Revision 1.2

- Added Revision D additions and changes:
  - 32.768 kHz oscillator option
  - SPI interface
- Moved U-Register details to AN93.
- Added package markings.

### Revision 1.2 to Revision 1.3

- Removed 5 V interface logic support.
- Lowered digital input voltage  $V_{IND}$  max from 5.3 V to  $(V_D + 0.3)$  V.
- Lowered total supply current (typ) from 26 to 17 mA.
- Added total supply current, wake-on-ring specification.

NOTES:

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