

COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET
Product Summary

Device	BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
Q1	30	0.4Ω @ V _{GS} = 10V	0.8A
		0.7Ω @ V _{GS} = 4.5V	0.62A
Q2	-30	0.9Ω @ V _{GS} = -10V	-0.55A
		1.7Ω @ V _{GS} = -4.5V	-0.4A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

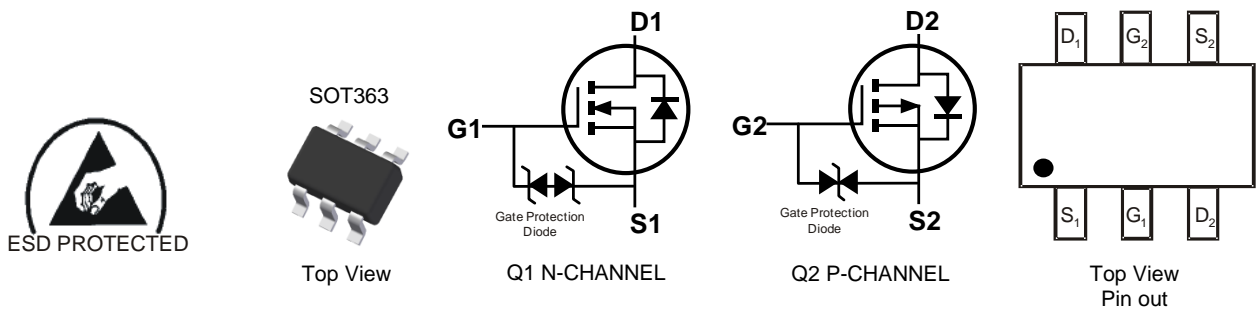
- Motor Control
- Power Management Functions
- DC-DC Converters

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- **ESD Protected Gate**
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

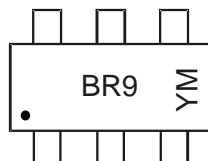
Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.027 grams (Approximate)


Ordering Information (Note 4)

Part Number	Case	Packaging
DMC3401LDW-7	SOT363	3000/Tape & Reel
DMC3401LDW-13	SOT363	10000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information


BR9 = Product Type Marking Code
 YM = Date Code Marking
 Y or \bar{Y} or \underline{Y} = Year (ex: G = 2019)
 M = Month (ex: 9 = September)

Date Code Key

Year	2018	2019	2020	2021	2022	2023	2024	2025
Code	F	G	H	I	J	K	L	M

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value_Q1	Value_Q2	Unit
Drain-Source Voltage			V _{DSS}	30	-30	V
Gate-Source Voltage			V _{GSS}	±20	±20	V
Continuous Drain Current (Note 6) Q1: V _{GS} = 10V Q2: V _{GS} = -10V	Steady State	T _A = +25°C	I _D	0.8	-0.55	A
		T _A = +70°C		0.6	-0.44	
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	0.4	-0.38	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	4	-2.4	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P _D	0.29	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{θJA}	433	°C/W
Total Power Dissipation (Note 6)		P _D	0.4	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	301	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics – N Channel – Q1 (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	V _{GS} = 0V, I _D = 250µA
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1.0	µA	V _{DS} = 30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±10	µA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.8	1.2	1.6	V	V _{DS} = V _{GS} , I _D = 250µA
Static Drain-Source On-Resistance	R _{DS(ON)}	-	0.2	0.4	Ω	V _{GS} = 10V, I _D = 0.59A
		-	0.3	0.7		V _{GS} = 4.5V, I _D = 0.2A
Diode Forward Voltage	V _{SD}	-	0.7	1.2	V	V _{GS} = 0V, I _S = 0.1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{ISS}	-	50	-	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{OSS}	-	12	-	pF	
Reverse Transfer Capacitance	C _{RSS}	-	10	-	pF	
Gate Resistance	R _g	-	58	-	Ω	V _{DS} = V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	-	0.5	-	nC	V _{DS} = 10V, I _D = 250mA
Total Gate Charge (V _{GS} = 10V)	Q _g	-	1.2	-	nC	
Gate-Source Charge	Q _{gs}	-	0.2	-	nC	
Gate-Drain Charge	Q _{gd}	-	0.1	-	nC	
Turn-On Delay Time	t _{D(ON)}	-	3.5	-	ns	V _{GS} = 10V, V _{DS} = 30V, I _D = 100mA, R _G = 25Ω
Turn-On Rise Time	t _R	-	3.3	-	ns	
Turn-Off Delay Time	t _{D(OFF)}	-	16.8	-	ns	
Turn-Off Fall Time	t _F	-	13.8	-	ns	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

Electrical Characteristics – P Channel – Q2 (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30	-	-	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	-	-	-1	μA	V _{DS} = -24V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±10	μA	V _{GS} = ±16V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-1	-2.2	-2.6	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	-	0.5	0.9	Ω	V _{GS} = -10V, I _D = -0.42A
		-	0.78	1.7		V _{GS} = -4.5V, I _D = -0.2A
Diode Forward Voltage	V _{SD}	-	-0.8	-1.2	V	V _{GS} = 0V, I _S = -0.23A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	-	19	-	pF	V _{DS} = -15V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	-	16	-	pF	
Reverse Transfer Capacitance	C _{rss}	-	3	-	pF	
Gate Resistance	R _g	-	4.4	-	kΩ	V _{DS} = V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = -4.5V)	Q _g	-	0.36	-	nC	V _{DS} = -10V, I _D = -0.24A
Total Gate Charge (V _{GS} = -10V)	Q _g	-	0.8	-	nC	
Gate-Source Charge	Q _{gs}	-	0.1	-	nC	
Gate-Drain Charge	Q _{gd}	-	0.1	-	nC	
Turn-On Delay Time	t _{D(ON)}	-	3.3	-	ns	V _{GS} = -10V, V _{DD} = -15V, I _D = -0.5A, R _G = 1Ω
Turn-On Rise Time	t _R	-	2.3	-	ns	
Turn-Off Delay Time	t _{D(OFF)}	-	406	-	ns	
Turn-Off Fall Time	t _F	-	237	-	ns	

Notes: 7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

Typical Characteristics - N-CHANNEL

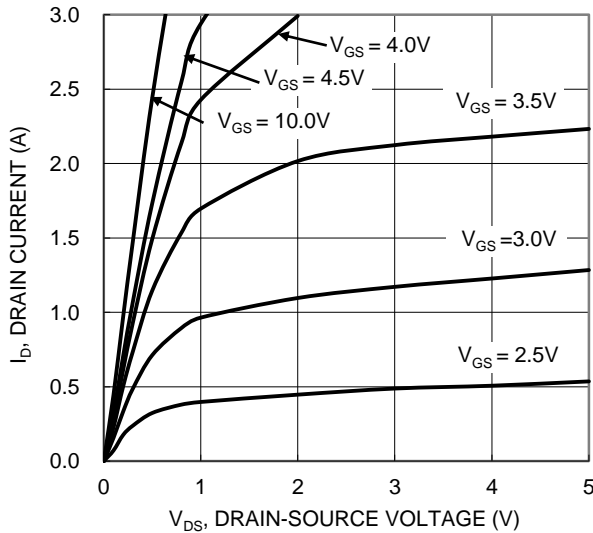


Figure 1. Typical Output Characteristic

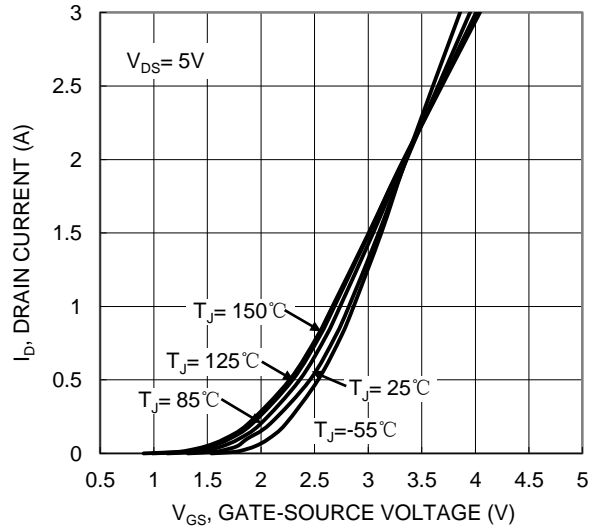


Figure 2. Typical Transfer Characteristic

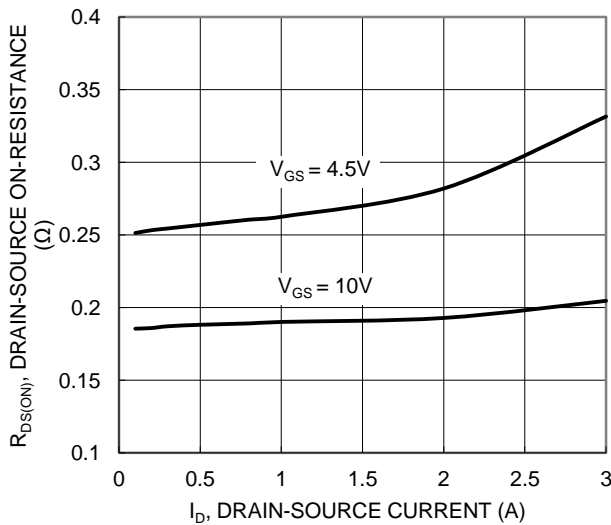


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

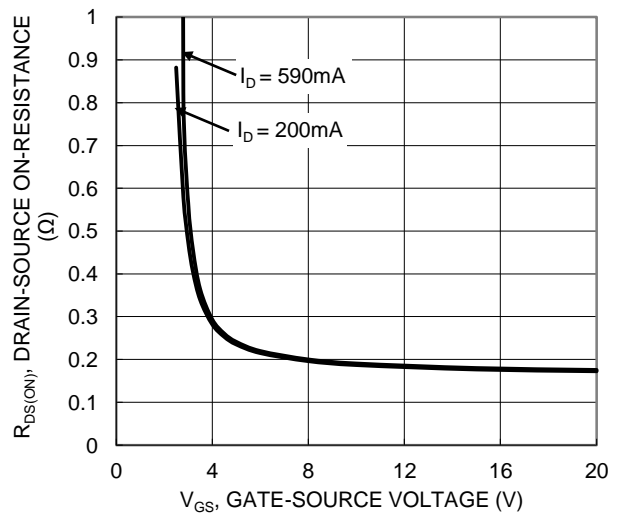


Figure 4. Typical Transfer Characteristic

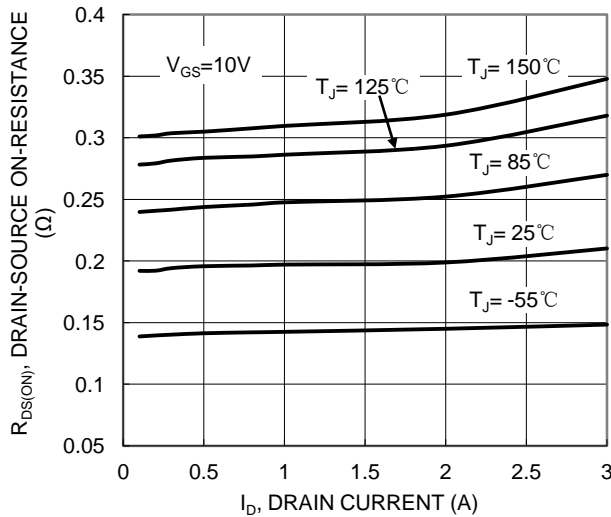


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

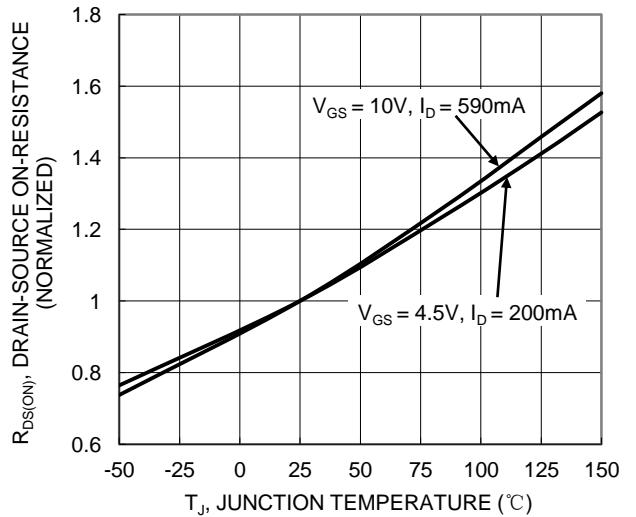


Figure 6. On-Resistance Variation with Temperature

Typical Characteristics - N-CHANNEL (Cont.)

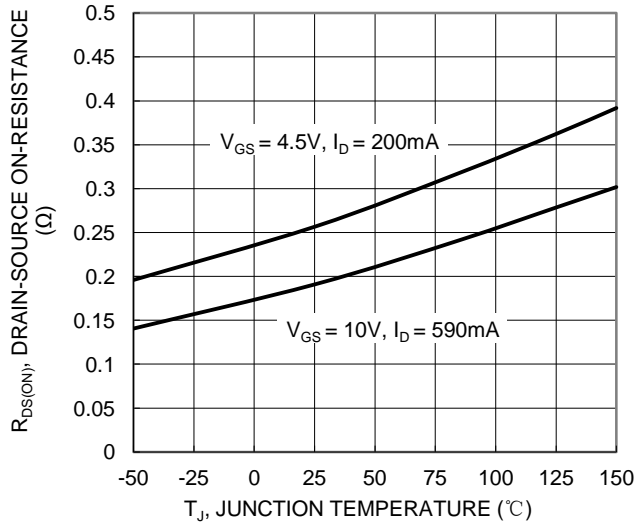


Figure 7. On-Resistance Variation with Temperature

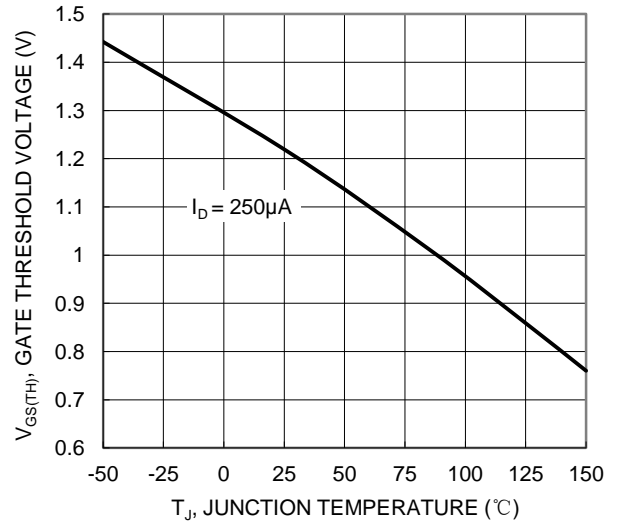


Figure 8. Gate Threshold Variation vs. Junction Temperature

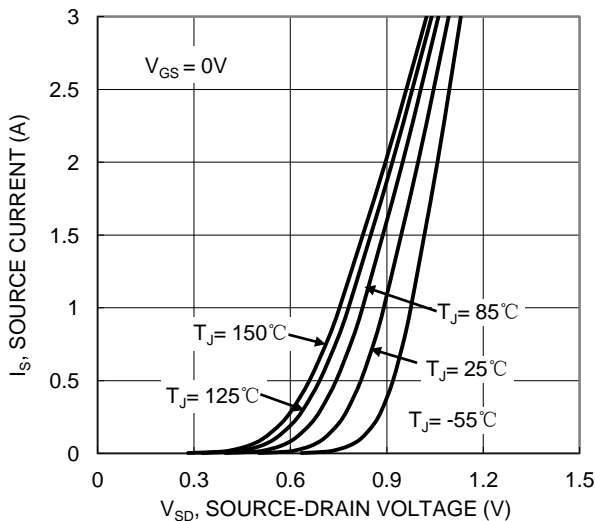


Figure 9. Diode Forward Voltage vs. Current

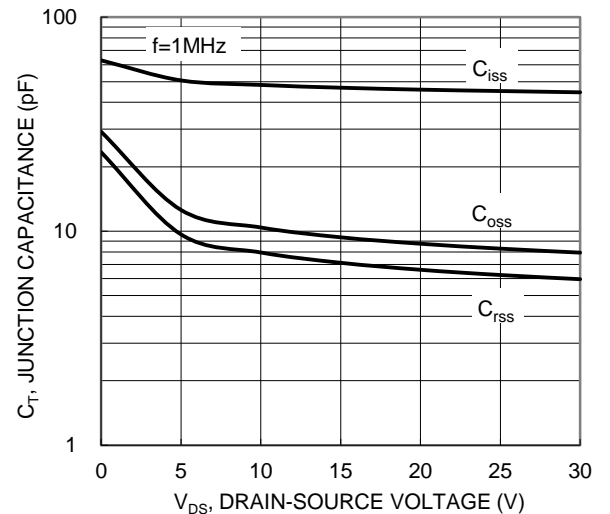


Figure 10. Typical Junction Capacitance

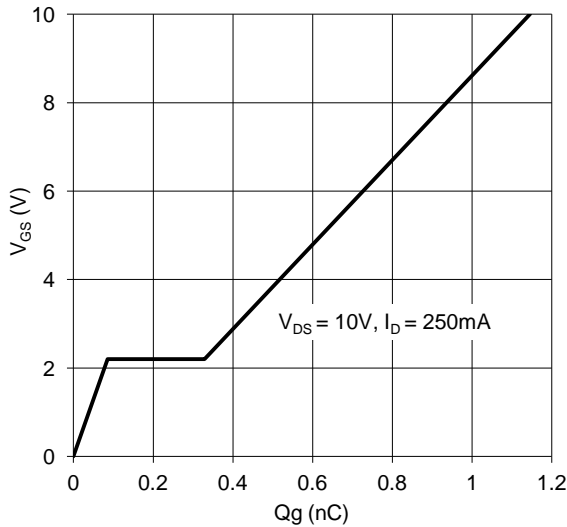


Figure 11. Gate Charge

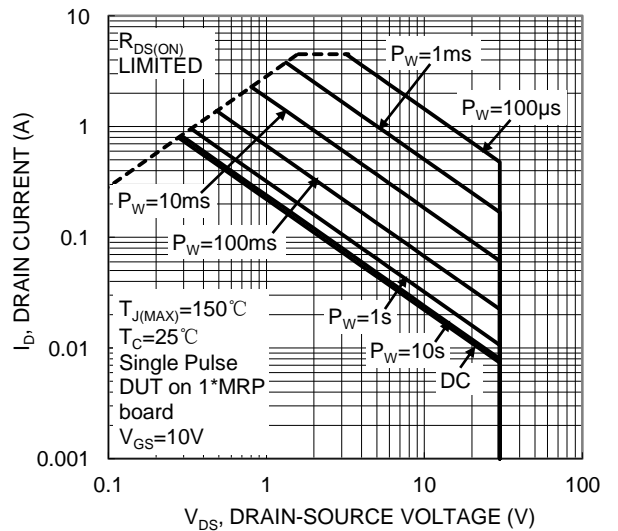


Figure 12. SOA, Safe Operation Area

Typical Characteristics - P-CHANNEL

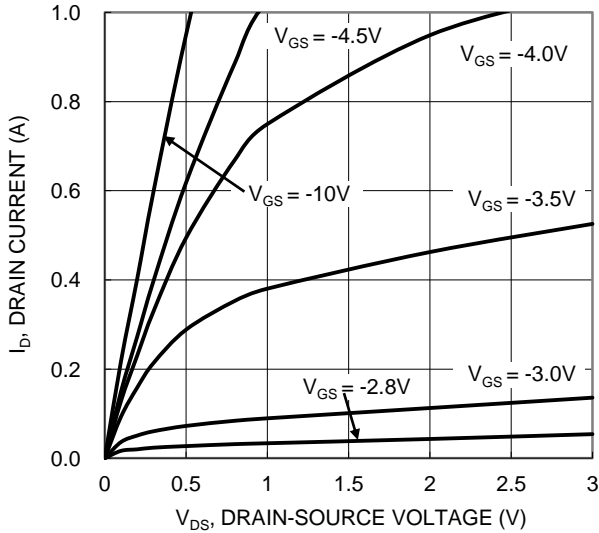


Figure 13. Typical Output Characteristic

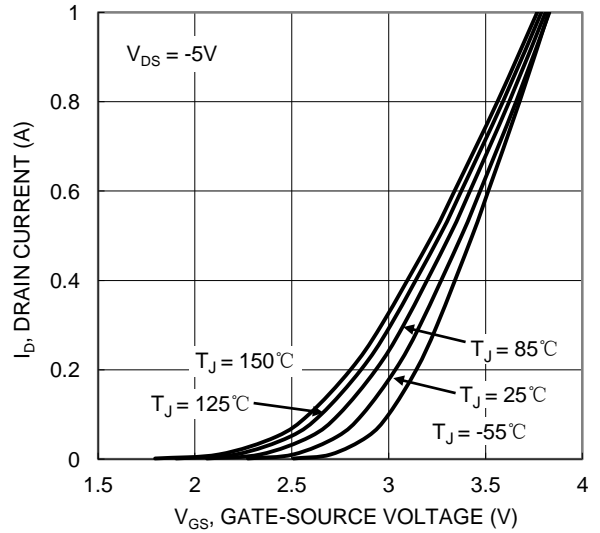


Figure 14. Typical Transfer Characteristic

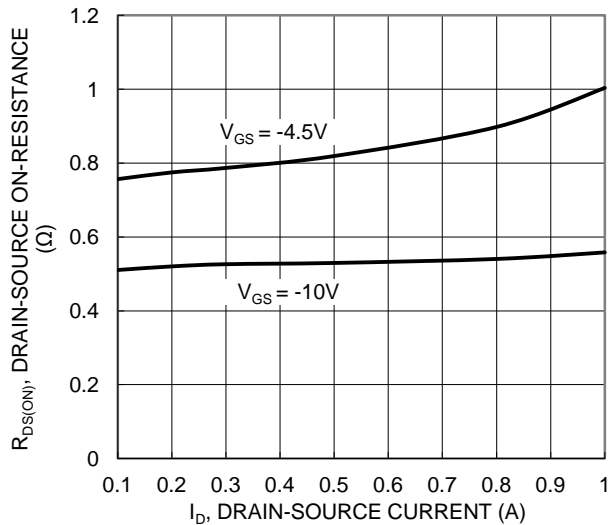


Figure 15. Typical On-Resistance vs. Drain Current and Gate Voltage

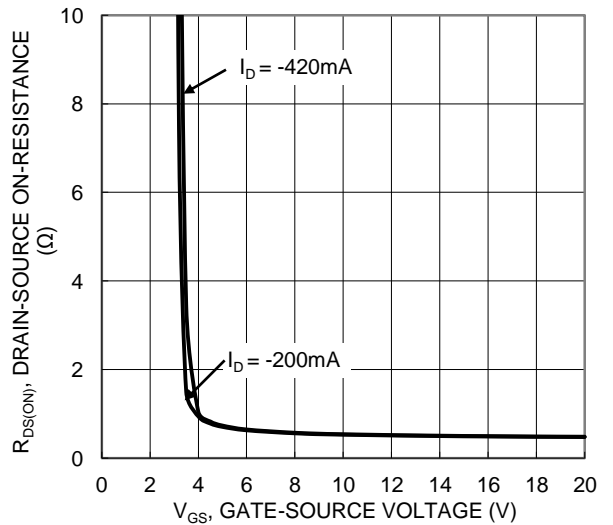


Figure 16. Typical Transfer Characteristic

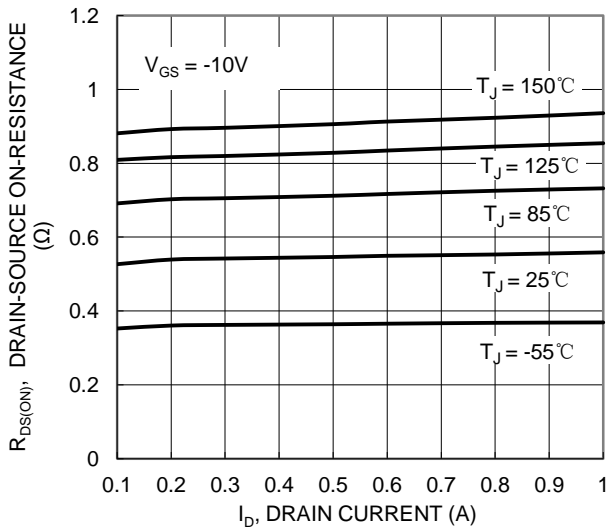


Figure 17. Typical On-Resistance vs. Drain Current and Junction Temperature

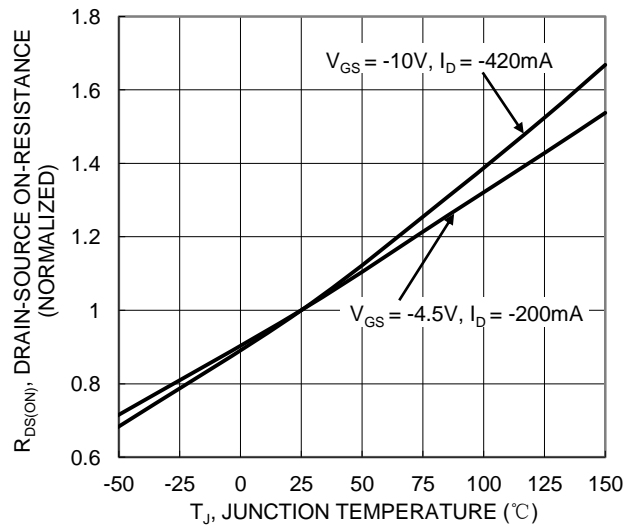


Figure 18. On-Resistance Variation with Junction Temperature

Typical Characteristics - P-CHANNEL (Cont.)

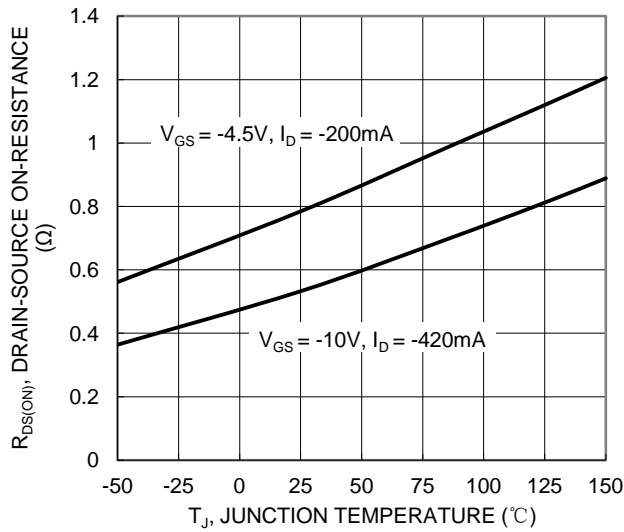


Figure 19. On-Resistance Variation with Junction Temperature

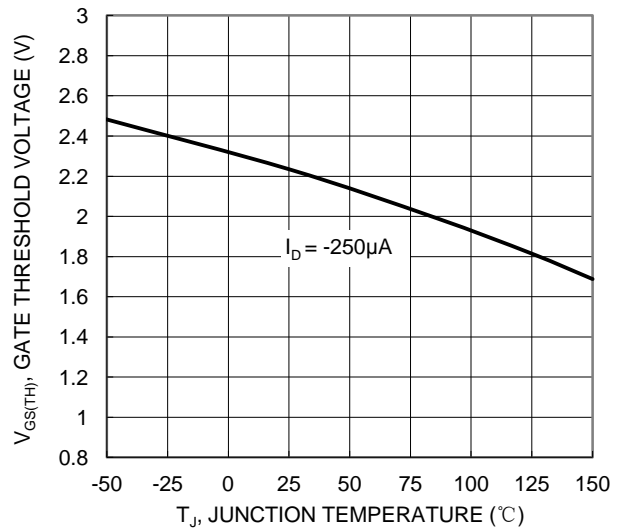


Figure 20. Gate Threshold Variation vs. Junction Temperature

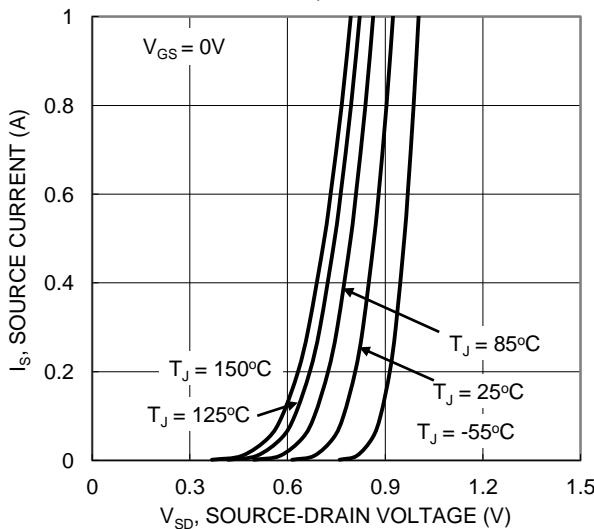


Figure 21. Diode Forward Voltage vs. Current

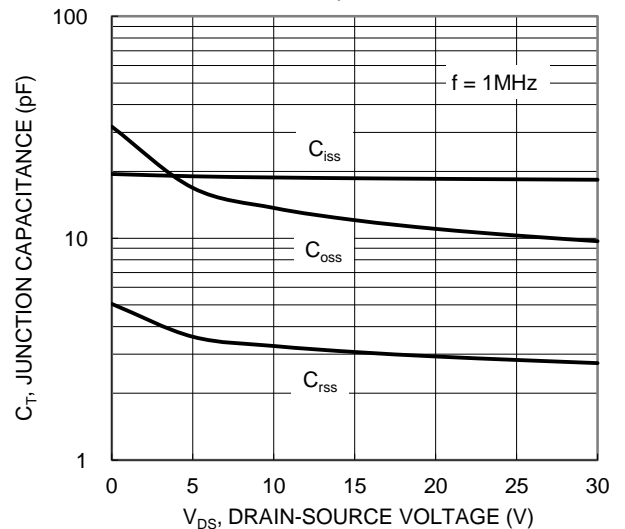


Figure 22. Typical Junction Capacitance

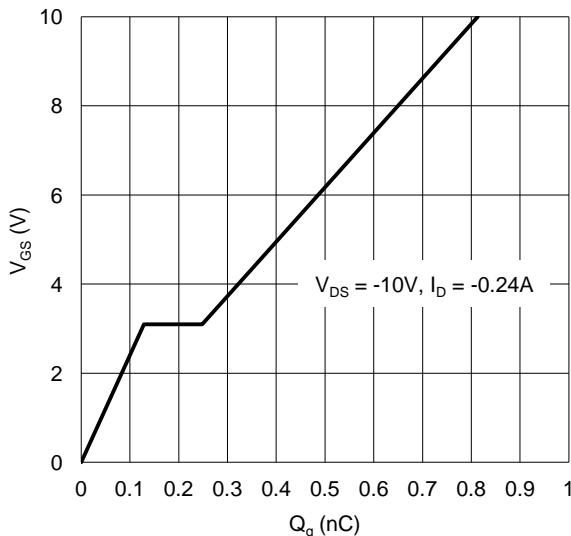


Figure 23. Gate Charge

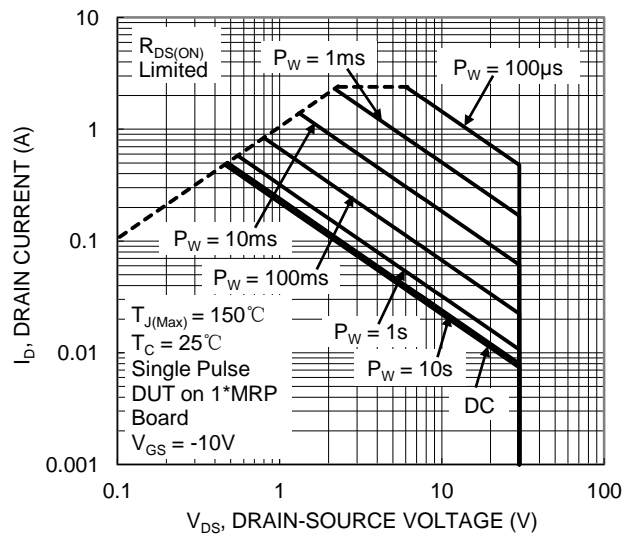


Figure 24. SOA, Safe Operation Area

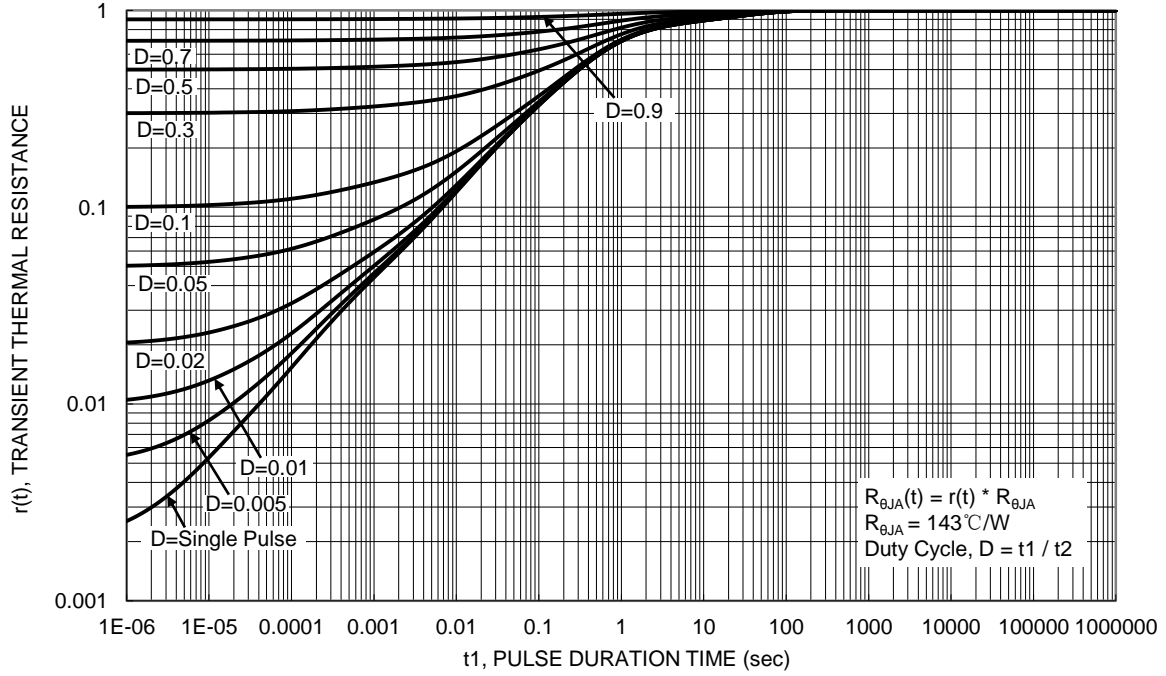
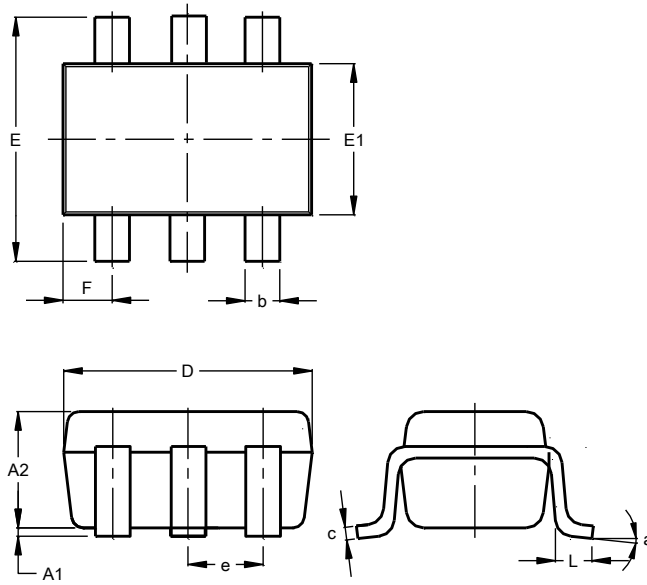


Figure 25. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT363

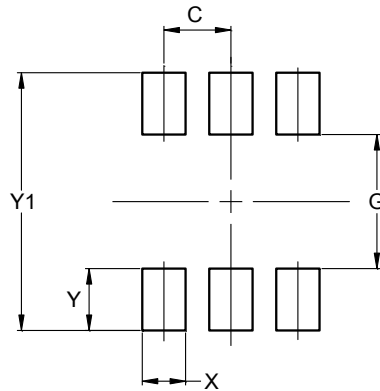


SOT363			
Dim	Min	Max	Typ
A1	0.00	0.10	0.05
A2	0.90	1.00	0.95
b	0.10	0.30	0.25
c	0.10	0.22	0.11
D	1.80	2.20	2.15
E	2.00	2.20	2.10
E1	1.15	1.35	1.30
e	0.650 BSC		
F	0.40	0.45	0.425
L	0.25	0.40	0.30
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT363



Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.420
Y	0.600
Y1	2.500

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