
Programmable USB Power Delivery Controller

Highlights

- Integrated USB Power Delivery (PD) PHY
- Support for Power Delivery Message Protocol
- Integrated Voltage and Current ADC Inputs
- Configuration Profile Selection
- On-chip Microcontroller
- SPI Interface
- Commercial, Industrial, and Automotive Grade Temperature Support
- Available in 28-TSSOP and 32-SQFN Packages

Target Applications

- AC Adapters & Chargers
 - Type-A
 - Type-B
 - Micro-A
 - Micro-B
 - Captive cable

Key Benefits

- Integrated USB Power Delivery (PD) PHY
 - Integrated receive termination
 - Requires minimal external components
- Support for Power Delivery Message Protocol
 - Message Generation/Consumption
 - Retry Generation
 - Error Handling
 - State Behavior
- Cable Detect Logic
 - Cable attachment type
- CFG_SEL pins allow selection of multiple profiles
 - Provider
 - Consumer/Provider
- Integrated Voltage (VMON) and Current (IMON) ADC Inputs
- Dead Battery Support
- On-chip Microcontroller
 - Manages I/Os and other signals
 - Implements power delivery policy engine and device policy manager
- Configuration Programming via OTP, or Vendor Defined Messaging
- Supports Low Power Modes
- Serial Peripheral Interface (SPI) Bus
- Internal 3.3 V and 1.8 V Voltage Regulators
- Integrated Oscillator Reduces BOM Costs
- Package
 - 28-pin TSSOP (9.7 x 6.1 mm)
 - 32-pin SQFN (5 x 5 mm)
- Environmental
 - Commercial temperature range (0°C to +70°C)
 - Industrial temperature range (-40°C to +85°C)
 - Automotive temperature range (-40°C to +105°C)

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1.0 INTRODUCTION

1.1 General Description

The UPD1001 is a programmable USB Power Delivery (PD) controller designed to adhere to the *USB Power Delivery Specification*. USB Power Delivery allows a host (or device) to provide or consume up to 5 Amps and/or up to 20 Volts of power from a USB PD capable partner device on the other end of the USB cable. USB PD capable standard and custom cables/connectors are supported, which in most cases are backward compatible with standard USB connections.

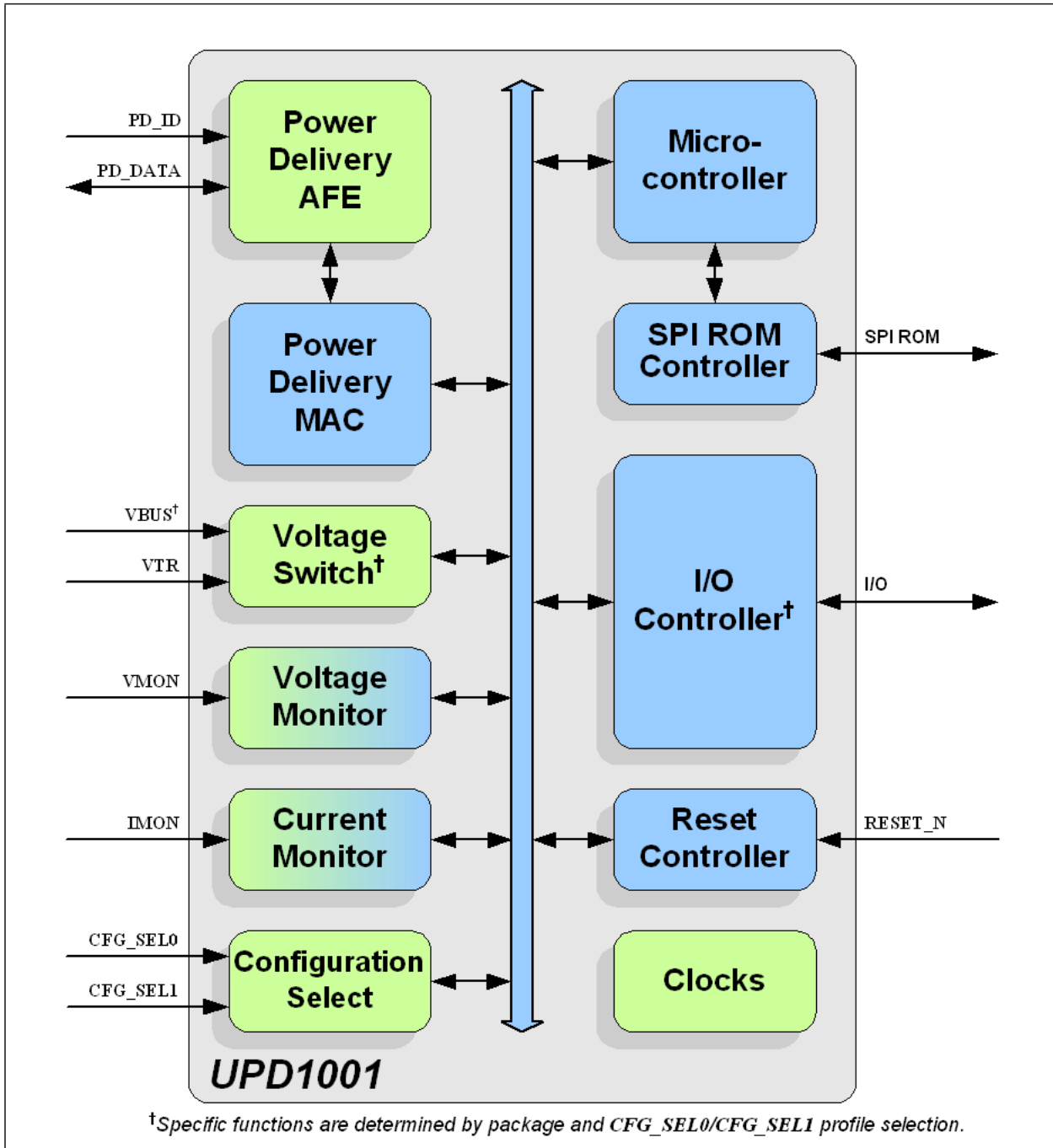
The UPD1001 provides a complete USB Power Delivery solution for all charger and adapter solutions. The functionality of the UPD1001 is selected via two configuration selection pins, **CFG_SEL0** and **CFG_SEL1**, which can be used to select unique PD and system configurations. Designing the UPD1001 into a system can be as simple as selecting a configuration, with no external EEPROM required. Advanced programmability options exist with an external EEPROM installed.

The integrated USB Power Delivery MAC and PHY support provider and consumer operation via the PD communication protocol, as specified in Revision 1.0 (Version 1.2) of the *USB Power Delivery Specification*. Monitoring of VBUS and battery charging is accomplished via the integrated voltage and current ADC inputs. The PHY supports cable ID detection/identification and loopback modes. The PHY includes a 24MHz FSK modulator/demodulator and provides integrated terminations. The USB PD MAC supports both USB PD insertion detection (cold socket) and dead battery cases.

The on-chip microcontroller manages the IOs and implements the power delivery local policy engine and device manager. The SPI ROM controller is used by the microcontroller for optional external code execution from ROM. A One Time Programmable (OTP) ROM is integrated in the UPD1001. Integrated 3.3 V and 1.8 V regulators allow device operation from a single power supply. The UPD1001 is available in commercial (0°C to +70°C), industrial (-40°C to +85°C), and automotive (-40°C to +105°C) temperature ranges. An internal block diagram of the UPD1001 is shown in [Figure 1-1](#).

UPD1001

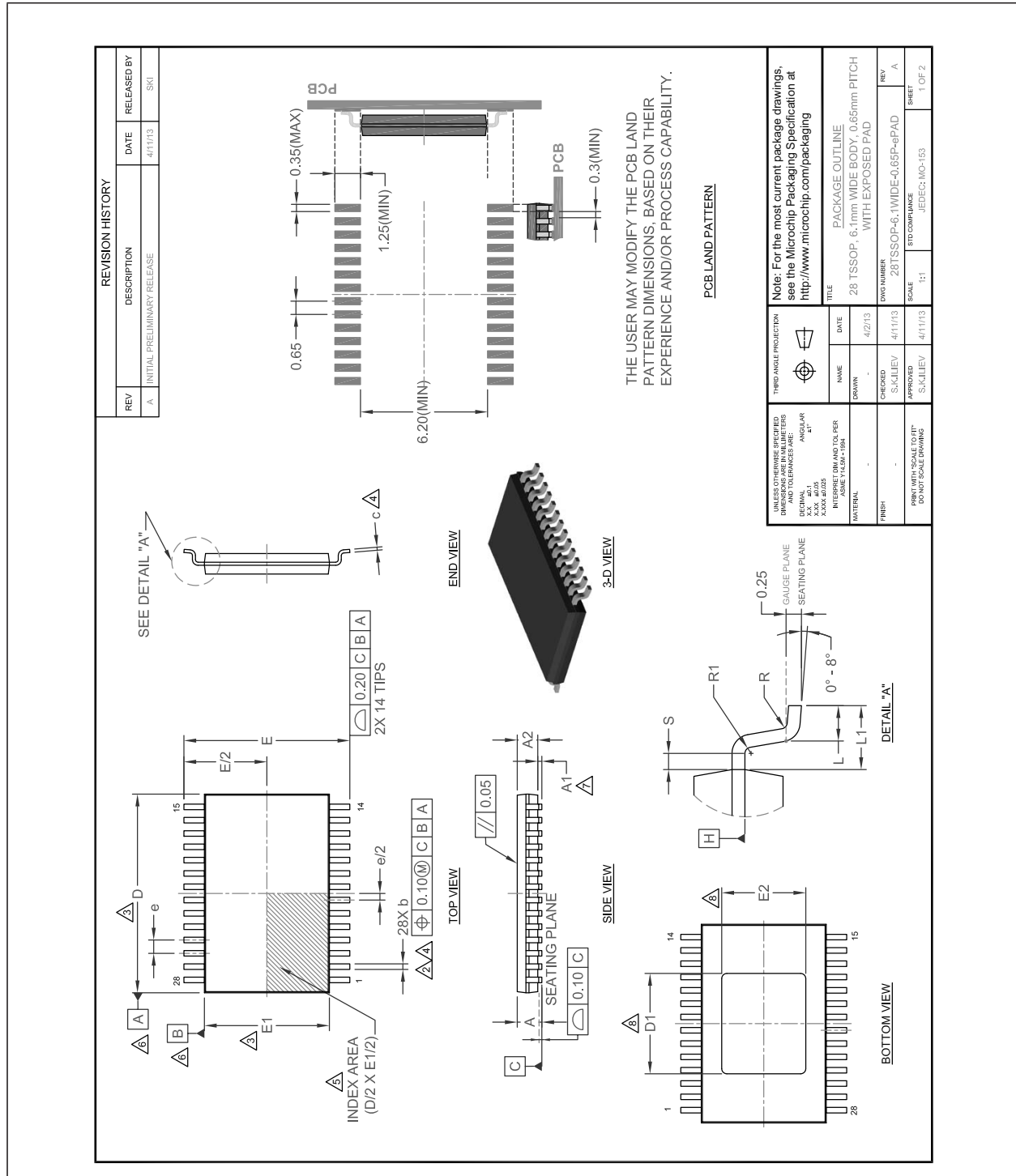
FIGURE 1-1: INTERNAL BLOCK DIAGRAM



2.0 PACKAGE OUTLINES

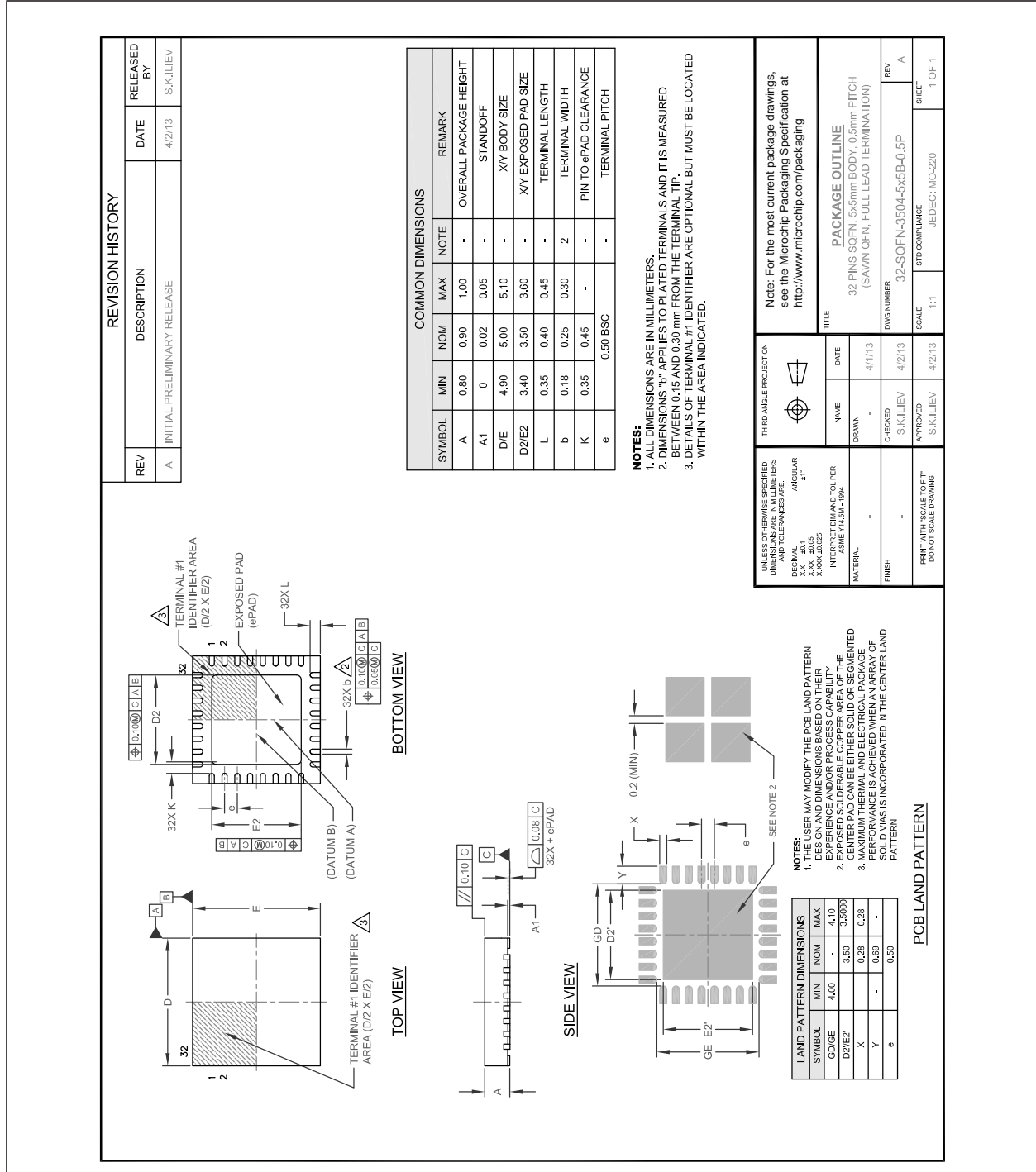
2.1 28-TSSOP

FIGURE 2-1: 28-TSSOP PACKAGE (DRAWING)



2.2 32-SQFN

FIGURE 2-3: 32-SQFN PACKAGE



UPD1001

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| Device | Tape and Reel Option | | Temperature Range | | Package | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 15%;">Device:</td> <td colspan="5">UPD1001</td> </tr> <tr> <td>Tape and Reel Option:</td> <td>Blank</td> <td>=</td> <td colspan="3">Standard packaging (tray)</td> </tr> <tr> <td></td> <td>T</td> <td>=</td> <td colspan="3">Tape and Reel^(Note 1)</td> </tr> <tr> <td>Temperature Range:</td> <td>A</td> <td>=</td> <td>0°C to +70°C</td> <td colspan="2">(Commercial)</td> </tr> <tr> <td></td> <td>AI</td> <td>=</td> <td>-40°C to +85°C</td> <td colspan="2">(Industrial)</td> </tr> <tr> <td></td> <td>AV</td> <td>=</td> <td>-40°C to +105°C</td> <td colspan="2">(Automotive)</td> </tr> <tr> <td>Package:</td> <td>ST</td> <td>=</td> <td colspan="3">28-pin TSSOP</td> </tr> <tr> <td></td> <td>MQ</td> <td>=</td> <td colspan="3">32-pin SQFN</td> </tr> </table> | | | | | | Device: | UPD1001 | | | | | Tape and Reel Option: | Blank | = | Standard packaging (tray) | | | | T | = | Tape and Reel ^(Note 1) | | | Temperature Range: | A | = | 0°C to +70°C | (Commercial) | | | AI | = | -40°C to +85°C | (Industrial) | | | AV | = | -40°C to +105°C | (Automotive) | | Package: | ST | = | 28-pin TSSOP | | | | MQ | = | 32-pin SQFN | | |
| Device: | UPD1001 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
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| | AI | = | -40°C to +85°C | (Industrial) | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | AV | = | -40°C to +105°C | (Automotive) | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
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| | MQ | = | 32-pin SQFN | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| <p>Examples:</p> <p>a) UPD1001-A/ST Tray, Commercial temp., 28-pin TSSOP</p> <p>b) UPD1001T-AI/MQ Tape & reel, Industrial temp., 32-pin SQFN</p> | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
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