

## Product Summary

| BV <sub>DSS</sub> | R <sub>DS(ON)</sub> Max        | I <sub>D</sub> Max<br>T <sub>A</sub> = +25°C |
|-------------------|--------------------------------|--|
| -20V              | 1.9Ω @ V <sub>GS</sub> = -4.5V | -530mA                                       |
|                   | 2.4Ω @ V <sub>GS</sub> = -2.5V | -471mA                                       |
|                   | 3.4Ω @ V <sub>GS</sub> = -1.8V | -397mA                                       |
|                   | 5.0Ω @ V <sub>GS</sub> = -1.5V | -328mA                                       |

## Description

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## Applications

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

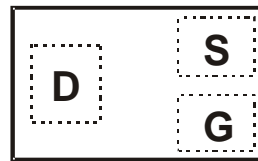
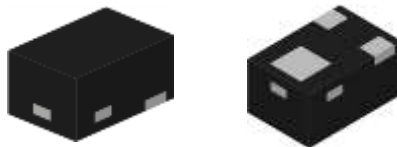
## Features and Benefits

- Low Package Profile
- 0.6mm x 0.4mm Package Footprint
- Low On-Resistance
- Very Low Gate Threshold Voltage: -1.0V Max
- **ESD Protected Gate**
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 standards for High Reliability**

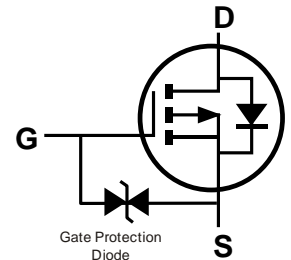
## Mechanical Data

- Case: X2-DFN0604-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e3)
- Weight: 0.001 grams (Approximate)

X2-DFN0604-3



Top View  
Package Pin Configuration



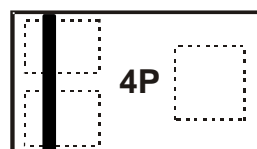
Equivalent Circuit

## Ordering Information (Note 4)

| Part Number   | Case         | Packaging          |
|---------------|--------------|--------------------|
| DMP22D4UFO-7B | X2-DFN0604-3 | 10,000/Tape & Reel |

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

## Marking Information



Top View  
Bar Denotes Gate  
and Source Side

4P = Product Type Marking Code

**Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

| Characteristic  |              |                           | Symbol    | Value   | Unit |
|---|--------------|---------------------------|-----------|---------|------|
| Drain-Source Voltage                                      |              |                           | $V_{DSS}$ | -20     | V    |
| Gate-Source Voltage                                       |              |                           | $V_{GSS}$ | $\pm 8$ | V    |
| Continuous Drain Current (Note 5) $V_{GS} = -4.5\text{V}$ | Steady State | $T_A = +25^\circ\text{C}$ | $I_D$     | -530    | mA   |
|   |              | $T_A = +85^\circ\text{C}$ |           | -383    |      |
| Pulsed Drain Current (Note 6)                             |              |                           | $I_{DM}$  | -0.6    | mA   |

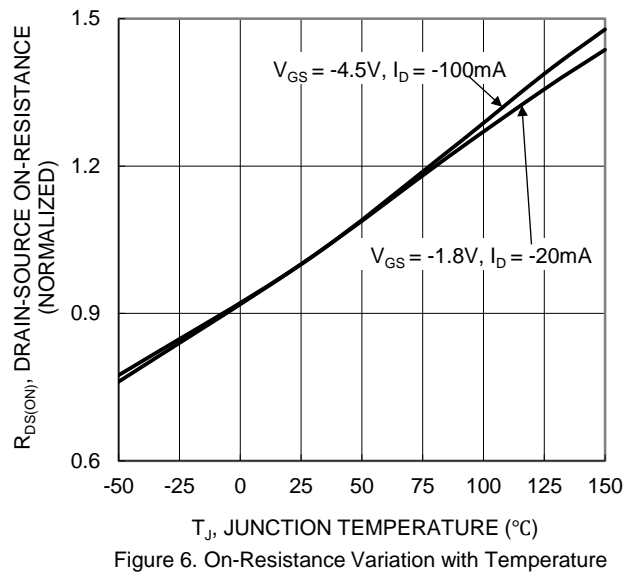
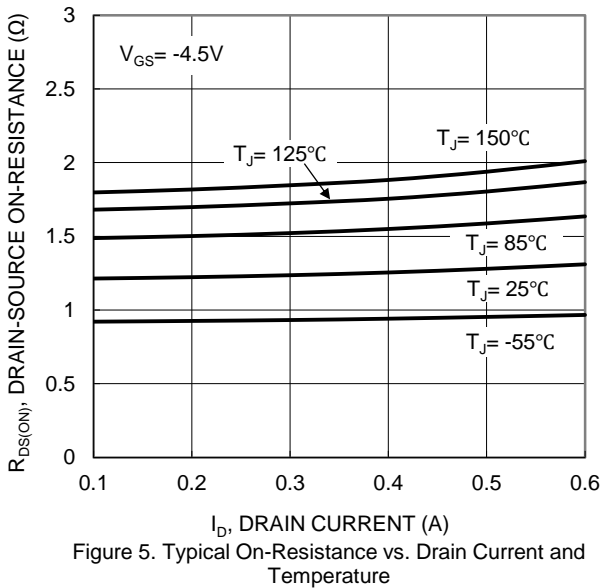
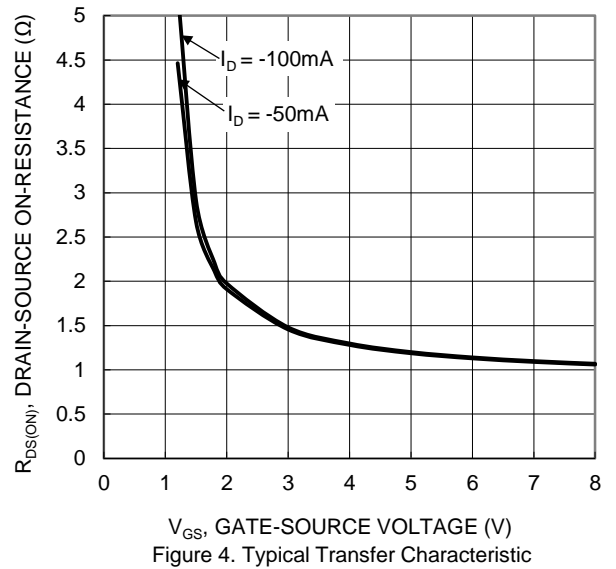
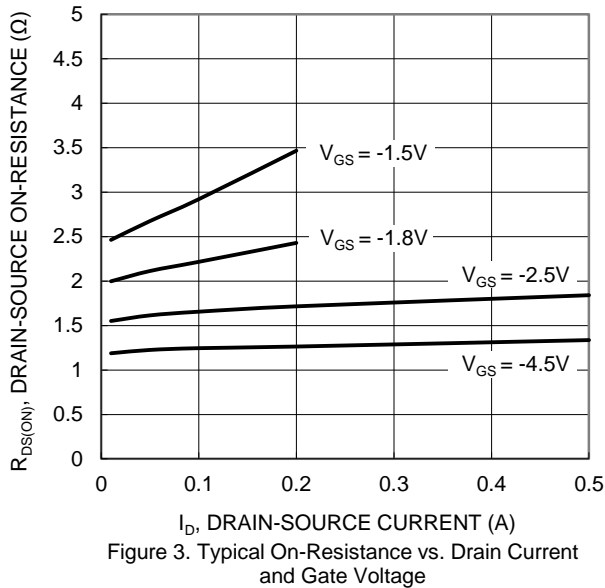
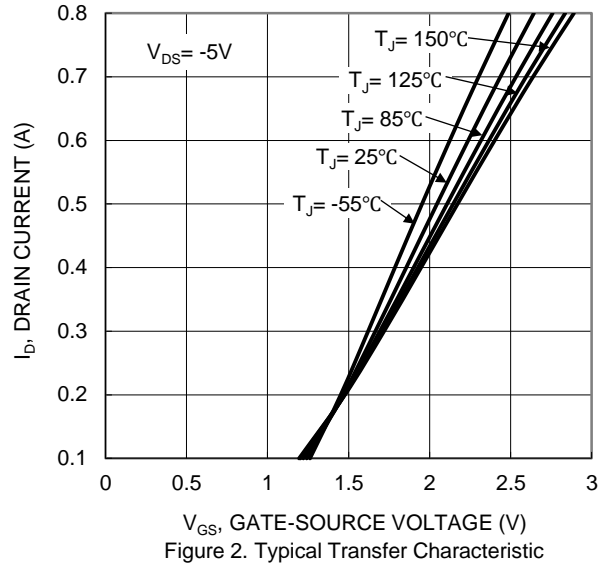
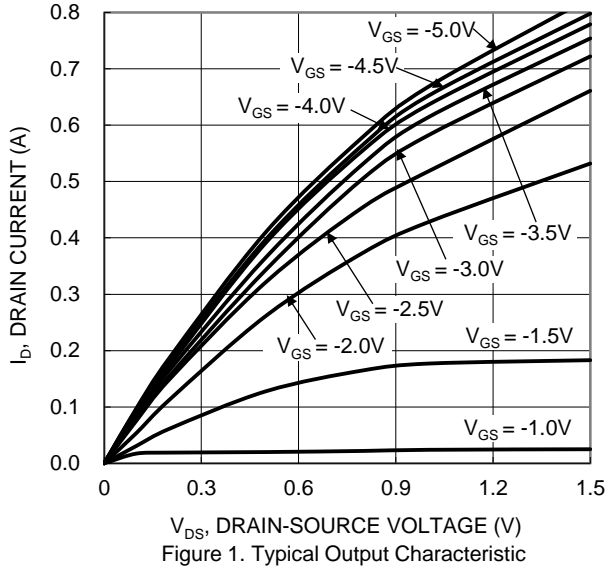
**Thermal Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

| Characteristic                                   |              |  | Symbol          | Value       | Unit               |
|--|--------------|--|-----------------|-------------|--------------------|
| Total Power Dissipation (Note 5)                 | Steady State |  | $P_D$           | 820         | mW                 |
| Thermal Resistance, Junction to Ambient (Note 5) | Steady State |  | $R_{\theta JA}$ | 155         | $^\circ\text{C/W}$ |
| Operating and Storage Temperature Range          |              |  | $T_J, T_{STG}$  | -55 to +150 | $^\circ\text{C}$   |

**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

| Characteristic                          | Symbol       | Min  | Typ  | Max      | Unit          | Test Condition  |
|---|--------------|------|------|----------|---------------|---|
| <b>OFF CHARACTERISTICS</b> (Note 7)     |              |      |      |          |               |   |
| Drain-Source Breakdown Voltage          | $BV_{DSS}$   | -20  | —    | —        | V             | $V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$                                       |
| Zero Gate Voltage Drain Current         | $I_{DSS}$    | —    | —    | -1       | $\mu\text{A}$ | $V_{DS} = -16\text{V}, V_{GS} = 0\text{V}$  |
| Gate-Source Leakage                     | $I_{GSS}$    | —    | —    | $\pm 10$ | $\mu\text{A}$ | $V_{GS} = \pm 5\text{V}, V_{DS} = 0\text{V}$                                      |
| <b>ON CHARACTERISTICS</b> (Note 7)      |              |      |      |          |               |   |
| Gate Threshold Voltage                  | $V_{GS(TH)}$ | -0.4 | —    | -1.0     | V             | $V_{DS} = V_{GS}, I_D = -250\mu\text{A}$  |
| Static Drain-Source On-Resistance       | $R_{DS(ON)}$ | —    | —    | 1.9      | $\Omega$      | $V_{GS} = -4.5\text{V}, I_D = -100\text{mA}$                                      |
|   |              | —    | —    | 2.4      |               | $V_{GS} = -2.5\text{V}, I_D = -50\text{mA}$                                       |
|   |              | —    | —    | 3.4      |               | $V_{GS} = -1.8\text{V}, I_D = -20\text{mA}$                                       |
|   |              | —    | —    | 5.0      |               | $V_{GS} = -1.5\text{V}, I_D = -10\text{mA}$                                       |
| Diode Forward Voltage                   | $V_{SD}$     | —    | -0.6 | -1.1     | V             | $V_{GS} = 0\text{V}, I_S = -10\text{mA}$  |
| <b>DYNAMIC CHARACTERISTICS</b> (Note 8) |              |      |      |          |               |   |
| Input Capacitance                       | $C_{ISS}$    | —    | 28.7 | —        | pF            | $V_{DS} = -15\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$                     |
| Output Capacitance                      | $C_{OSS}$    | —    | 4.2  | —        | pF            |   |
| Reverse Transfer Capacitance            | $C_{RSS}$    | —    | 2.9  | —        | pF            |   |
| Gate Resistance                         | $R_G$        | —    | 399  | —        | $\Omega$      | $V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$                       |
| Total Gate Charge                       | $Q_G$        | —    | 0.4  | —        | nC            | $V_{GS} = -4.5\text{V}, V_{DS} = -10\text{V}, I_D = -250\text{mA}$                |
| Gate-Source Charge                      | $Q_{GS}$     | —    | 0.08 | —        | nC            |   |
| Gate-Drain Charge                       | $Q_{GD}$     | —    | 0.06 | —        | nC            |   |
| Turn-On Delay Time                      | $t_{D(ON)}$  | —    | 5.8  | —        | ns            | $V_{DD} = -15\text{V}, V_{GS} = -4.5\text{V}, R_G = 2\Omega, I_D = -200\text{mA}$ |
| Turn-On Rise Time                       | $t_R$        | —    | 5.7  | —        | ns            |   |
| Turn-Off Delay Time                     | $t_{D(OFF)}$ | —    | 31.1 | —        | ns            |   |
| Turn-Off Fall Time                      | $t_F$        | —    | 16.4 | —        | ns            |   |

- Notes:
5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
  6. Device mounted on minimum recommended pad layout test board, 10 $\mu\text{s}$  pulse duty cycle = 1%.
  7. Short duration pulse test used to minimize self-heating effect.
  8. Guaranteed by design. Not subject to product testing.



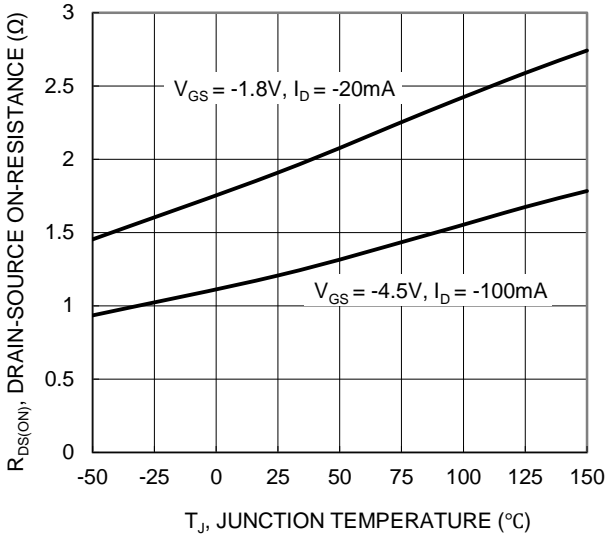


Figure 7. On-Resistance Variation with Temperature

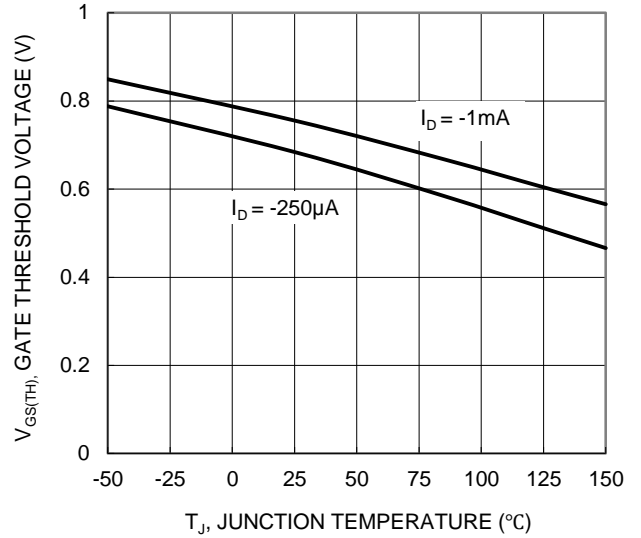


Figure 8. Gate Threshold Variation vs. Junction Temperature

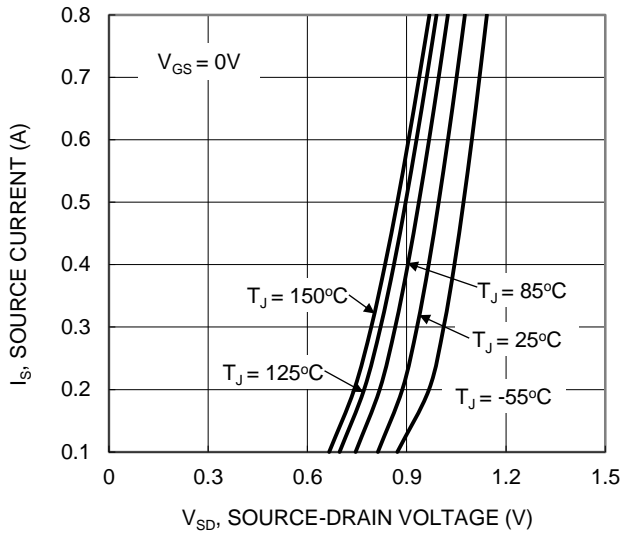


Figure 9. Diode Forward Voltage vs. Current

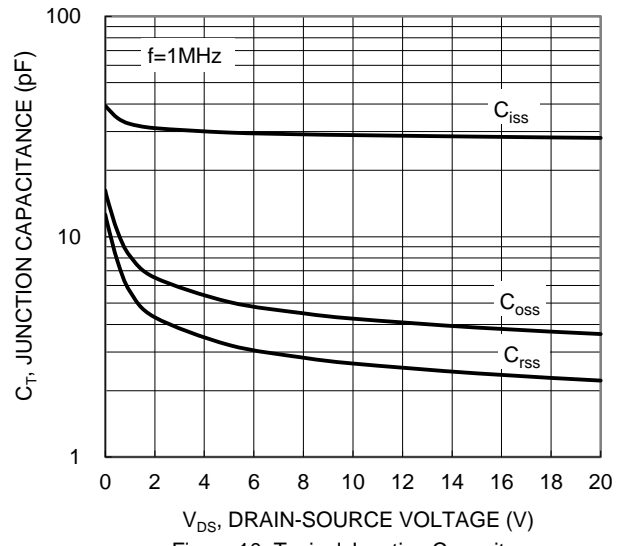


Figure 10. Typical Junction Capacitance

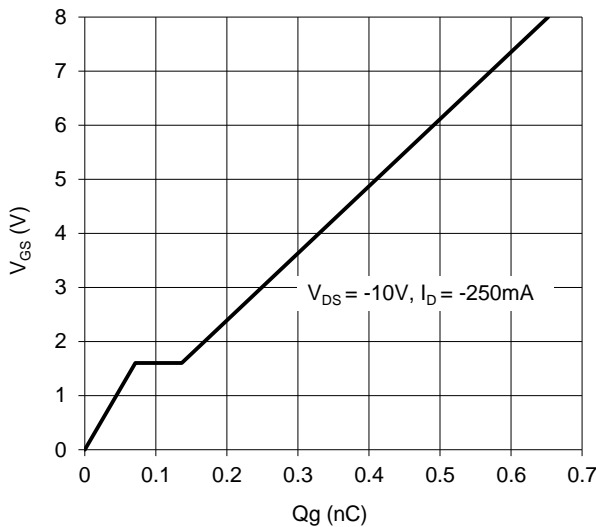


Figure 11. Gate Charge

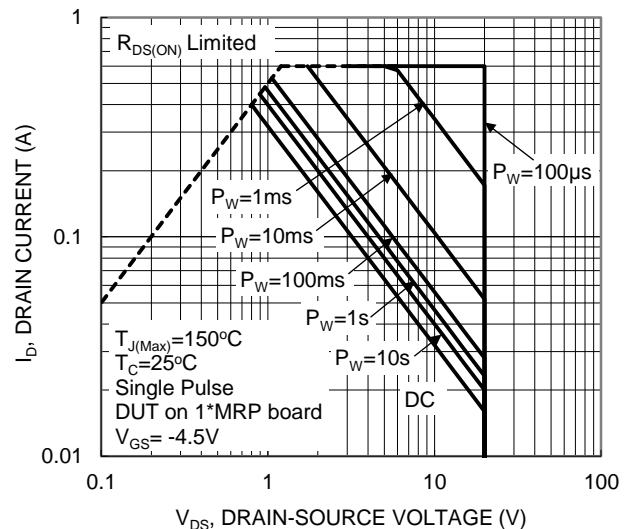


Figure 12. SOA, Safe Operation Area

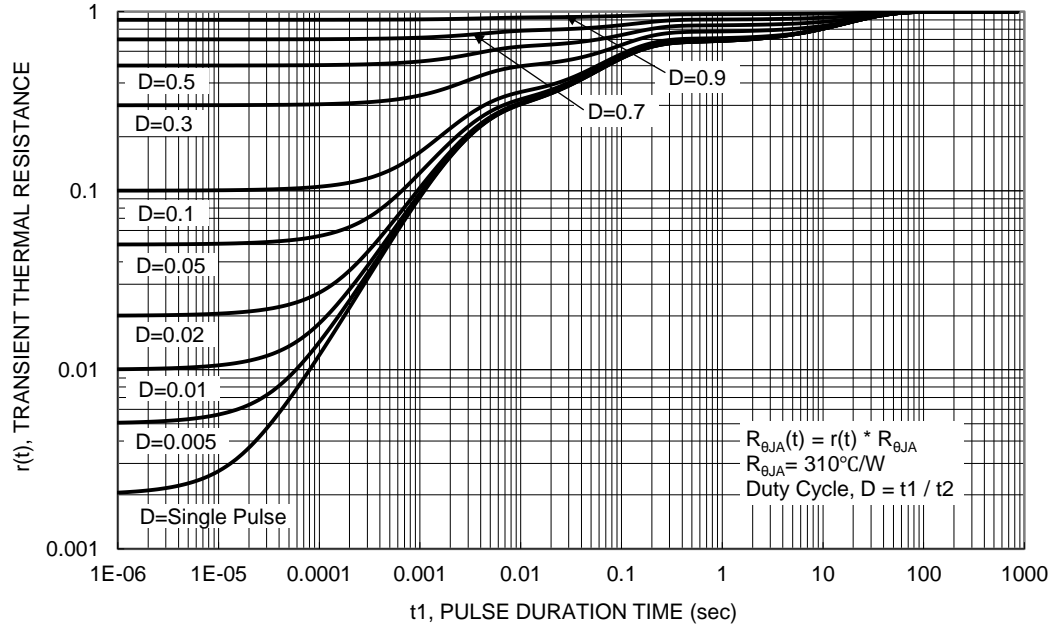
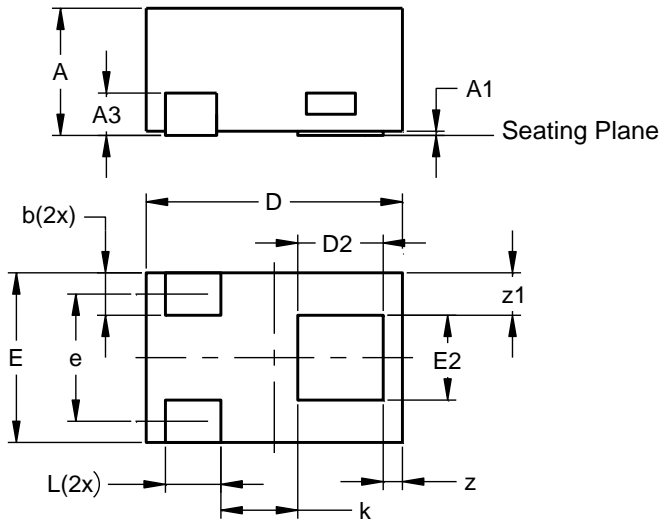


Figure 13. Transient Thermal Resistance

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**X2-DFN0604-3**

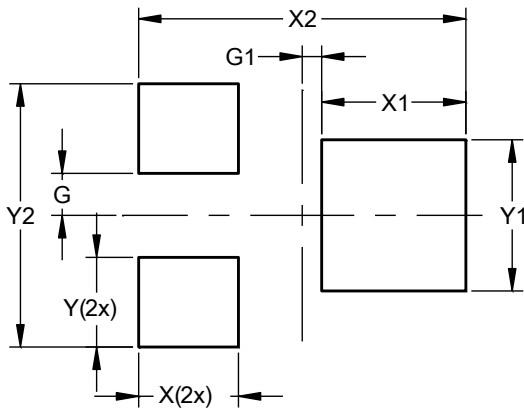


| X2-DFN0604-3         |      |      |       |
|----------------------|------|------|-------|
| Dim                  | Min  | Max  | Typ   |
| A                    | --   | 0.40 | 0.36  |
| A1                   | 0.00 | 0.03 | 0.02  |
| A3                   | --   | --   | 0.10  |
| b                    | 0.07 | 0.15 | 0.10  |
| D                    | 0.55 | 0.65 | 0.60  |
| D2                   | 0.15 | 0.25 | 0.20  |
| E                    | 0.35 | 0.45 | 0.40  |
| E2                   | 0.15 | 0.25 | 0.20  |
| e                    | --   | --   | 0.30  |
| k                    | 0.15 | --   | --    |
| L                    | 0.10 | 0.18 | 0.13  |
| z                    | --   | --   | 0.045 |
| z1                   | --   | --   | 0.10  |
| All Dimensions in mm |      |      |       |

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**X2-DFN0604-3**



| Dimensions | Value (in mm) |
|------------|---------------|
| G          | 0.075         |
| G1         | 0.035         |
| X          | 0.180         |
| X1         | 0.260         |
| X2         | 0.590         |
| Y          | 0.160         |
| Y1         | 0.270         |
| Y2         | 0.470         |

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