

DATA SHEET

SKY13320-374LF: 0.1 to 6.0 GHz GaAs SPDT Switch

Applications

- Two-way radios
- WiMAX
- WLANs

Features

- Broadband frequency range: 0.1 to 6.0 GHz
- Low insertion loss: 0.5 dB @ 2.4 GHz
- High isolation: 28 dB @ 0.9 GHz
- High P_{0.1dB}: +33 dBm @ 3.0 V
- Operating voltage range from 1.8 to 5.0 V
- Small MLPD (6-pin, 1.5 x 1.5 mm) package (MSL1, 260 °C per JEDEC J-STD-020)



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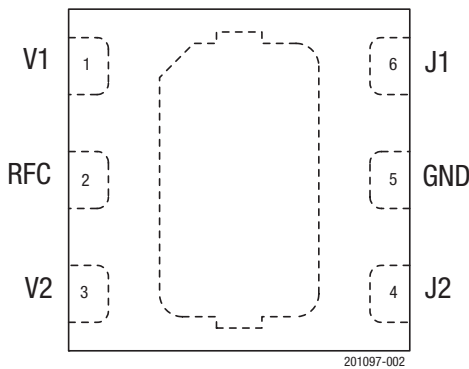


Figure 2. SKY13320-374LF Pinout (Top View)

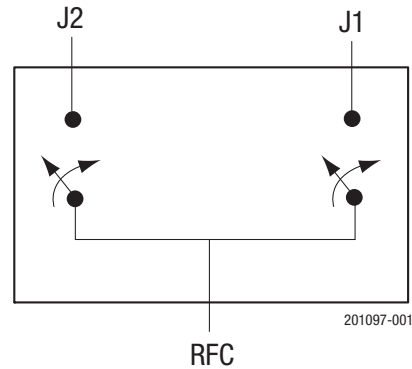


Figure 1. SKY13320-374LF Block Diagram

Description

The SKY13320-374LF is a pHEMT GaAs FET I/C high-power switch. The switch is an ideal choice for two-way radios, WiMAX, and WLAN applications such as 802.11 a/b/g where low loss, high isolation, and excellent linearity are key requirements.

The switch is manufactured in a compact, 1.5 x 1.5 mm, 6-pin exposed pad plastic Micro Leadframe Package Dual (MLPD) package.

A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.

Table 1. SKY13320-374LF Signal Descriptions

Pin	Name	Description	Pin	Name	Description
1	V1	DC control voltage	4	J2	RF output ¹
2	RFC	RF common input (Note 1)	5	GND	Ground
3	V2	DC control voltage	6	J1	RF output ¹

¹ A 47 pF blocking capacitor is required for >1 GHz operation. Use larger value capacitors for lower frequency operation.

Electrical and Mechanical Specifications

The absolute maximum ratings of the SKY13320-374LF are provided in Table 2. The recommended operating conditions are specified in Table 3 and electrical specifications are provided in Table 4.

Typical performance characteristics of the SKY13320-374LF are illustrated in Figures 3 through 5.

The state of the SKY13320-374LF is determined by the logic provided in Table 5.

Table 2. SKY13320-374LF Absolute Maximum Ratings¹

Parameter	Symbol	Minimum	Typical	Maximum	Units
Input power @ 3.3 V (f > 100 MHz)	P _{IN}		+37		dBm
Voltage range	V _{CTL}	1.8		7.0	V
Storage temperature	T _{STG}	-65		+150	°C
Junction temperature	T _J			+150	°C
Operating temperature	T _{OP}	-40		+85	°C
Thermal resistance	Θ _{JC}		42		°C/W

¹ Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

ESD HANDLING: *Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD when handling or transporting. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD handling precautions should be used at all times.*

Table 3. SKY13320-374LF Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Units
Frequency	f	0.1		6.0	GHz
Control voltage:					
Low	V _{CTL_L}	0		0.2	V
High	V _{CTL_H}	1.8		5.0	V

Table 4. SKY13320-374LF Electrical Specifications¹**($V_{CTL} = 0\text{ V}$ and $+3.0\text{ V}$, $T_{OP} = +25\text{ °C}$, $P_{IN} = 0\text{ dBm}$, Characteristic Impedance [Z_0] = $50\text{ }\Omega$, Unless Otherwise Noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Insertion loss		0.1-0.9 GHz		0.40	0.55	dB
		2.4-2.5 GHz		0.50	0.65	dB
		3.3-3.8 GHz		0.50	0.65	dB
		4.9-6.0 GHz		0.60	0.80	dB
Isolation		0.1-0.9 GHz	25	28		dB
		2.4-2.5 GHz	22	25		dB
		3.3-3.8 GHz	23	26		dB
		4.9-6.0 GHz	21	24		dB
Return loss (insertion loss state) ²		0.1-0.9 GHz		30		dB
		2.4-2.5 GHz		25		dB
		3.3-3.8 GHz		20		dB
		4.9-6.0 GHz		20		dB
Switching characteristics: Rise/fall time		10/90% or 90/10% RF		40		ns
		On/off time		80		ns
Video feedthrough				45		mV
Input power for 0.1 dB compression		f = 2.45 GHz				
		$V_{CTL} = 3.0\text{ V}$, $V_{CTL} = 1.8\text{ V}$		+33 +27		dBm dBm
Input IP3	IIP3	$P_{IN} = +17\text{ dBm/ tone}$, tone spacing = 1 MHz, f = 2.45 GHz		+52		dBm
2 nd harmonic	2fo	$P_{IN} = +22\text{ dBm CW}$, f = 2.45 GHz		-66		dBc
3 rd harmonic	3fo	$P_{IN} = +22\text{ dBm CW}$, f = 2.45 GHz		-60		dBc
Power dissipation		$V_{CTL} = 3.0\text{ V}$, $I_{CTL} = 5\text{ }\mu\text{A}$		15		μW
Supply current	I_{CTL}			5		μA

¹ Performance is guaranteed only under the conditions listed in this table.² Lower frequency return loss is dependent on the DC blocking capacitor value.

Typical Performance Characteristics

($V_{CTL} = 0\text{ V}$ and $+3.0\text{ V}$, $T_{OP} = +25\text{ }^{\circ}\text{C}$, $P_{IN} = 0\text{ dBm}$, Characteristic Impedance [Z_0] = $50\text{ }\Omega$, Blocking Capacitors = 47 pF , Unless Otherwise Noted)

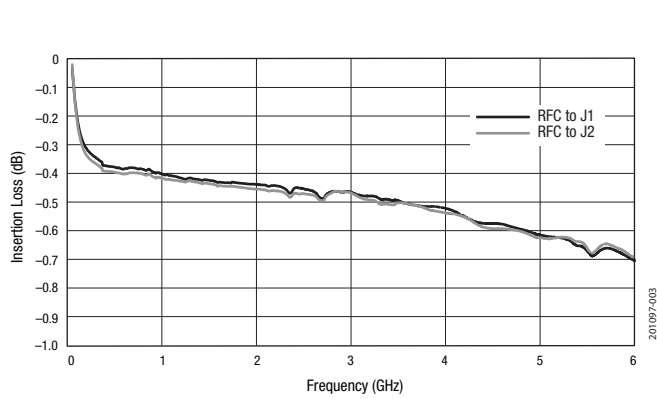


Figure 3. Typical Insertion Loss

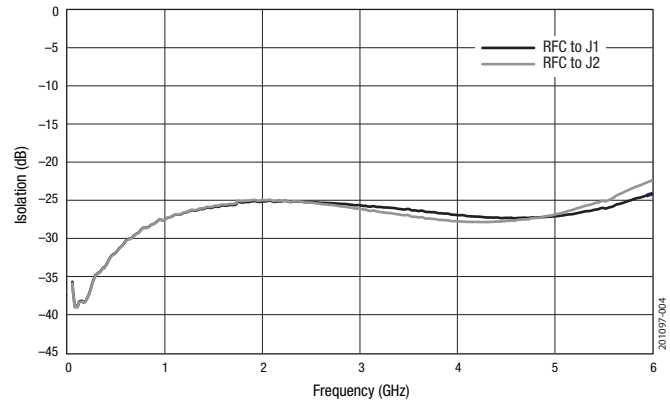


Figure 4. Typical Isolation

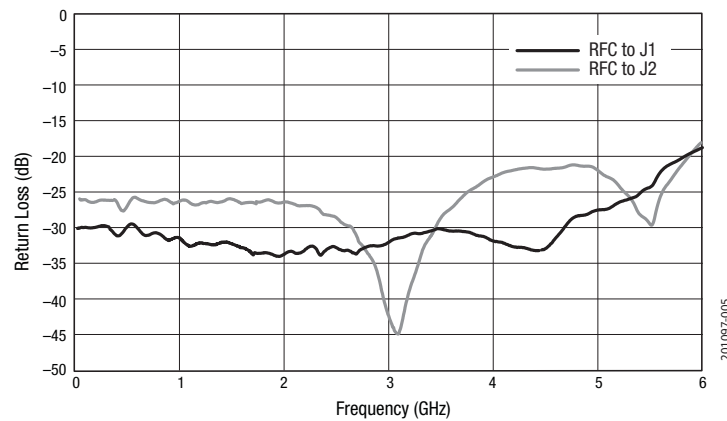


Figure 5. Typical Return Loss

Table 5. SKY13320-374LF Truth Table

V1 (Pin 1)	V2 (Pin 3)	RFC to J1 Path	RFC to J2 Path
1	0	Insertion loss	Isolation
0	1	Isolation	Insertion loss
1	1	undefined	undefined
0	0	undefined	undefined

¹ "1" = $+1.8\text{ V}$ to $+5\text{ V}$. "0" = 0 V to $+0.2\text{ V}$. Any state other than described in this table places the switch into an undefined state. An undefined state will not damage the device.

Evaluation Board Description

The SKY13320-374LF Evaluation Board is used to test the performance of the SKY13320-374LF SPDT Switch. An Evaluation Board schematic diagram is provided in Figure 6. An assembly drawing for the Evaluation Board is shown in Figure 7.

Package Dimensions

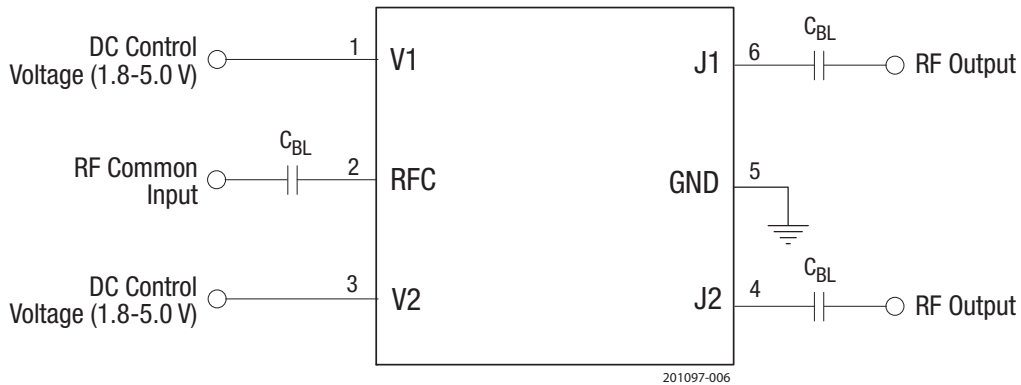
The PCB layout footprint for the SKY13320-374LF is provided in Figure 8. Typical part markings are shown in Figure 9. Package dimensions are shown in Figure 10, and tape and reel dimensions are provided in Figure 11.

Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

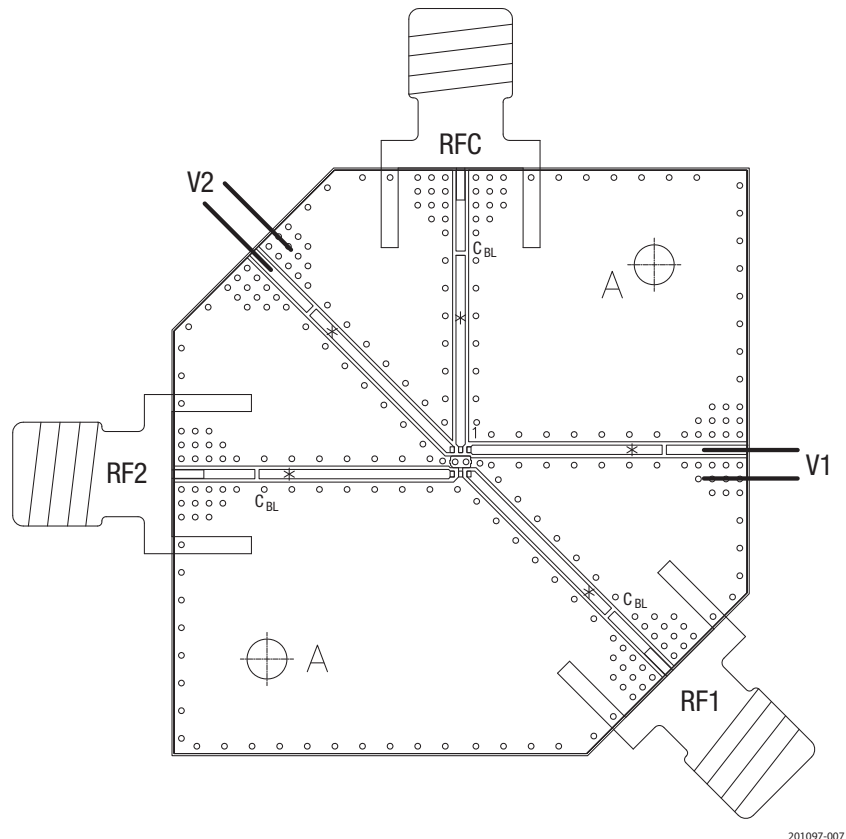
The SKY13320-374LF is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.



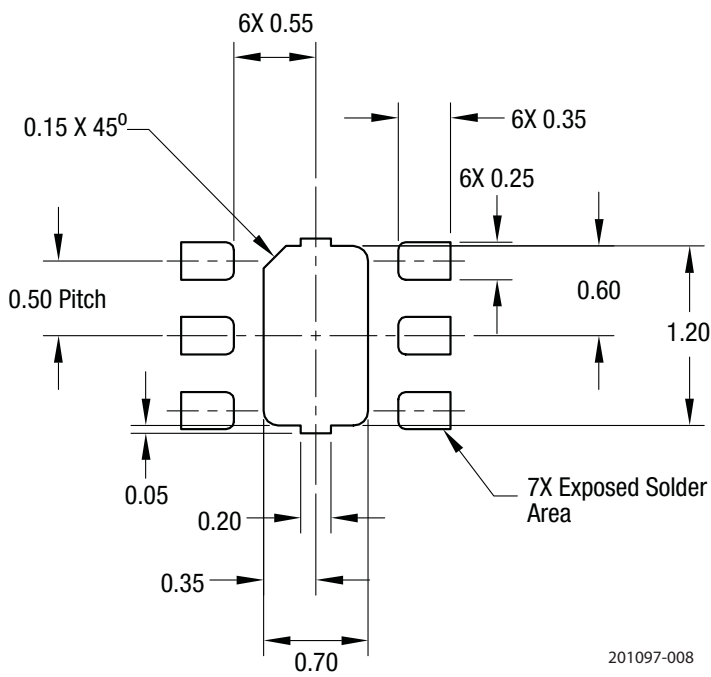
$C_{BL} = 47 \text{ pF}$ for $>1 \text{ GHz}$ operation.
Exposed ground paddle should be grounded for best performance.

Figure 6. SKY13320-374LF Evaluation Board Schematic



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Figure 7. SKY13320-374LF Evaluation Board Assembly Diagram



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Figure 8. SKY13320-374LF PCB Layout Footprint (Top View)

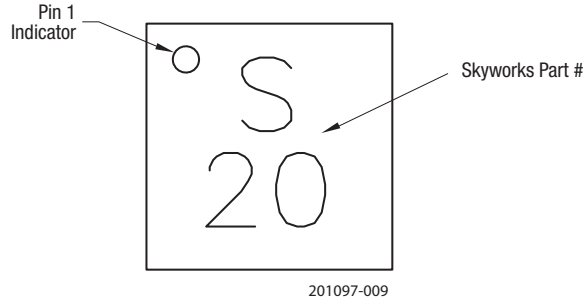
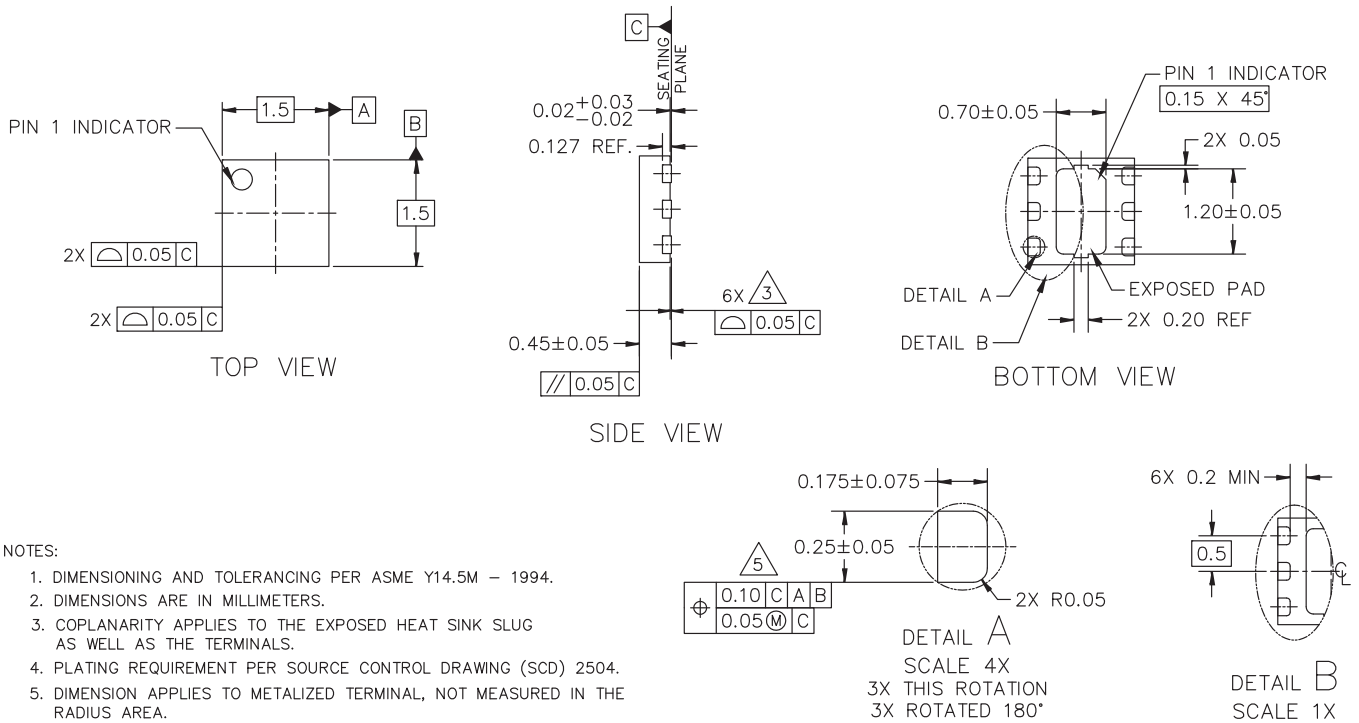


Figure 9. Typical Part Markings (Top View)



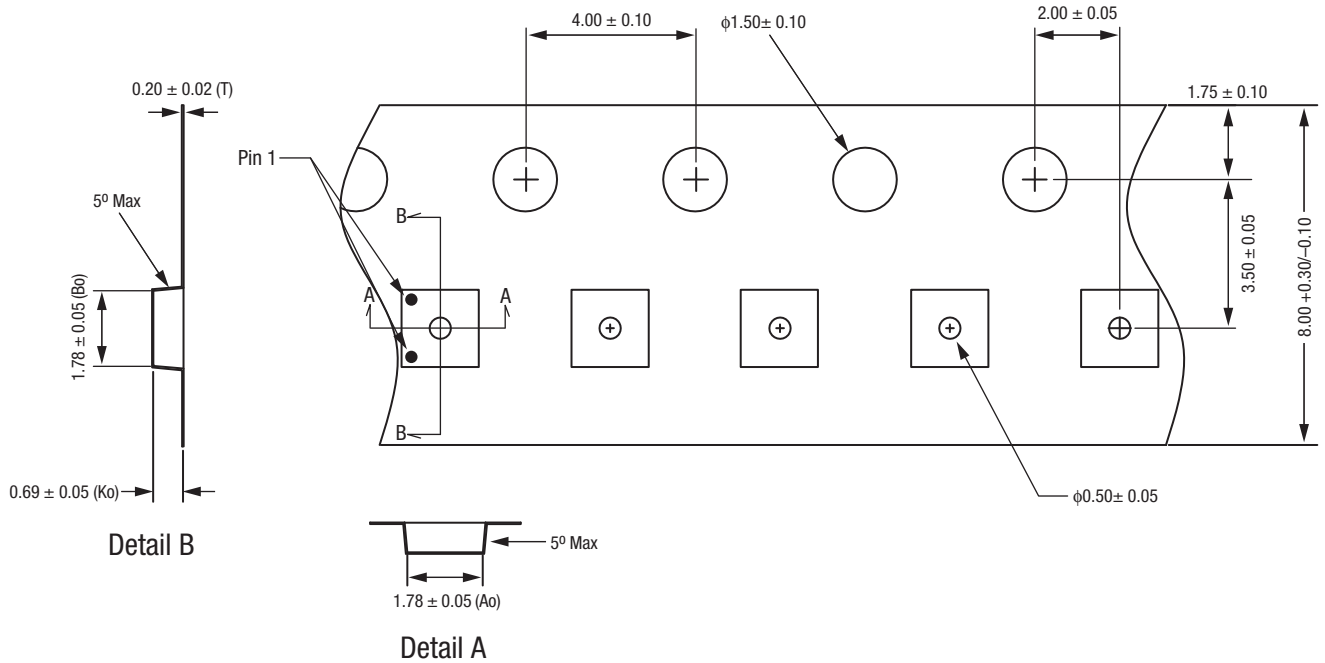
NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M – 1994.
2. DIMENSIONS ARE IN MILLIMETERS.
3. COPLANARITY APPLIES TO THE EXPOSED HEAT SINK SLUG AS WELL AS THE TERMINALS.
4. PLATING REQUIREMENT PER SOURCE CONTROL DRAWING (SCD) 2504.
5. DIMENSION APPLIES TO METALIZED TERMINAL, NOT MEASURED IN THE RADIUS AREA.

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Figure 10. SKY13320-374LF Package Dimensions

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Notes:

1. Carrier tape: black conductive polycarbonate or polystyrene.
2. Cover tape material: transparent conductive PSA.
3. Cover tape size: 5.4 mm width.
4. All measurements are in millimeters.
5. Pin 1 orientation is in lower left corner for SOT-666 packages.
Pin 1 orientation is in upper left corner for 1.5 x 1.5 mm MLPD, QFN, and DFN packages.

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Figure 11. SKY13320-374LF Tape and Reel Dimensions

Ordering Information

Product Description	Product Part Number	Evaluation Board Part Number
SKY13320-374LF SPDT Switch	SKY13320-374LF	SKY13320-374LF-EVB

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