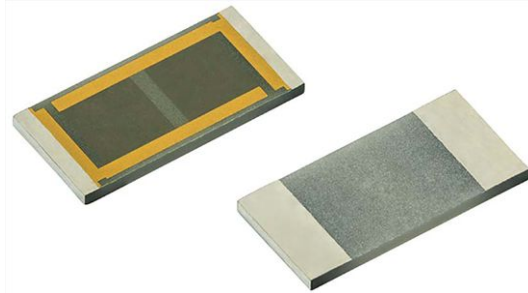


## High Power Aluminum Nitride, Wraparound Surface Mount, Precision Thin Film Chip Resistor (up to 6 W)



PCAN series chip resistors are designed on aluminum nitride ceramic substrates with enlarged backside terminations to reduce the thermal resistance between the topside resistor layer and the solder joint on the end users circuit assembly.

Actual power handling capability is limited by the end user mounting process. As with any high power chip resistor the ability to remove the heat is critical to the overall performance of the device.

### FEATURES

- High thermal conductivity aluminum nitride substrate
- Power rating up to 6.0 W
- Resistance range 30  $\Omega$  to 175  $\Omega$
- Resistor tolerance to  $\pm 0.1\%$
- TCR to  $\pm 25$  ppm/ $^{\circ}\text{C}$
- Flame resistant UL 94 V-0

### APPLICATIONS

- Power supplies
- Power switching
- Braking system

### TYPICAL PERFORMANCE

	ABSOLUTE
■	
TCR	25
TOL.	0.1

STANDARD ELECTRICAL SPECIFICATIONS		
TEST	SPECIFICATIONS	CONDITIONS
Material	Nichrome	-
Resistance Range	30 $\Omega$ to 175 $\Omega$	-
TCR: Absolute	25 ppm/ $^{\circ}\text{C}$ (standard) and 100 ppm/ $^{\circ}\text{C}$	-
Tolerance: Absolute	0.1 %, 0.25 %, 0.5 %, 1.0 % and 5.0 %	-55 $^{\circ}\text{C}$ to +150 $^{\circ}\text{C}$
Power Rating: Resistor	2.0 W to 6.0 W <sup>(1)</sup>	Maximum at +70 $^{\circ}\text{C}$
Stability: Absolute	$\Delta R$ 1.0 %	1000 h at +70 $^{\circ}\text{C}$
Voltage Coefficient	< 0.1 ppm/V	-
Working Voltage	75 V to 200 V	-
Operating Temperature Range	-55 $^{\circ}\text{C}$ to +155 $^{\circ}\text{C}$	-
Storage Temperature Range	-55 $^{\circ}\text{C}$ to +155 $^{\circ}\text{C}$	-
Noise	< -30 dB	-
Shelf Life Stability: Absolute	$\pm 0.01\%$	1 year at +25 $^{\circ}\text{C}$

#### Note

<sup>(1)</sup> Dependant on component mounting by user.

COMPONENT RATINGS			
CASE SIZE	POWER RATING (mW)	WORKING VOLTAGE (V)	RESISTANCE RANGE ( $\Omega$ )
1206	2000 <sup>(2)</sup>	200	30 to 175
2512	6000 <sup>(2)</sup>	200	30 to 175

#### Notes

• 0603 and 0805 case size under engineering qualification.

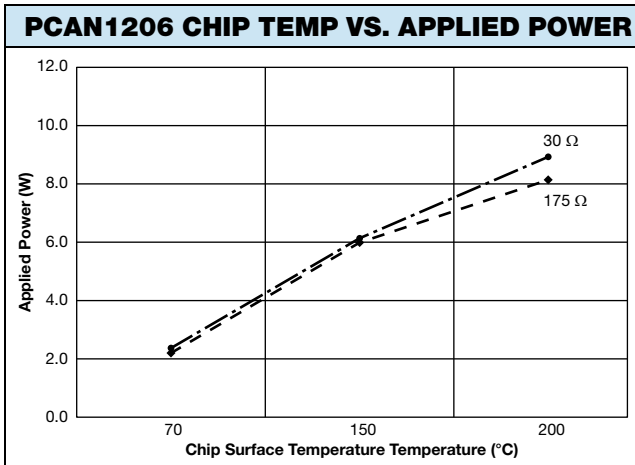
<sup>(2)</sup> Dependant on component mounting by user.

ENVIRONMENTAL TESTS		
ENVIRONMENTAL TEST	LIMITS MIL-PRF-55342 CHARACTERISTIC "H"	TYPICAL VISHAY PERFORMANCE
Resistance temperature characteristic	± 50 ppm/°C	± 25 ppm/°C
Maximum ambient temperature at rated wattage	+70 °C	+70 °C
Maximum ambient temperature at power derating	+150 °C	+150 °C
Thermal shock	± 0.25 %	± 0.10 %
Low temperature operation	± 0.25 %	± 0.10 %
Short time overload	± 0.1 %	± 0.10 %
High temperature exposure	± 0.2 %	± 0.10 %
Resistance to soldering heat	± 0.25 %	± 0.10 %
Moisture resistance	± 0.4 %	± 0.50 %
Life at +70 °C for 1000 h	± 0.5 %	± 1.00 %

DIMENSIONS in inches					
CASE SIZE	LENGTH L	WIDTH W	THICKNESS T MIN./MAX.	TOP PAD D	BOTTOM PAD E
1206	0.126 ± 0.008	0.063 ± 0.005	0.015 ± 0.003	0.020 + 0.005/- 0.010	0.040 ± 0.005
2512	0.259 + 0.009/- 0.015	0.124 ± 0.005	0.015 ± 0.003	0.020 ± 0.005	0.050 ± 0.005

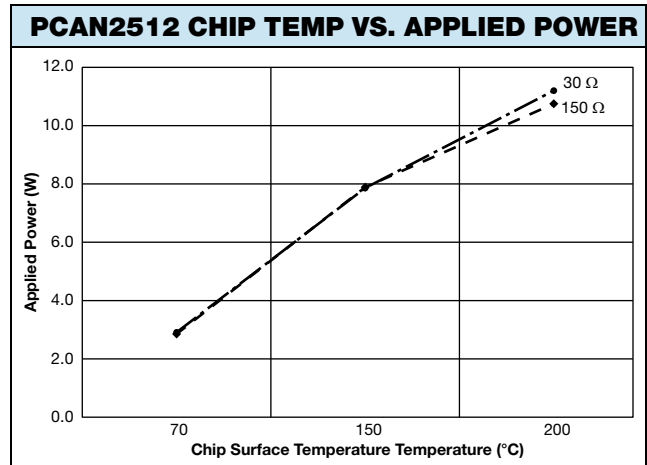
LAND PATTERN DIMENSIONS in inches	

STANDARD MATERIAL SPECIFICATIONS	
Resistive element	Nichrome
Substrate material	Aluminum nitride
Terminations (Tin/lead)	Tin/lead solder over nickel barrier
Terminations (Lead (Pb)-free)	Tin/silver/copper (Sn96.5/Ag3.0/Cu0.5) solder over nickel barrier



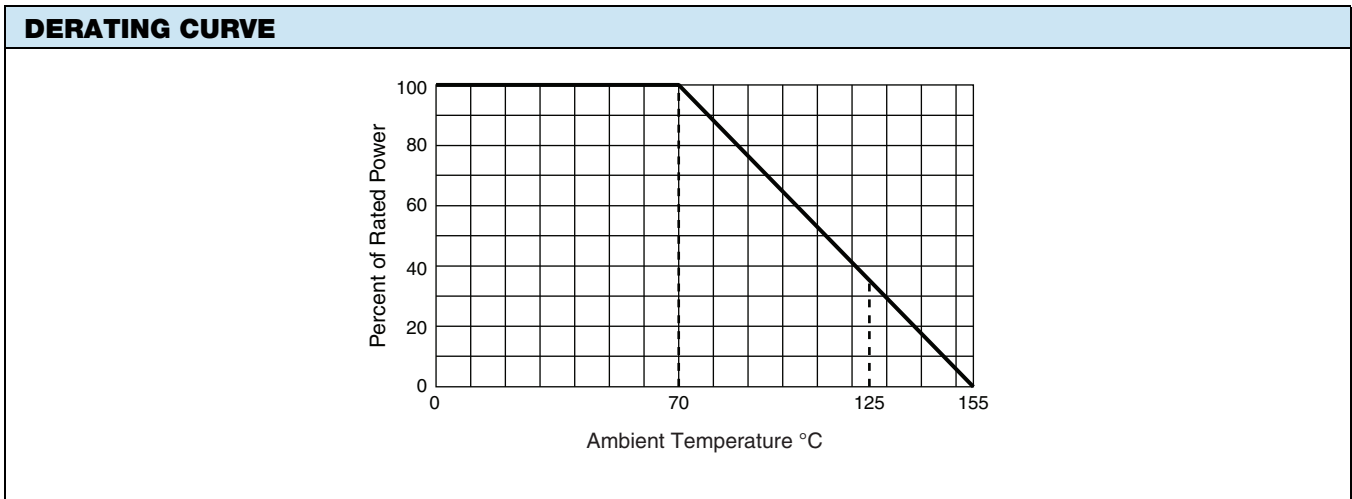
**Note**

- Chip surface temperature measured using FLIR SC645 thermal imaging system with an approximate test card surface temperature of 85 °C.
- Thermal imaging was conducted under ambient conditions resulting in a steady state test card surface temperature of 85 °C over the full range of power levels.
- Thermal imaging and load life testing was conducted mounting one device to a 1.6" x 3.7" test card with 3.5 mil copper plating on both surfaces. Thermal vias on 50 mil centers were utilized for heat transfer between surfaces of the test card.



**Note**

- Chip surface temperature measured using FLIR SC645 thermal imaging system with an approximate test card surface temperature of 85 °C.





GLOBAL PART NUMBER INFORMATION																
New Global Part Numbering: PCAN1206H1000BBT1																
<b>P</b>	<b>C</b>	<b>A</b>	<b>N</b>	<b>1</b>	<b>2</b>	<b>0</b>	<b>6</b>	<b>H</b>	<b>1</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>B</b>	<b>B</b>	<b>T</b>	<b>1</b>
GLOBAL MODEL	CASE SIZE	TCR CHARACTERISTIC		RESISTANCE		TOLERANCE		TERMINATION		PACKAGING						
PCAN	1206 2512	E = ± 25 ppm/°C H = ± 50 ppm/°C K = ± 100 ppm/°C		The first 3 digits are significant figures and the last digit specifies the number of zeros to follow. "R" designates the decimal point.  Example: 10R0 = 10 Ω 1000 = 100 Ω		B = ± 0.1 % C = ± 0.25 % D = ± 0.5 % F = ± 1.0 % G = ± 2.0 %		B = Wraparound Sn/Pb solder w/ nickel barrier S = Wraparound lead (Pb)-free solder (e1) RoHS compliant G = Wraparound Au, over Ni (gold) termination epoxy bondable RoHS compliant (e4)		<b>BS</b> = BULK 100 min., 1 mult <b>WS</b> = WAFFLE 100 min., 1 mult <b>WO</b> = 100 pc min. waffle, 1 mult <b>WI</b> = 100 min., 1 mult (package unit single lot date code)  TAPE AND REEL <b>T0</b> = 100 min., 100 mult <b>T1</b> = 1000 min., 1000 mult <b>T3</b> = 300 min., 300 mult <b>T5</b> = 500 min., 500 mult <b>TF</b> = Full reel <b>TS</b> = 100 min., 1 mult <b>TI</b> = 100 min., 1 mult (item single lot date code) <b>TP</b> = 100 min., 1 mult (package unit single lot date code)						



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