#### Nch 600V 30A Power MOSFET

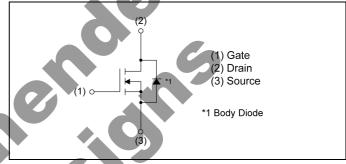
$V_{DSS}$	600V
R <sub>DS(on)</sub> (Max.)	0.130Ω
I <sub>D</sub>	±30A
$P_D$	305W

# Outline TO-247

#### Features

- 1) Low on-resistance.
- 2) Ultra fast switching speed.
- 3) Parallel use is easy.
- 4) Pb-free lead plating; RoHS compliant





Packaging specifications

	ing productions	
	Packing	Tube
V.	Reel size (mm)	-
Tymo	Tape width (mm)	-
Туре	Basic ordering unit (pcs)	450
	Taping code	C9
	Marking	R6030KNZ1

## Application

Switching

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		$V_{DSS}$	600	V
Continuous drain current (T <sub>c</sub> = 25	5°C)	I <sub>D</sub> *1	±30	Α
Pulsed drain current		I <sub>DP</sub> *2	±90	Α
Cata Cauraa valtara	static	V <sub>GSS</sub>	±20	V
Gate - Source voltage	AC(f>1Hz)		±30	V
Avalanche current, single pulse		I <sub>AS</sub>	5.2	Α
Avalanche energy, single pulse		E <sub>AS</sub> *3	636	mJ
Power dissipation (T <sub>c</sub> = 25°C)	P <sub>D</sub>	305	W	
Junction temperature		T <sub>j</sub>	150	°C
Operating junction and storage te	mperature range	T <sub>stg</sub>	-55 to +150	°C

#### ●Thermal resistance

Downwortow	Cymah al	Values			l le:4
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub> *4	-	-	0.41	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub>	-	-	30	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-		265	°C

## ●Electrical characteristics (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	600	-	-	V
		$V_{DS} = 600V, V_{GS} = 0V$				
Zero gate voltage drain current	I <sub>DSS</sub>	$T_j = 25^{\circ}C$	-	-	100	μΑ
		$T_j = 125^{\circ}C$	-	ı	1000	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	1	±100	nA
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS}$ = 10V, $I_D$ = 1mA	3	-	5	V
40		V <sub>GS</sub> = 10V, I <sub>D</sub> = 14.5A				
Static drain - source on - state resistance	R <sub>DS(on)</sub> *5	T <sub>j</sub> = 25°C	-	0.115	0.130	Ω
		T <sub>j</sub> = 125°C	-	0.24	-	
Gate resistance	$R_{G}$	f = 1MHz, open drain	-	2.1	-	Ω

## ● Electrical characteristics (T<sub>a</sub> = 25°C)

Davamatar	Cymah al	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Uniil
Forward Transfer Admittance	Y <sub>fs</sub>  *5	V <sub>DS</sub> = 10V, I <sub>D</sub> = 15A	10	20	-	S
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	2350		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 25V	-	2000		pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	140	-	
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 300V$ , $V_{GS} = 10V$	-	36	-	
Rise time	t <sub>r</sub> *5	I <sub>D</sub> = 15A	16	75	-	no
Turn - off delay time	t <sub>d(off)</sub> *5	$R_L \simeq 20\Omega$	-	90	-	ns
Fall time	<b>t</b> <sub>f</sub> *5	$R_G = 10\Omega$	-	45	-	

## ● Gate charge characteristics (T<sub>a</sub> = 25°C)

Parameter	Symbol Conditions		Values			Unit
raiailletei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{*5}$	V <sub>DD</sub> ≈ 300V	-	56	1	
Gate - Source charge	Q <sub>gs</sub> *5	I <sub>D</sub> = 30A	-	18	-	nC
Gate - Drain charge	Q <sub>gd</sub> *5	V <sub>GS</sub> = 10V	-	23	-	
Gate plateau voltage	V <sub>(plateau)</sub>	$V_{DD} \simeq 300V$ , $I_D = 30A$	-	6.3	-	V

<sup>\*1</sup> Limited only by maximum channel temperature allowed.

<sup>\*2</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

<sup>\*3</sup> L $\doteqdot$ 50mH, V<sub>DD</sub>=50V, R<sub>G</sub>=25 $\Omega$ , STARTING T<sub>j</sub>=25°C

<sup>\*4</sup> T<sub>C</sub>=25°C

<sup>\*5</sup> Pulsed

Unit

Ws/K

## ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions		Unit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offit
Continuous forward current	I <sub>S</sub> *1	T <sub>C</sub> = 25°C	-	-	30	А
Pulse forward current	I <sub>SP</sub> *2		-	-	90	A
Forward voltage	V <sub>SD</sub> *5	$V_{GS} = 0V, I_{S} = 30A$	-	` `	1.5	V
Reverse recovery time	t <sub>rr</sub>		-	517	-	ns
Reverse recovery charge	Q <sub>rr</sub>	I <sub>S</sub> = 30A di/dt = 100A/μs		9.6	-	μC
Peak reverse recovery current	I <sub>rrm</sub>			37	-	Α

Typical transient thermal characteristics

Symbol	Value	Unit	Symbol	Value
R <sub>th1</sub>	0.190		C <sub>th1</sub>	0.0143
R <sub>th2</sub>	0.429	K/W	C <sub>th2</sub>	0.322
R <sub>th3</sub>	0.250		C <sub>th3</sub>	14.7

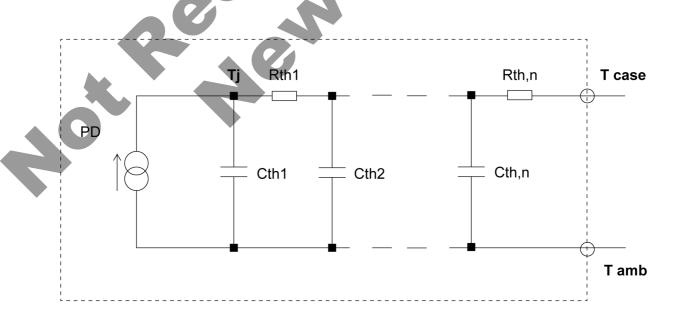


Fig.1 Power Dissipation Derating Curve

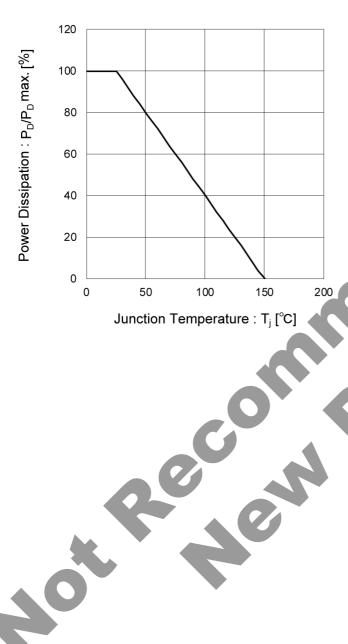


Fig.2 Maximum Safe Operating Area

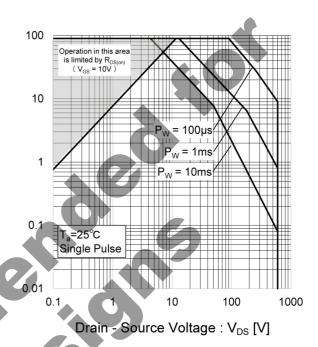
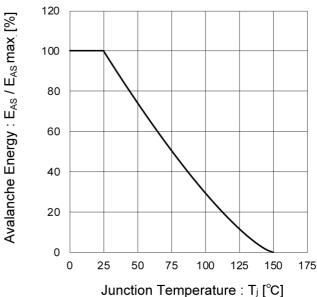
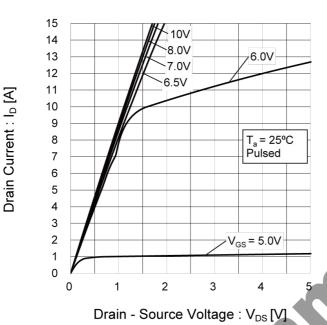


Fig.3 Avalanche Energy Derating Curve vs. Junction Temperature



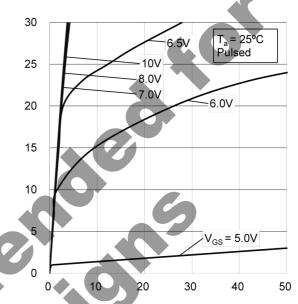
Drain Current : I<sub>D</sub> [A]

Fig.4 Typical Output Characteristics(I)



Drain Current : I<sub>D</sub> [A]

Fig.5 Typical Output Characteristics(II)



Drain - Source Voltage : V<sub>DS</sub> [V]



Fig.6 Breakdown Voltage vs.

Junction Temperature

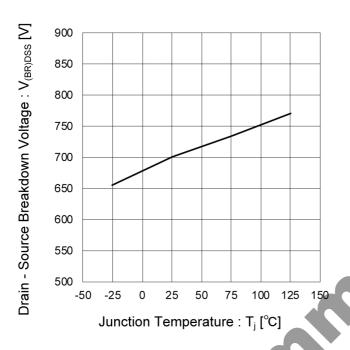


Fig.7 Typical Transfer Characteristics

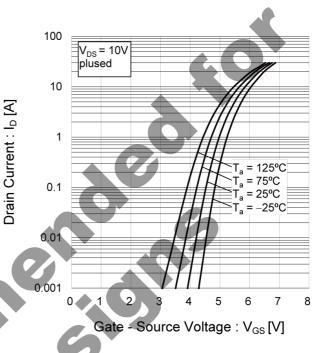


Fig.8 Gate Threshold Voltage vs.
Junction Temperature

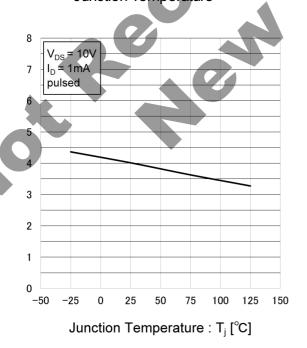
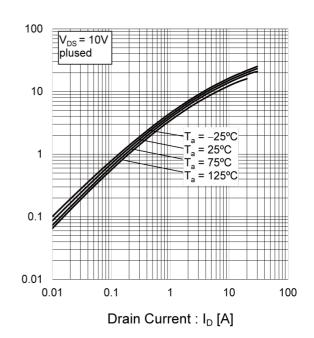


Fig.9 Forward Transfer Admittance vs.

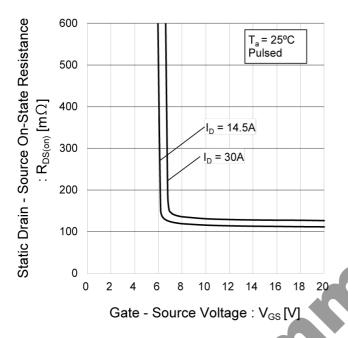
Drain Current



Gate Threshold Voltage: V<sub>GS(th)</sub> [V]

Transconductance : gfs [S]

Fig.10 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Resistance vs. Junction Temperature

Fig.11 Static Drain - Source On - State

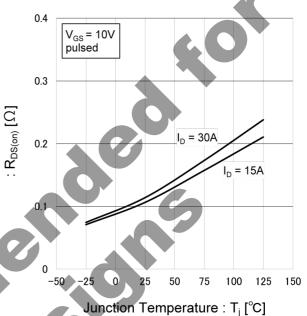
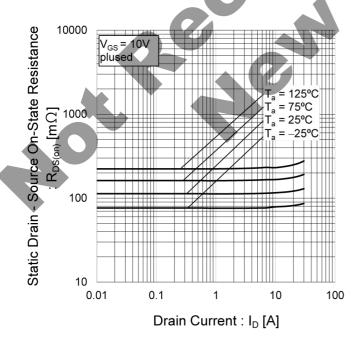


Fig.12 Static Drain - Source On - State Resistance vs. Drain Current(I)



Static Drain - Source On-State Resistance

Fig.13 Typical Capacitance vs.

Drain - Source Voltage

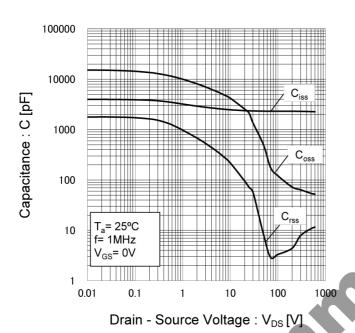


Fig.14 Switching Characteristics

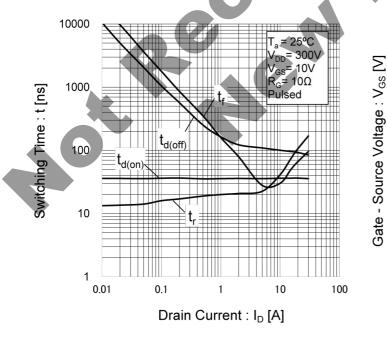


Fig.15 Dynamic Input Characteristics

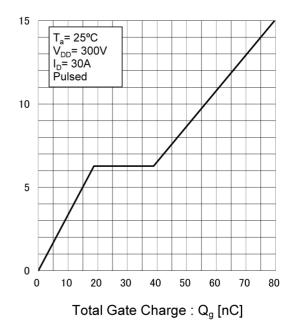
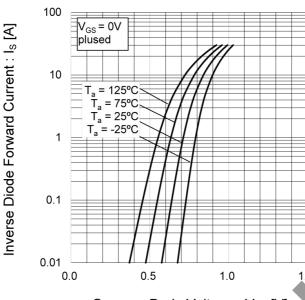
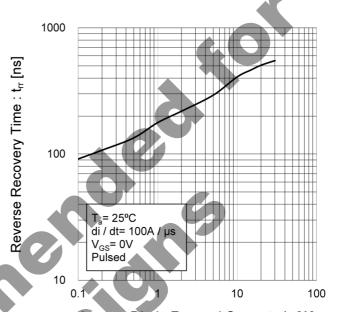


Fig.16 Inverse Diode Forward Current vs. Source - Drain Voltage



Source - Drain Voltage : V<sub>SD</sub> [V]

Fig.17 Reverse Recovery Time vs.
Inverse Diode Forward Current



Inverse Diode Forward Current : I<sub>S</sub> [A]



#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

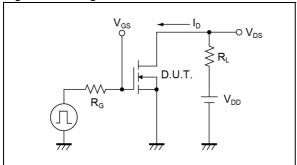


Fig.2-1 Gate Charge Measurement Circuit

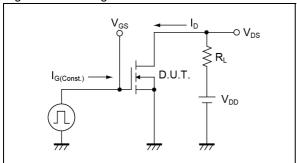


Fig.3-1 Avalanche Measurement Circuit

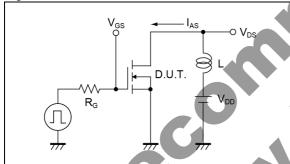


Fig.4-1 dv/dt Measurement Circuit

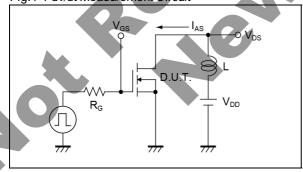


Fig.5-1 dv/dt Measurement Circuit

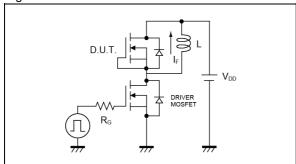


Fig.1-2 Switching Waveforms

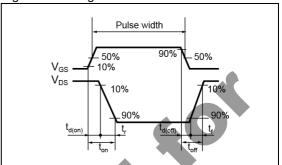


Fig.2-2 Gate Charge Waveform

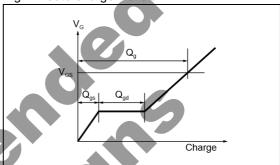


Fig.3-2 Avalanche Waveform

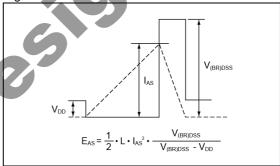


Fig.4-2 dv/dt Waveform

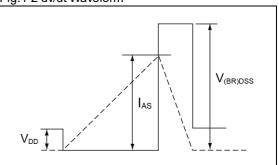
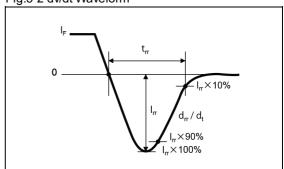
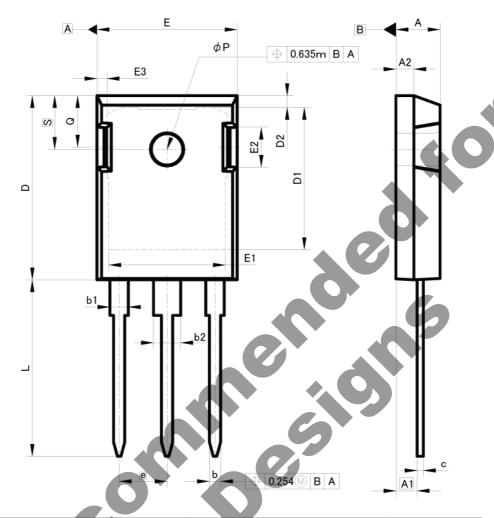


Fig.5-2 dv/dt Waveform



#### Dimensions

TO-247



DIM	MILIM	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
A	4.83	5.21	0.19	0.205	
A1	2.29	2.54	0.09	0.1	
A2	1,91	2.16	0.075	0.085	
b	1.14	1.40	0.045	0.055	
b1	1.91	2.20	0.075	0.087	
b2	2.92	3.20	0.115	0.126	
С	0.61	0.80	0.024	0.031	
D	20.80	21.34	0.819	0.84	
D1	17.43	17.83	0.686	0.702	
Е	15.75	16.13	0.62	0.635	
е	5.4	45	0.2	22	
N	3	3	3	3	
L	19.81	20.57	0.78	0.81	
L1	3.81	4.07	0.15	0.16	
ФР	3.55	3.65	0.14	0.144	
Q	5.59	6.20	0.22	0.244	
S	6.	15	<u> </u>		

Dimension in mm/inches



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