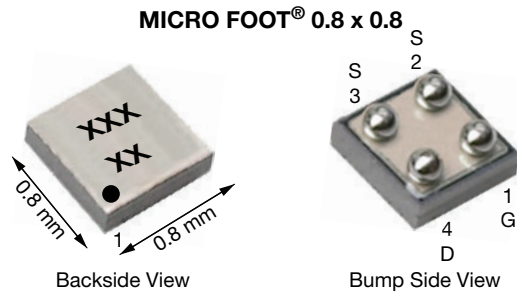


## N-Channel 12 V (D-S) MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω) MAX.	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (TYP.)
12	0.043 at V <sub>GS</sub> = 4.5 V	3.9	6.5 nC
	0.050 at V <sub>GS</sub> = 2.5 V	3.6	
	0.065 at V <sub>GS</sub> = 1.8 V	3.2	



**Marking Code:** xx = AD  
xxx = Date/Lot traceability code

**Ordering Information:**  
Si8806DB-T2-E1 (lead (Pb)-free and halogen-free)

### FEATURES

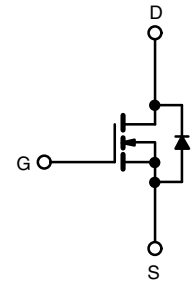
- TrenchFET® power MOSFET
- Small 0.8 mm x 0.8 mm outline area
- Low 0.4 mm max. profile
- Low On-resistance
- Material categorization:  
for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

### APPLICATIONS

- Load switch with low voltage drop
- Load switch for low voltage power lines
- Smart phones, tablet PCs, mobile computing



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)				
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	V <sub>DS</sub>	12	V	
Gate-Source Voltage	V <sub>GS</sub>	± 8		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	I <sub>D</sub>	T <sub>A</sub> = 25 °C	3.9 <sup>a</sup>	A
		T <sub>A</sub> = 70 °C	3.1 <sup>a</sup>	
		T <sub>A</sub> = 25 °C	2.8 <sup>b</sup>	
		T <sub>A</sub> = 70 °C	2.3 <sup>b</sup>	
Pulsed Drain Current (t = 300 μs)	I <sub>DM</sub>	20		
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>A</sub> = 25 °C	0.7 <sup>a</sup>	
		T <sub>A</sub> = 25 °C	0.4 <sup>b</sup>	
Maximum Power Dissipation	P <sub>D</sub>	T <sub>A</sub> = 25 °C	0.9 <sup>a</sup>	W
		T <sub>A</sub> = 70 °C	0.6 <sup>a</sup>	
		T <sub>A</sub> = 25 °C	0.5 <sup>b</sup>	
		T <sub>A</sub> = 70 °C	0.3 <sup>b</sup>	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	
Soldering Recommendations (Peak Temperature) <sup>c</sup>		260		

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum Junction-to-Ambient <sup>a, d</sup>	R <sub>thJA</sub>	105	135	°C/W
Maximum Junction-to-Ambient <sup>b, e</sup>		200	260	

#### Notes

- Surface mounted on 1" x 1" FR4 board with full copper, t = 5 s.
- Surface mounted on 1" x 1" FR4 board with minimum copper, t = 5 s.
- Refer to IPC/JEDEC® (J-STD-020), no manual or hand soldering.
- Maximum under steady state conditions is 185 °C/W.
- Maximum under steady state conditions is 330 °C/W.

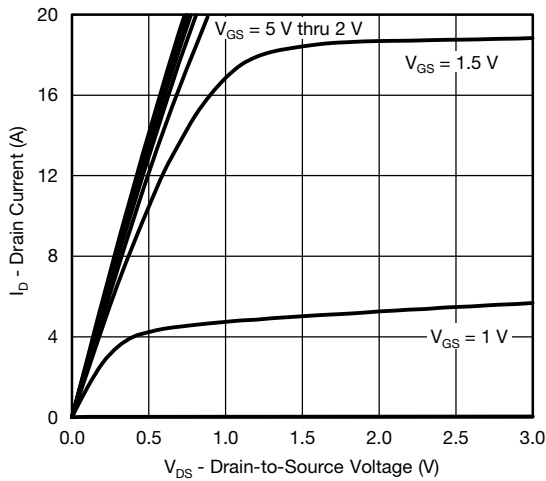
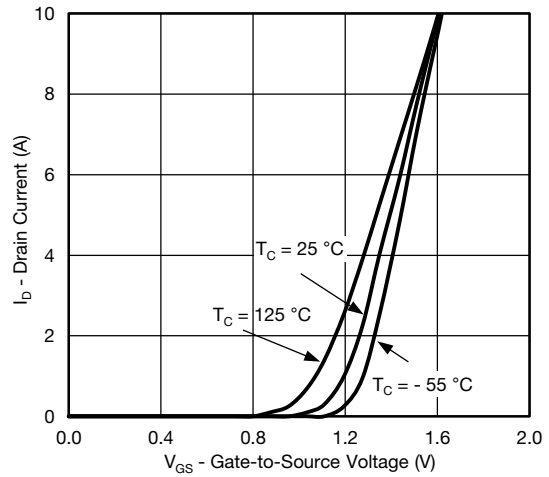
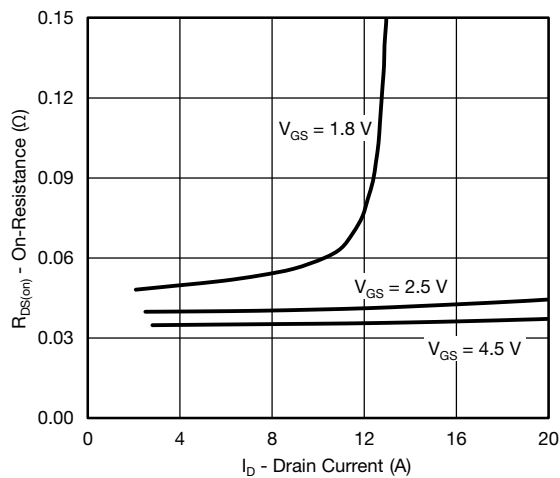
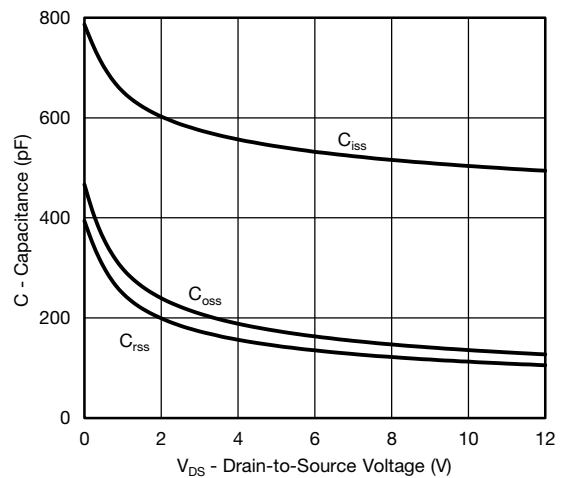
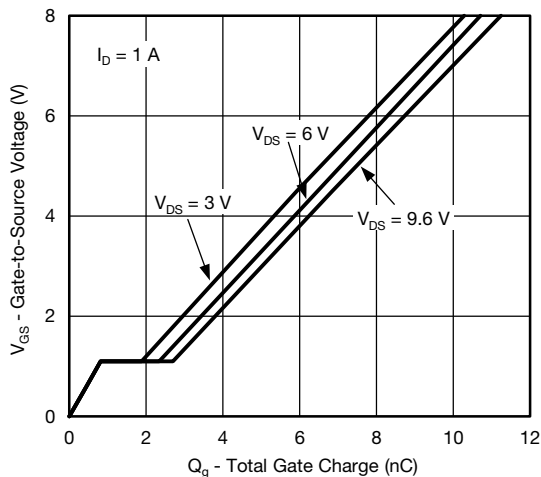
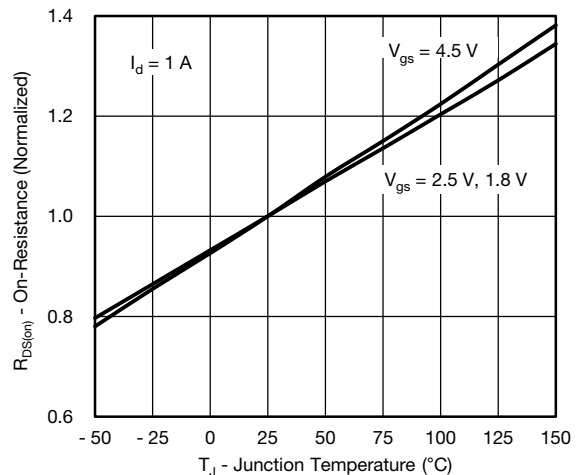


<b>SPECIFICATIONS</b> ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	12	-	-	V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$	-	6	-	mV/ $^\circ\text{C}$
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$		-	-2.9	-	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	0.4	-	1	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 8\text{ V}$	-	-	$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 12\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	$\mu\text{A}$
		$V_{DS} = 12\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$	-	-	10	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 4.5\text{ V}$	10	-	-	A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 4.5\text{ V}, I_D = 1\text{ A}$	-	0.035	0.043	$\Omega$
		$V_{GS} = 2.5\text{ V}, I_D = 1\text{ A}$	-	0.039	0.050	
		$V_{GS} = 1.8\text{ V}, I_D = 0.5\text{ A}$	-	0.047	0.065	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 6\text{ V}, I_D = 1\text{ A}$	-	16	-	S
<b>Dynamic <sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = 6\text{ V}, V_{GS} = 8\text{ V}, I_D = 1\text{ A}$	-	11	17	nC
			-	6.5	10	
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 6\text{ V}, V_{GS} = 4.5\text{ V}, I_D = 1\text{ A}$	-	0.9	-	nC
Gate-Drain Charge	$Q_{gd}$		-	1.6	-	
Gate Resistance	$R_g$	$f = 1\text{ MHz}$	-	6	-	$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 6\text{ V}, R_L = 6\text{ }\Omega$ $I_D \cong 1\text{ A}, V_{GEN} = 4.5\text{ V}, R_g = 1\text{ }\Omega$	-	10	20	ns
Rise Time	$t_r$		-	20	40	
Turn-Off Delay Time	$t_{d(off)}$		-	30	60	
Fall Time	$t_f$		-	12	25	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 6\text{ V}, R_L = 6\text{ }\Omega$ $I_D \cong 1\text{ A}, V_{GEN} = 8\text{ V}, R_g = 1\text{ }\Omega$	-	7	15	ns
Rise Time	$t_r$		-	16	35	
Turn-Off Delay Time	$t_{d(off)}$		-	25	50	
Fall Time	$t_f$		-	9	20	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Source-Drain Diode Current	$I_S$	$T_A = 25\text{ }^\circ\text{C}$	-	-	0.7	A
Pulse Diode Forward Current	$I_{SM}$		-	-	20	
Body Diode Voltage	$V_{SD}$	$I_S = 1\text{ A}, V_{GS} = 0\text{ V}$	-	0.8	1.2	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 1\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$	-	20	40	ns
Body Diode Reverse Recovery Charge	$Q_{rr}$		-	5	10	nC
Reverse Recovery Fall Time	$t_a$		-	5	-	ns
Reverse Recovery Rise Time	$t_b$		-	15	-	

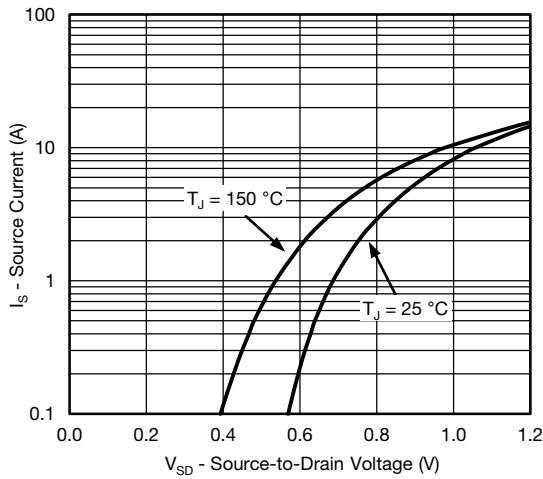
**Notes**

- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
b. Guaranteed by design, not subject to production testing.

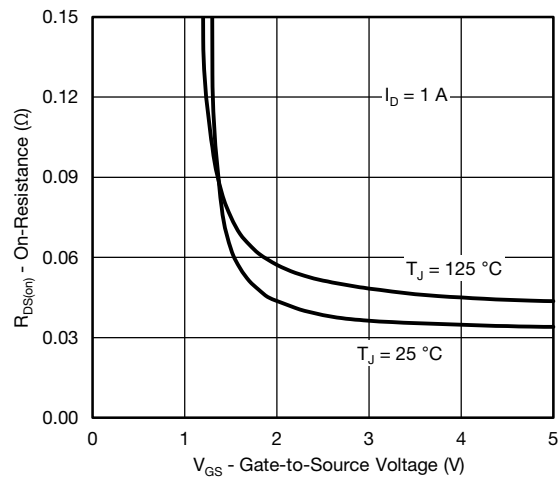
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Output Characteristics**

**Transfer Characteristics**

**On-Resistance vs. Drain Current**

**Capacitance**

**Gate Charge**

**On-Resistance vs. Junction Temperature**

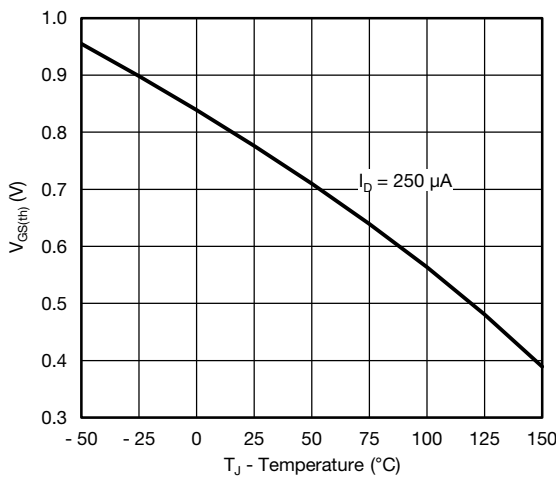
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



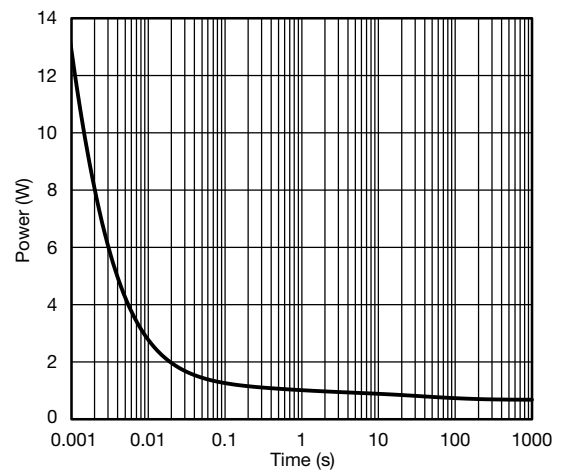
**Source-Drain Diode Forward Voltage**



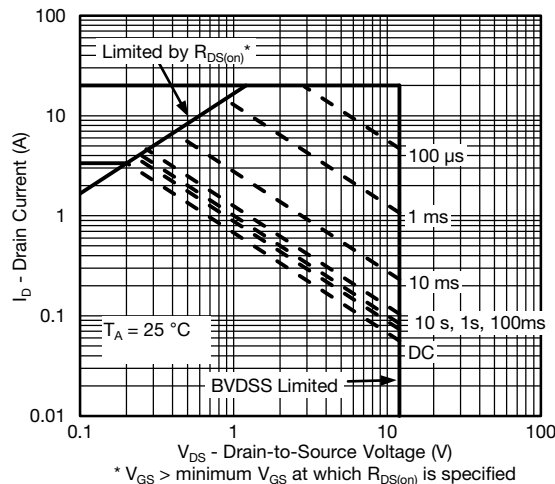
**On-Resistance vs. Gate-to-Source Voltage**



**Threshold Voltage**



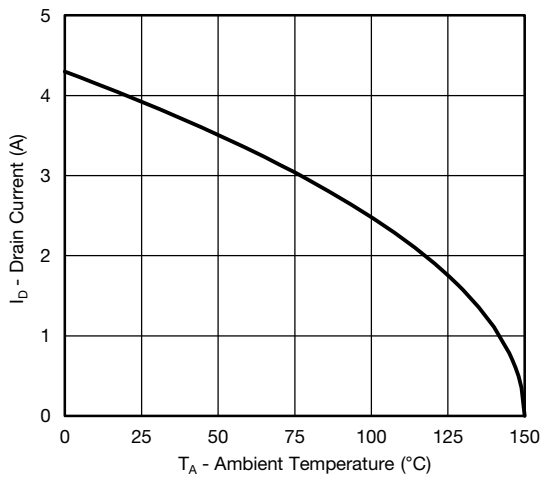
**Single Pulse Power (Junction-to-Ambient)**



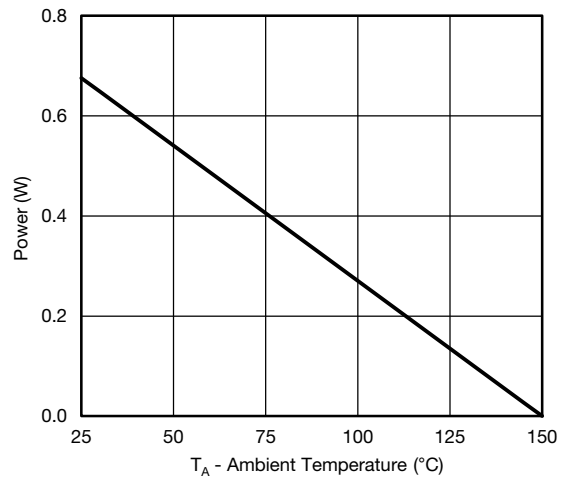
**Safe Operating Area, Junction-to-Ambient**



**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



**Current Derating\***



**Power Derating**

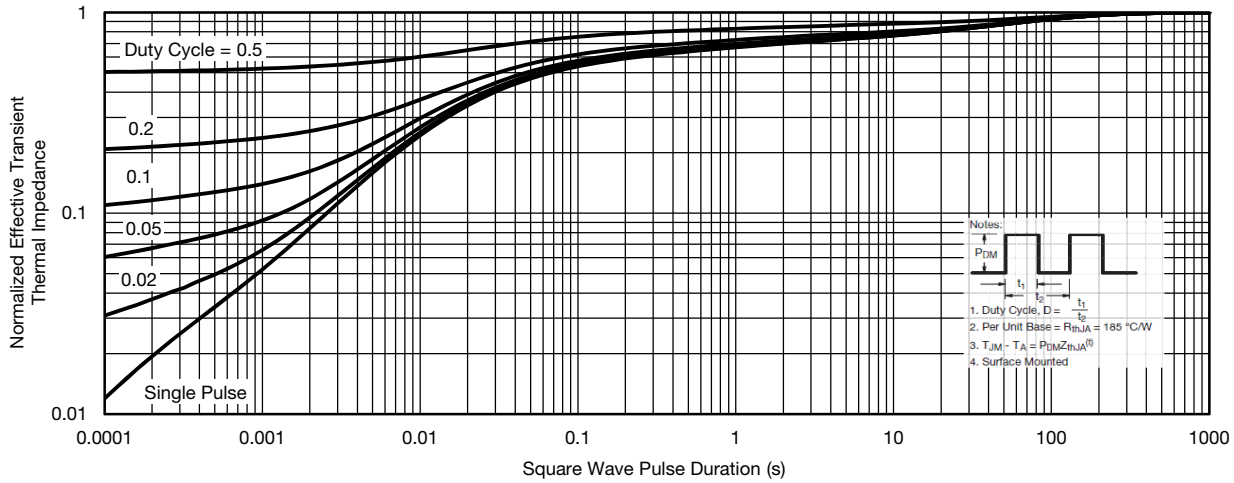
**Note**

When mounted on 1" x 1" FR4 with full copper.

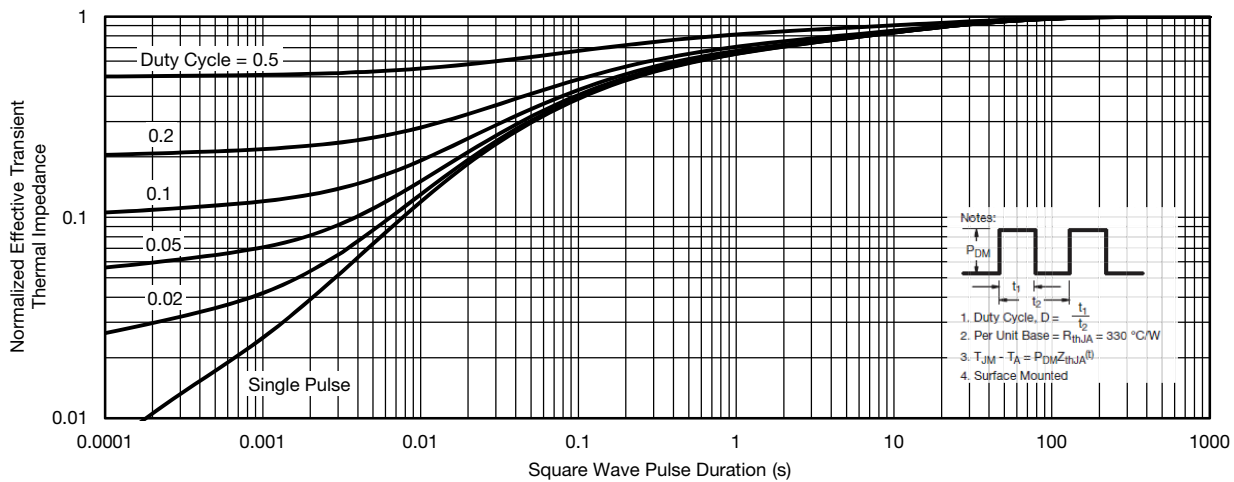
\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150\text{ °C}$ , using junction-to-ambient thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient (On 1" x 1" FR4 Board with Maximum Copper)



Normalized Thermal Transient Impedance, Junction-to-Ambient (On 1" x 1" FR4 Board with Minimum Copper)

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- Оперативные сроки поставки под заказ (от 5 рабочих дней);
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- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
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- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
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## JONHON

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