

Product Summary

| BV _{DSS} | R _{DS(ON)} Max | I _D Max T _C = +25°C |
|-------------------|-------------------------------|--|
| 40V | 7.9mΩ @ V _{GS} = 10V | 54.8A |
| | 12mΩ @ V _{GS} = 4.5V | 44.5A |

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, which makes it ideal for high-efficiency power management applications.

- Power Management Functions
- DC-DC Converters

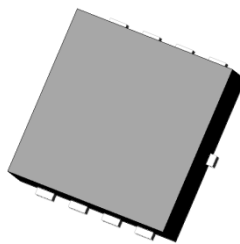
Features

- 100% Unclamped Inductive Switching—Ensures More Reliable and Robust End Application
- Low On-Resistance
- Low Input Capacitance
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

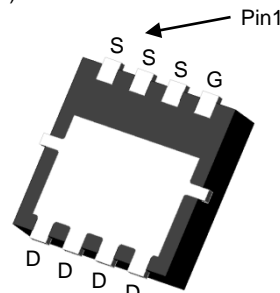
Mechanical Data

- Case: PowerDI[®] 3333-8 (Type UX)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.072 grams (Approximate)

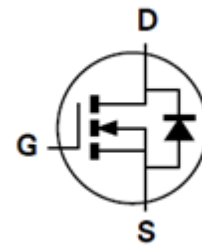
PowerDI3333-8 (Type UX)



Top View



Bottom View



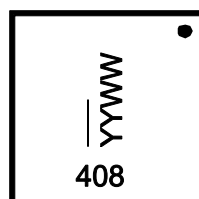
Equivalent Circuit

Ordering Information (Note 4)

| Part Number | Case | Packaging |
|---------------|-------------------------|------------------|
| DMT4008LFV-7 | PowerDI3333-8 (Type UX) | 2000/Tape & Reel |
| DMT4008LFV-13 | PowerDI3333-8 (Type UX) | 3000/Tape & Reel |

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, see <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



408 = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 18 = 2018)
 WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit |
|---|------------------|------------------------|------|
| Drain-Source Voltage | V _{DSS} | 40 | V |
| Gate-Source Voltage | V _{GSS} | ±20 | V |
| Continuous Drain Current, V _{GS} = 10V (Note 6) | I _D | T _C = +25°C | 54.8 |
| | | T _C = +70°C | 43.9 |
| Continuous Drain Current, V _{GS} = 10V (Note 5) | I _D | T _A = +25°C | 12.1 |
| | | T _A = +70°C | 9.7 |
| Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%) | I _{DM} | 70 | A |
| Maximum Continuous Body Diode Forward Current (Note 6) | I _S | 29.8 | A |
| Pulsed Body Diode Forward Current (10μs Pulse, Duty Cycle = 1%) | I _{SM} | 70 | A |
| Avalanche Current, L = 0.3mH | I _{AS} | 11.3 | A |
| Avalanche Energy, L = 0.3mH | E _{AS} | 19.2 | mJ |

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit |
|--|-----------------------------------|------------------------|------|
| Total Power Dissipation (Note 5) | P _D | T _A = +25°C | 1.9 |
| Thermal Resistance, Junction to Ambient (Note 5) | | R _{θJA} | 64 |
| Total Power Dissipation (Note 6) | P _D | T _C = +25°C | 35.7 |
| Thermal Resistance, Junction to Case (Note 6) | | R _{θJC} | 3.5 |
| Operating and Storage Temperature Range | T _J , T _{STG} | -55 to +150 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
|--|---------------------|-----|------|------|------|--|
| OFF CHARACTERISTICS (Note 7) | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | 40 | — | — | V | V _{GS} = 0V, I _D = 250μA |
| Zero Gate Voltage Drain Current | I _{DSS} | — | — | 1 | μA | V _{DS} = 32V, V _{GS} = 0V |
| Gate-Source Leakage | I _{GSS} | — | — | ±100 | nA | V _{GS} = ±20V, V _{DS} = 0V |
| ON CHARACTERISTICS (Note 7) | | | | | | |
| Gate Threshold Voltage | V _{GS(TH)} | 1 | — | 3 | V | V _{DS} = V _{GS} , I _D = 250μA |
| Static Drain-Source On-Resistance | R _{DS(ON)} | — | 6.5 | 7.9 | mΩ | V _{GS} = 10V, I _D = 12A |
| | | — | 9.4 | 12 | | V _{GS} = 4.5V, I _D = 10A |
| Diode Forward Voltage | V _{SD} | — | — | 1.2 | V | V _{GS} = 0V, I _S = 10A |
| DYNAMIC CHARACTERISTICS (Note 8) | | | | | | |
| Input Capacitance | C _{iss} | — | 1179 | — | pF | V _{DS} = 20V, V _{GS} = 0V, f = 1MHz |
| Output Capacitance | C _{oss} | — | 384 | — | pF | |
| Reverse Transfer Capacitance | C _{rss} | — | 42 | — | pF | |
| Gate Resistance | R _g | — | 1.7 | — | Ω | V _{DS} = 0V, V _{GS} = 0V, f = 1MHz |
| Total Gate Charge (V _{GS} = 10V) | Q _g | — | 17.1 | — | nC | |
| Total Gate Charge (V _{GS} = 4.5V) | Q _g | — | 8.3 | — | nC | |
| Gate-Source Charge | Q _{gs} | — | 2.4 | — | nC | |
| Gate-Drain Charge | Q _{gd} | — | 3.4 | — | nC | V _{DS} = 20V, I _D = 10A |
| Turn-On Delay Time | t _{D(ON)} | — | 3.5 | — | ns | |
| Turn-On Rise Time | t _R | — | 3.7 | — | ns | |
| Turn-Off Delay Time | t _{D(OFF)} | — | 17.1 | — | ns | |
| Turn-Off Fall Time | t _F | — | 6.4 | — | ns | |
| Body Diode Reverse Recovery Time | t _{RR} | — | 19.8 | — | ns | V _{DD} = 20V, V _{GS} = 10V, R _G = 6Ω, I _D = 10A |
| Body Diode Reverse Recovery Charge | Q _{RR} | — | 8.8 | — | nC | |
| | | | | | | I _F = 10A, di/dt = 400A/μs |

- Notes:
- Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer and 1inch square copper plate.
 - Thermal resistance from junction to soldering point (on the exposed drain pad).
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

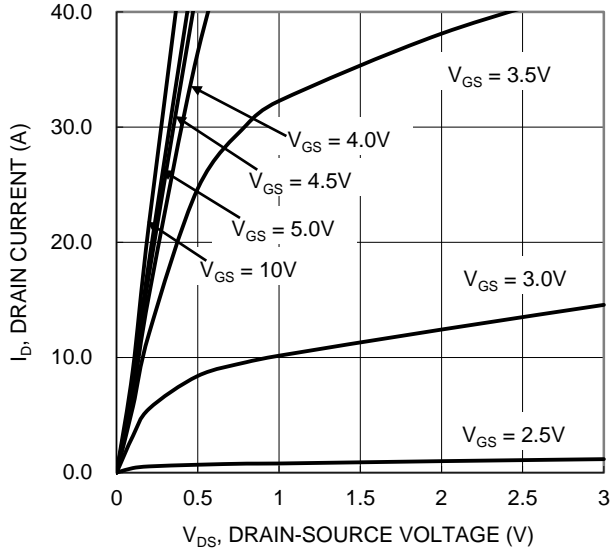


Figure 1. Typical Output Characteristic

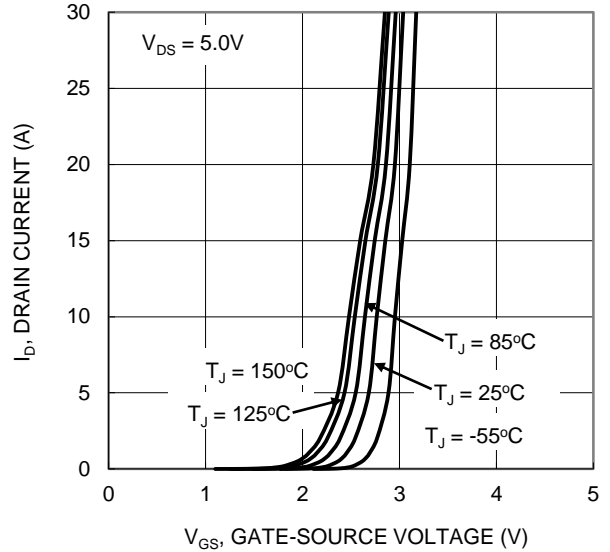


Figure 2. Typical Transfer Characteristic

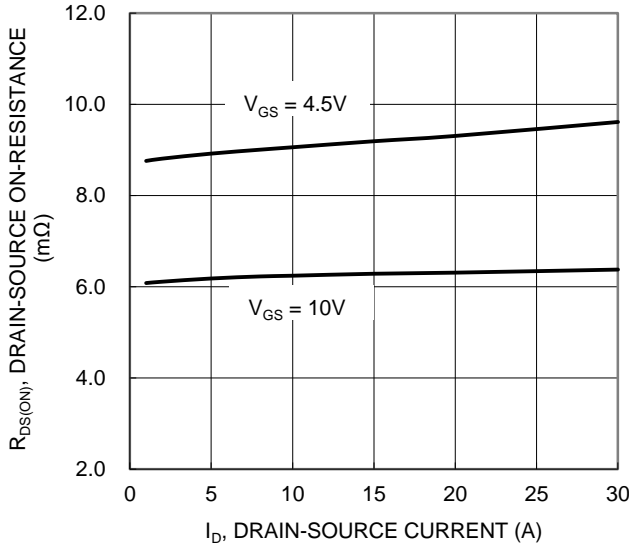


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

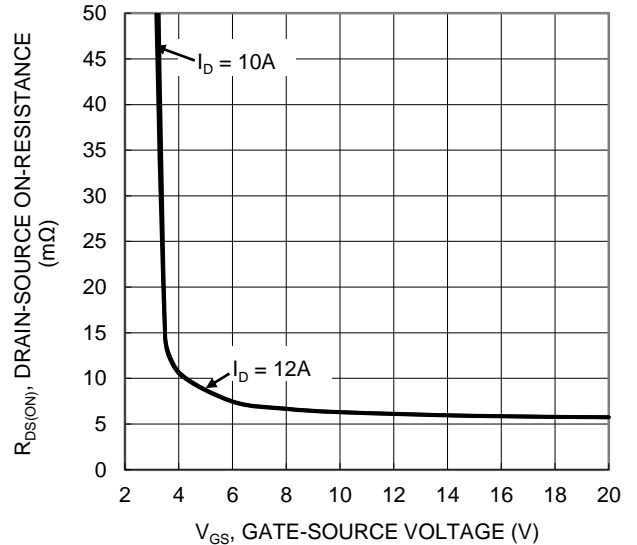


Figure 4. Typical Transfer Characteristic

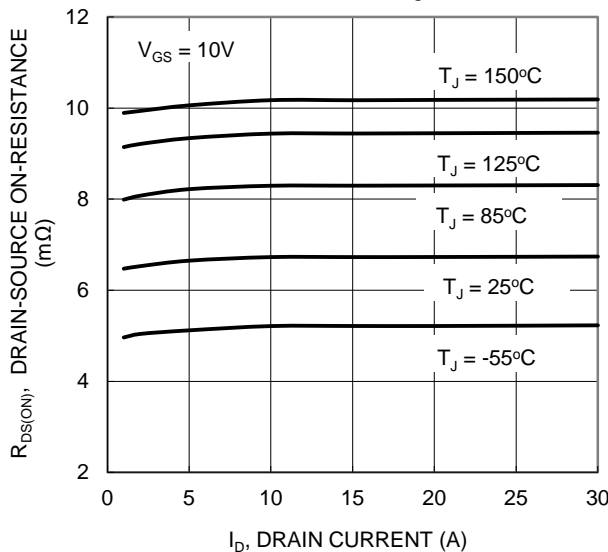


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

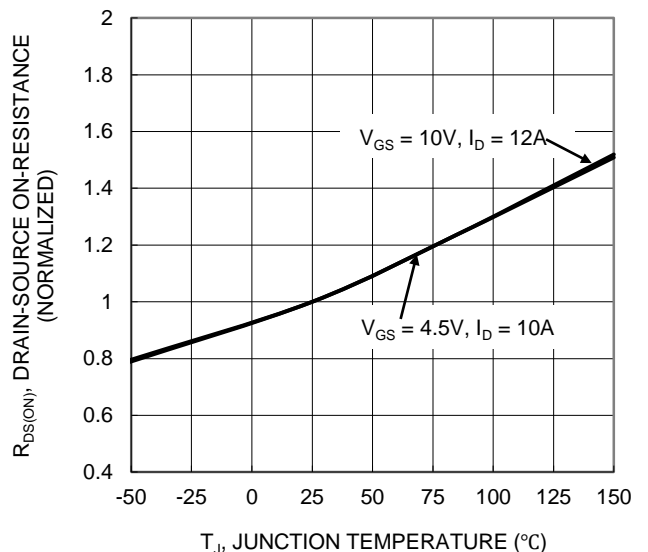


Figure 6. On-Resistance Variation with Temperature

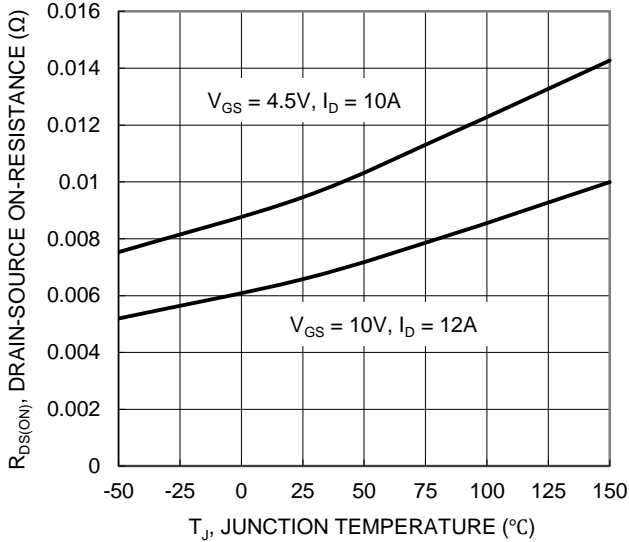


Figure 7. On-Resistance Variation with Temperature

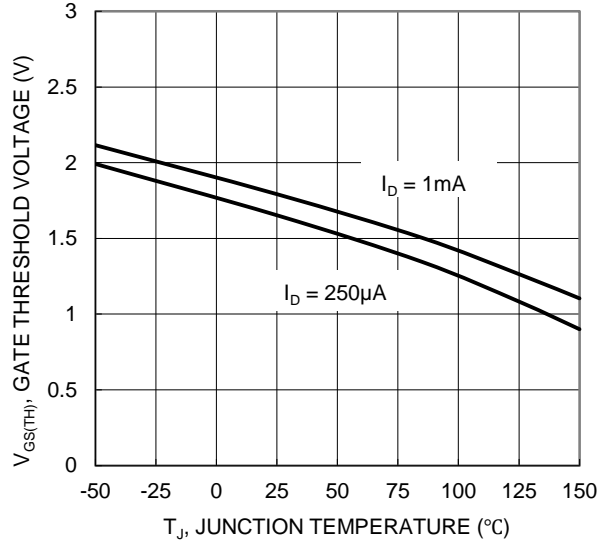


Figure 8. Gate Threshold Variation vs. Junction Temperature

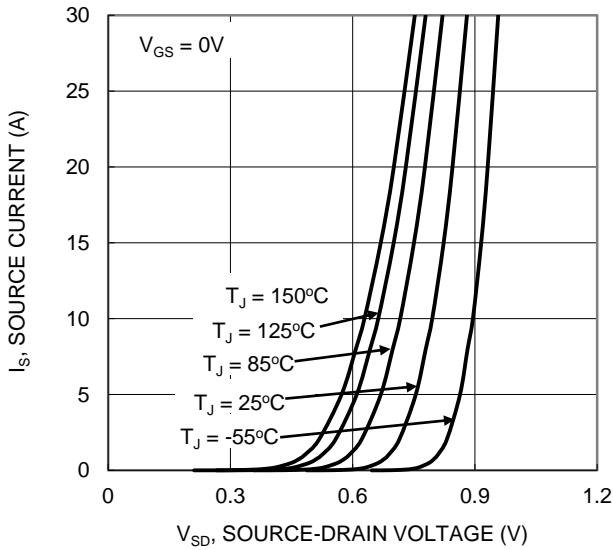


Figure 9. Diode Forward Voltage vs. Current

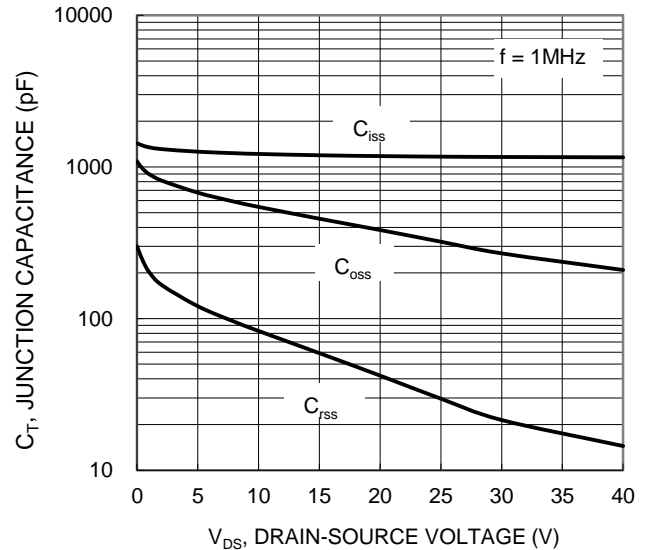


Figure 10. Typical Junction Capacitance

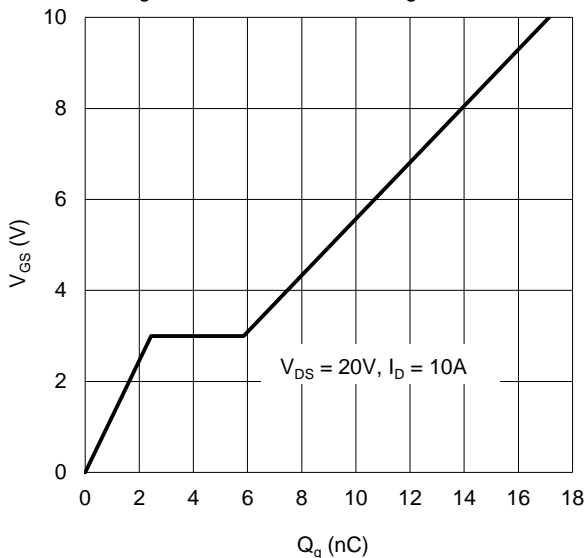


Figure 11. Gate Charge

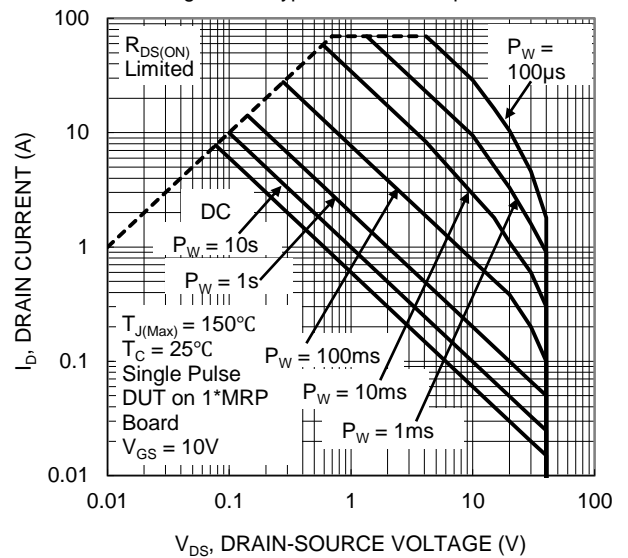


Figure 12. SOA, Safe Operation Area

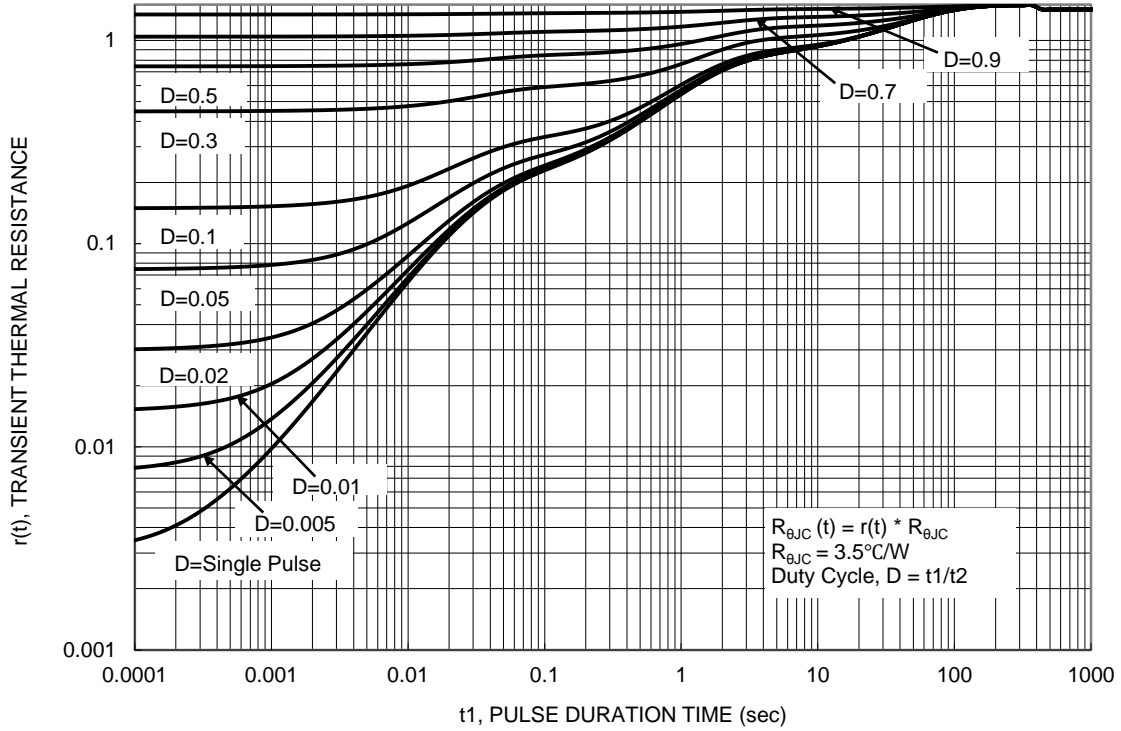
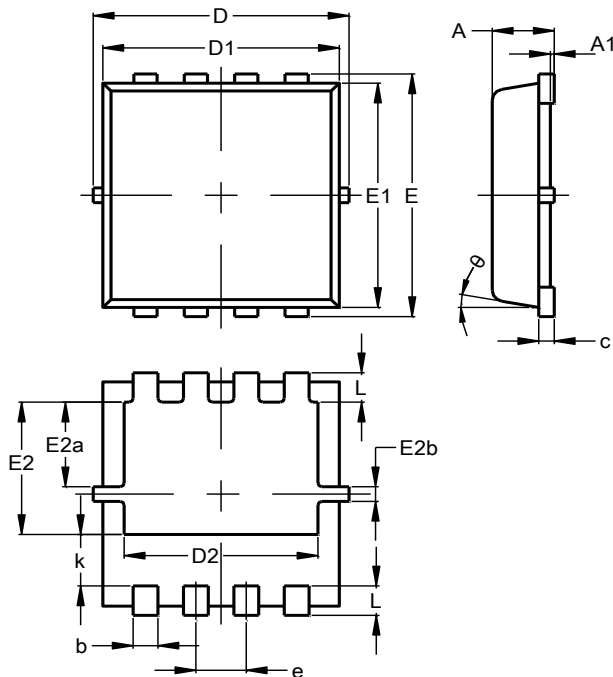


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8 (Type UX)

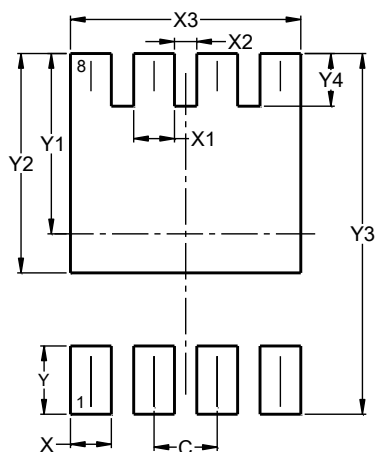


| PowerDI3333-8 (Type UX) | | | |
|----------------------------|----------|------|------|
| Dim | Min | Max | Typ |
| A | 0.75 | 0.85 | 0.80 |
| A1 | 0.00 | 0.05 | — |
| b | 0.25 | 0.40 | 0.32 |
| c | 0.10 | 0.25 | 0.15 |
| D | 3.20 | 3.40 | 3.30 |
| D1 | 2.95 | 3.15 | 3.05 |
| D2 | 2.30 | 2.70 | 2.50 |
| E | 3.20 | 3.40 | 3.30 |
| E1 | 2.95 | 3.15 | 3.05 |
| E2 | 1.60 | 2.00 | 1.80 |
| E2a | 0.95 | 1.35 | 1.15 |
| E2b | 0.10 | 0.30 | 0.20 |
| e | 0.65 BSC | | |
| k | 0.50 | 0.90 | 0.70 |
| L | 0.30 | 0.50 | 0.40 |
| θ | 0° | 12° | 10° |
| All Dimensions in mm | | | |

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8 (Type UX)



| Dimensions | Value (in mm) |
|------------|---------------|
| C | 0.650 |
| X | 0.420 |
| X1 | 0.420 |
| X2 | 0.230 |
| X3 | 2.370 |
| Y | 0.700 |
| Y1 | 1.850 |
| Y2 | 2.250 |
| Y3 | 3.700 |
| Y4 | 0.540 |

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