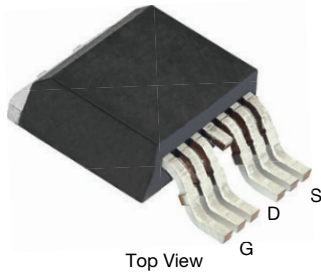


N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (Ω) MAX.	I _D (A) ^d	Q _g (TYP.)
100	0.0038 at V _{GS} = 10 V	120	76
	0.0046 at V _{GS} = 7.5 V	120	

TO-263 7-Lead


Ordering Information:

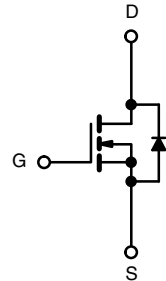
SUM70040M-GE3 (lead (Pb)-free and halogen-free)

FEATURES

- ThunderFET® power MOSFET
- Maximum 175 °C junction temperature
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
 COMPLIANT
 HALOGEN
FREE
APPLICATIONS

- Power supply
- Secondary synchronous rectification
- DC/DC converter
- Power tools
- Motor drive switch
- DC/AC inverter
- Battery management
- OR-ing



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V _{DS}	100	V
Gate-Source Voltage		V _{GS}	± 20	
Continuous Drain Current (T _J = 150 °C)	T _C = 25 °C	I _D	120 ^d	A
	T _C = 70 °C		120 ^d	
Pulsed Drain Current (t = 100 μs)		I _{DM}	480	
Avalanche Current		I _{AS}	73	
Single Avalanche Energy ^a	L = 0.1 mH	E _{AS}	266	mJ
Maximum Power Dissipation ^a	T _C = 25 °C	P _D	375 ^b	W
	T _C = 125 °C		125 ^b	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) ^c		R _{thJA}	40	°C/W
Junction-to-Case (Drain)		R _{thJC}	0.4	

Notes

- Duty cycle ≤ 1 %.
- See SOA curve for voltage derating.
- When mounted on 1" square PCB (FR4 material).
- Package limited.



SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	100	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2.5	-	4	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V	-	-	± 250	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V	-	-	1	μA
		V _{DS} = 100 V, V _{GS} = 0 V, T _J = 125 °C	-	-	150	
		V _{DS} = 100 V, V _{GS} = 0 V, T _J = 175 °C	-	-	5	mA
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 10 V, V _{GS} = 10 V	120	-	-	A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 20 A	-	0.0030	0.0038	Ω
		V _{GS} = 7.5 V, I _D = 15 A	-	0.0035	0.0046	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 20 A	-	82	-	S
Dynamic ^b						
Input Capacitance	C _{ISS}	V _{GS} = 0 V, V _{DS} = 50 V, f = 1 MHz	-	5100	-	pF
Output Capacitance	C _{OSS}		-	2025	-	
Reverse Transfer Capacitance	C _{RSS}		-	165	-	
Total Gate Charge ^c	Q _g	V _{DS} = 50 V, V _{GS} = 10 V, I _D = 20 A	-	76	120	nC
Gate-Source Charge ^c	Q _{gs}		-	23	-	
Gate-Drain Charge ^c	Q _{gd}		-	17	-	
Gate Resistance	R _g	f = 1 MHz	0.6	3.3	6.6	Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 50 V, R _L = 5 Ω I _D ≅ 10 A, V _{GEN} = 10 V, R _g = 1 Ω	-	15	30	ns
Rise Time ^c	t _r		-	22	40	
Turn-Off Delay Time ^c	t _{d(off)}		-	55	100	
Fall Time ^c	t _f		-	15	30	
Drain-Source Body Diode Ratings and Characteristics ^b (T_C = 25 °C)						
Pulsed Current (t = 100 μs)	I _{SM}		-	-	480	A
Forward Voltage ^a	V _{SD}	I _F = 10 A, V _{GS} = 0 V	-	0.8	1.5	V
Reverse Recovery Time	t _{rr}	I _F = 20 A, di/dt = 100 A/μs	-	94	150	ns
Peak Reverse Recovery Charge	I _{RM(REC)}		-	4.6	10	A
Reverse Recovery Charge	Q _{rr}		-	0.23	0.5	μC

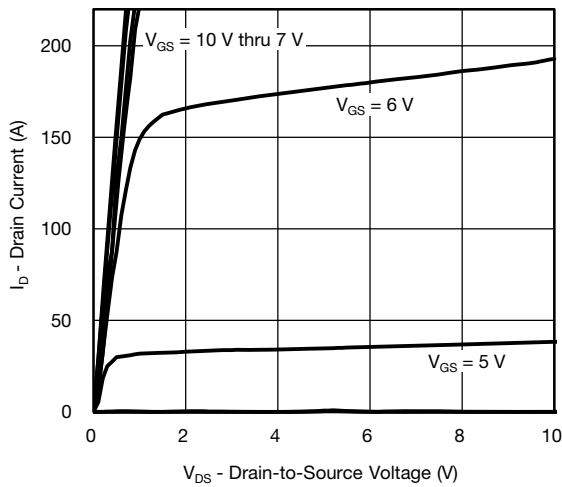
Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

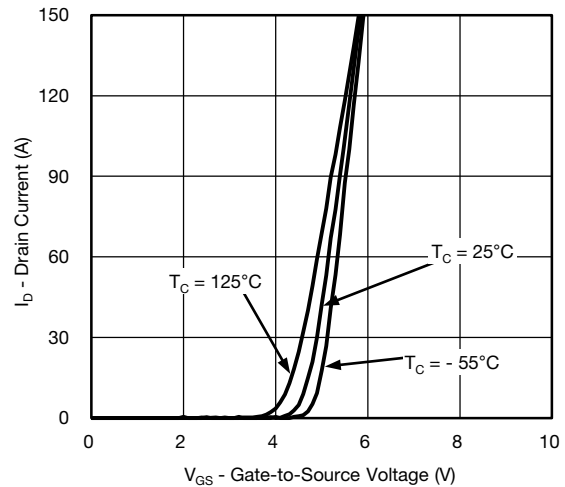
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



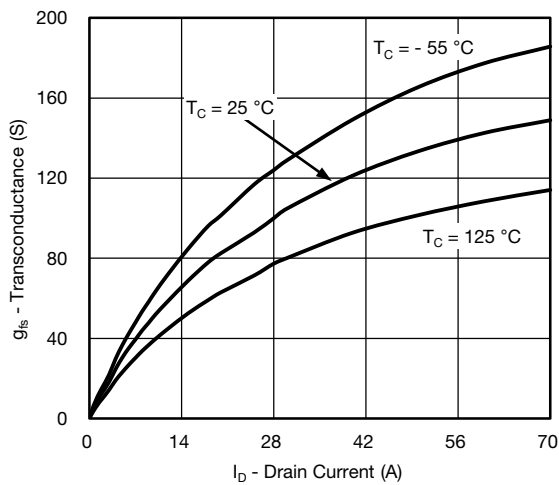
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



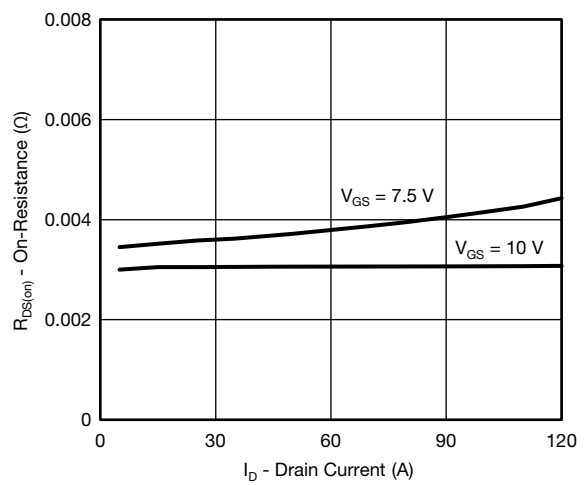
Output Characteristics



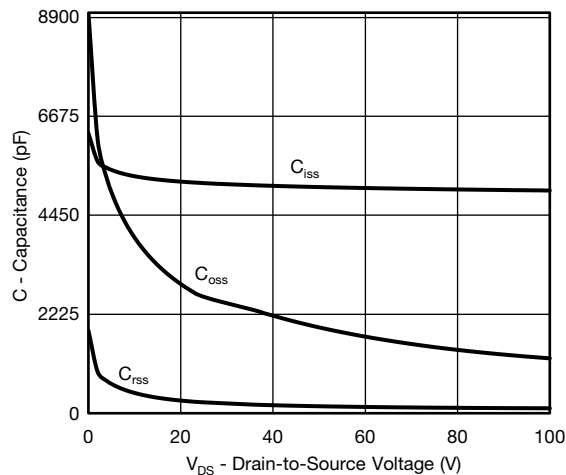
Transfer Characteristics



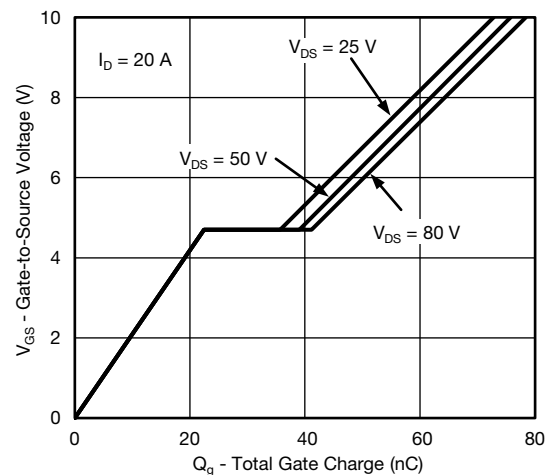
Transconductance



On-Resistance vs. Drain Current



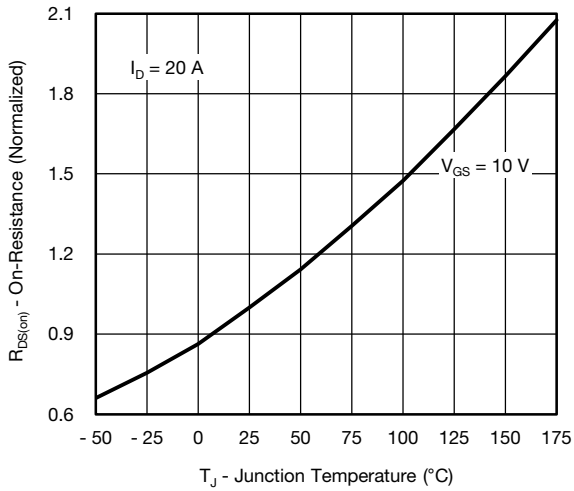
Capacitance



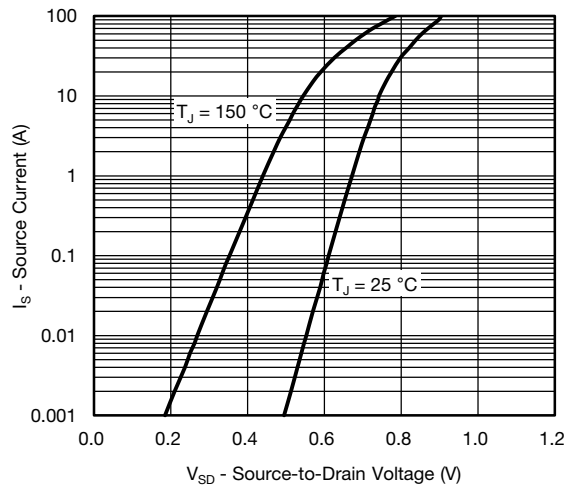
Gate Charge



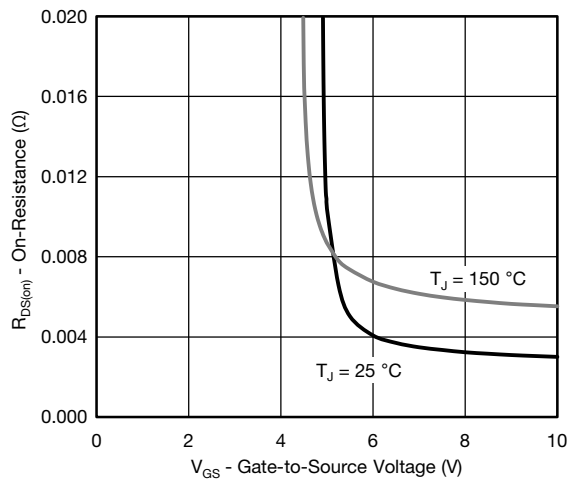
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



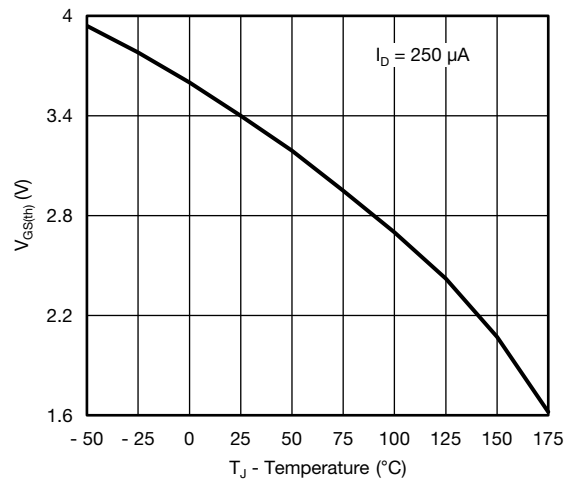
On-Resistance vs. Junction Temperature



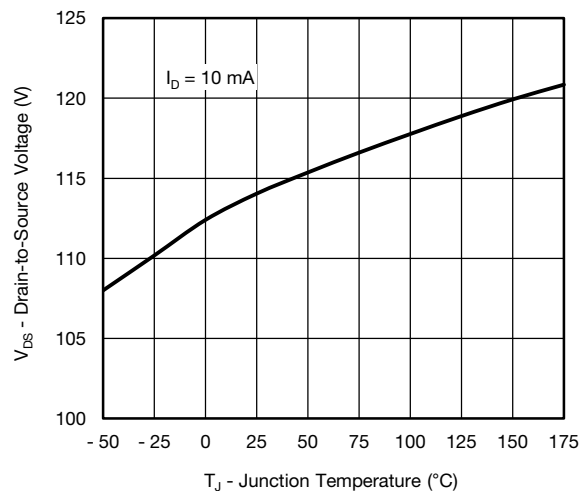
Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



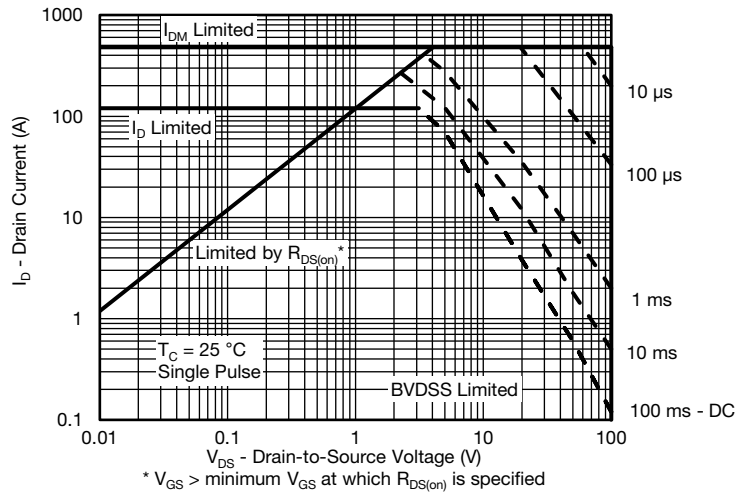
Threshold Voltage



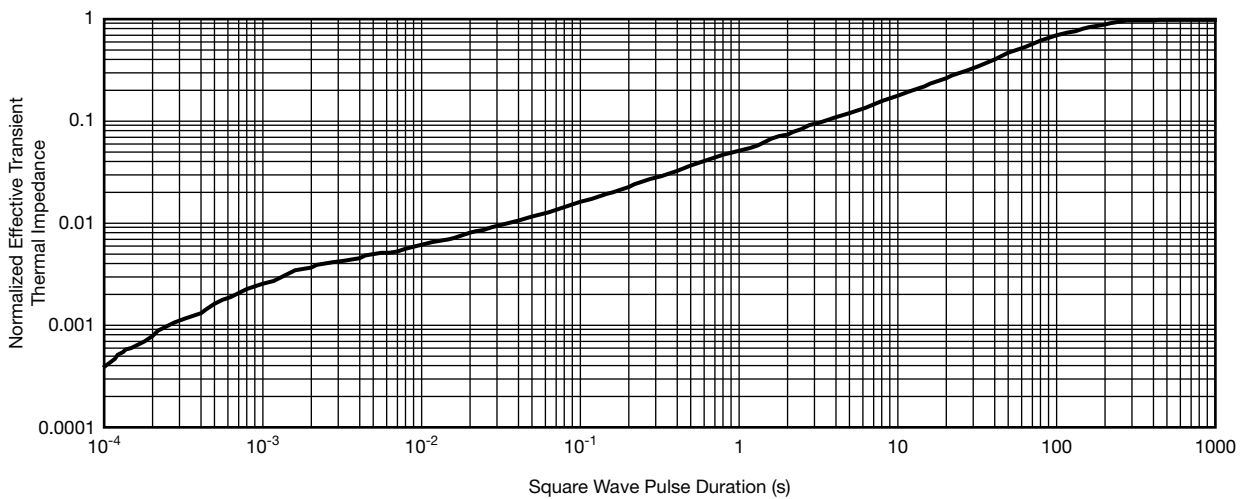
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



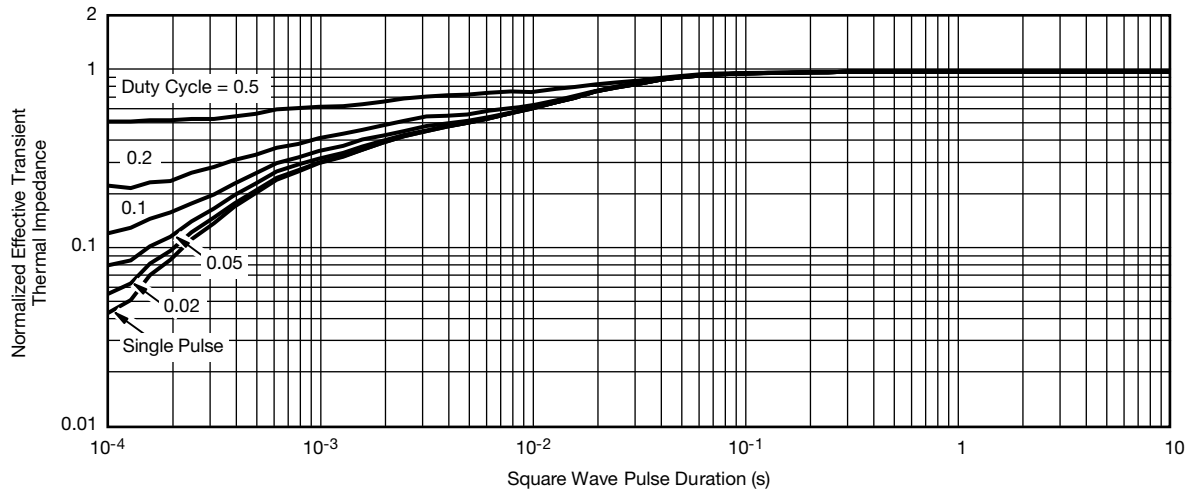
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



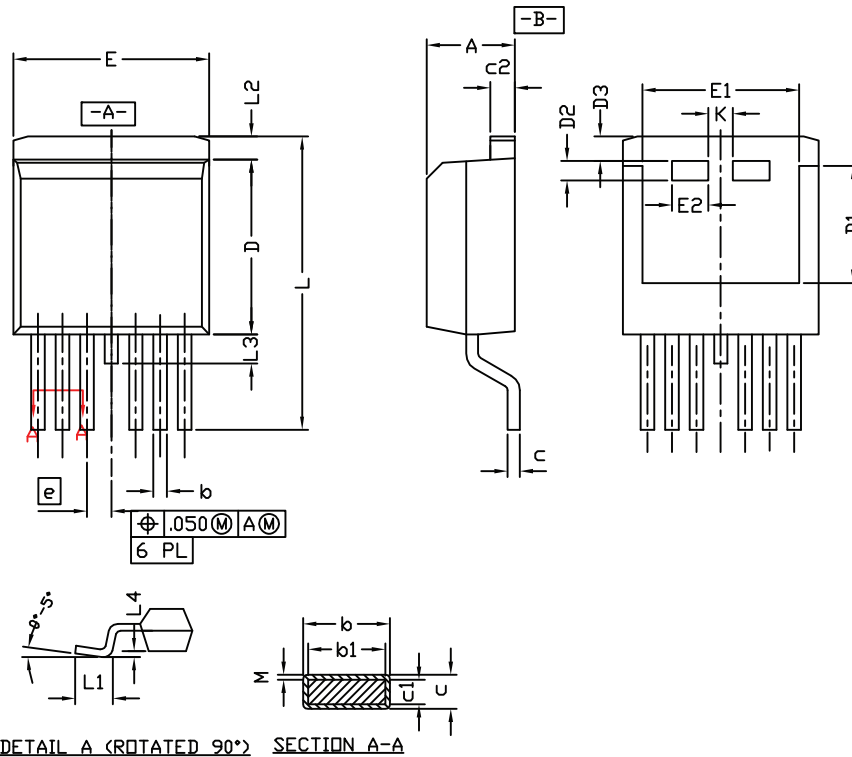
Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction to Case (25 °C)
- are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?65814.

D²PAK (TO-263-7L) Case Outline



DETAIL A (ROTATED 90°) SECTION A-A

Notes

1. Plane B includes maximum features of heat sink tab and plastic.
2. No more than 25 % of L1 can fall above seating plane by max. 8 mils.
3. Pin to pin coplanarity max. 4 mils.
4. Lead thickness 25 mils.
5. For SUM part numbers lead thickness is 24 mils to 29 mils.
6. For reference only.
7. Use inches as the primary measurement.
8. This feature is only for SUM.

DIM.	INCHES		MILLIMETERS	
	MIN.	MAX.	MIN.	MAX.
A	0.160	0.190	4.064	4.826
b	0.020	0.039	0.508	0.990
b1	0.020	0.035	0.508	0.889
b2	0.045	0.055	1.143	1.397
c* SUB	0.012	0.018	0.305	0.457
c* SUM	0.022	0.028	0.559	0.711
c1	0.018	0.025	0.457	0.635
c2	0.045	0.055	1.143	1.397
D	0.340	0.380	8.636	9.652
D1	0.220	0.240	5.588	6.096
D2	0.038	0.042	0.965	1.067
D3	0.045	0.055	1.143	1.397
E	0.380	0.410	9.652	10.414
E1	0.245	-	6.223	-
E2	0.072	0.078	1.829	1.981
e	0.050 BSC		1.27 BSC	
K	0.045	0.055	1.143	1.397
L	0.575	0.625	14.605	15.875
L1	0.090	0.110	2.286	2.794
L2	0.040	0.055	1.016	1.397
L3	0.050	0.070	1.270	1.778
L4	0.010 BSC		0.254 BSC	
M	-	0.002	-	0.050
ECN: T13-0709-Rev. B, 30-Sep-13				
DWG: 6006				



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