

BIG8051™

User manual

All MikroElektronika's development systems represent irreplaceable tools for programming and developing microcontroller-based devices. Carefully chosen components and the use of machines of the last generation for mounting and testing thereof are the best guarantee of high reliability of our devices. Due to simple design, a large number of add-on modules and ready to use examples, all our users, regardless of their experience, have the possibility to develop their project in a fast and efficient way.

Development system



SOFTWARE AND HARDWARE SOLUTIONS FOR EMBEDDED WORLD ...making it simple

TO OUR VALUED CUSTOMERS

I want to express my thanks to you for being interested in our products and for having confidence in Mikroelektronika.

The primary aim of our company is to design and produce high quality electronic products and to constantly improve the performance thereof in order to better suit your needs.



Nebojsa Matic
General Manager

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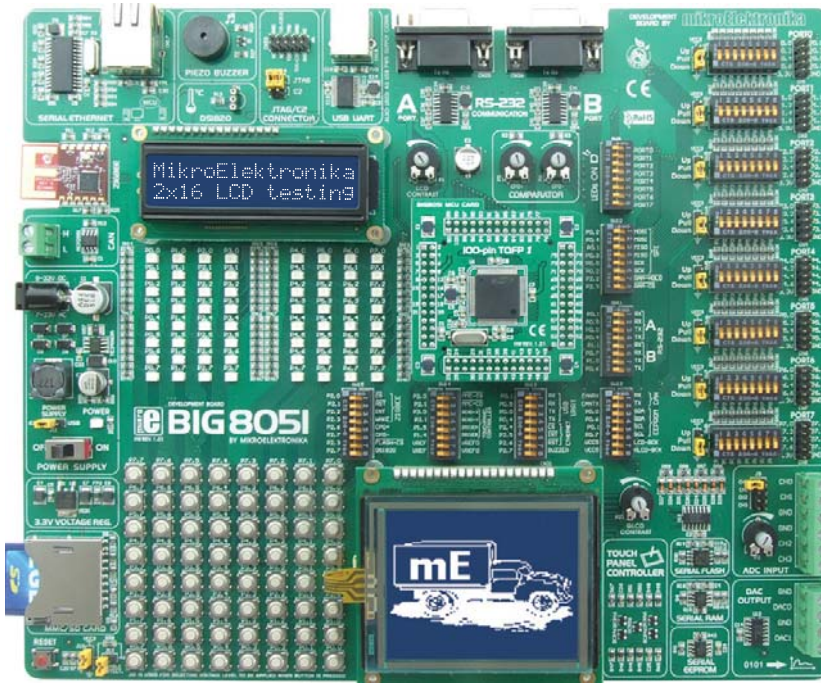
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General information

The *BIG8051* development system provides a development environment for programming and experimenting with 8051 microcontrollers from Silicon Laboratories®. Numerous modules, such as 128x64 graphic LCD display, 2x16 alphanumeric LCD display, piezo buzzer, CAN, ZigBee, etc, are provided on the board and allow you to easily simulate the operation of your target device.



Full-featured development system for 8051 microcontroller based devices



Analog to digital conversion of voltage signal



CAN communication module



Access to LAN network



Graphic LCD display with backlight



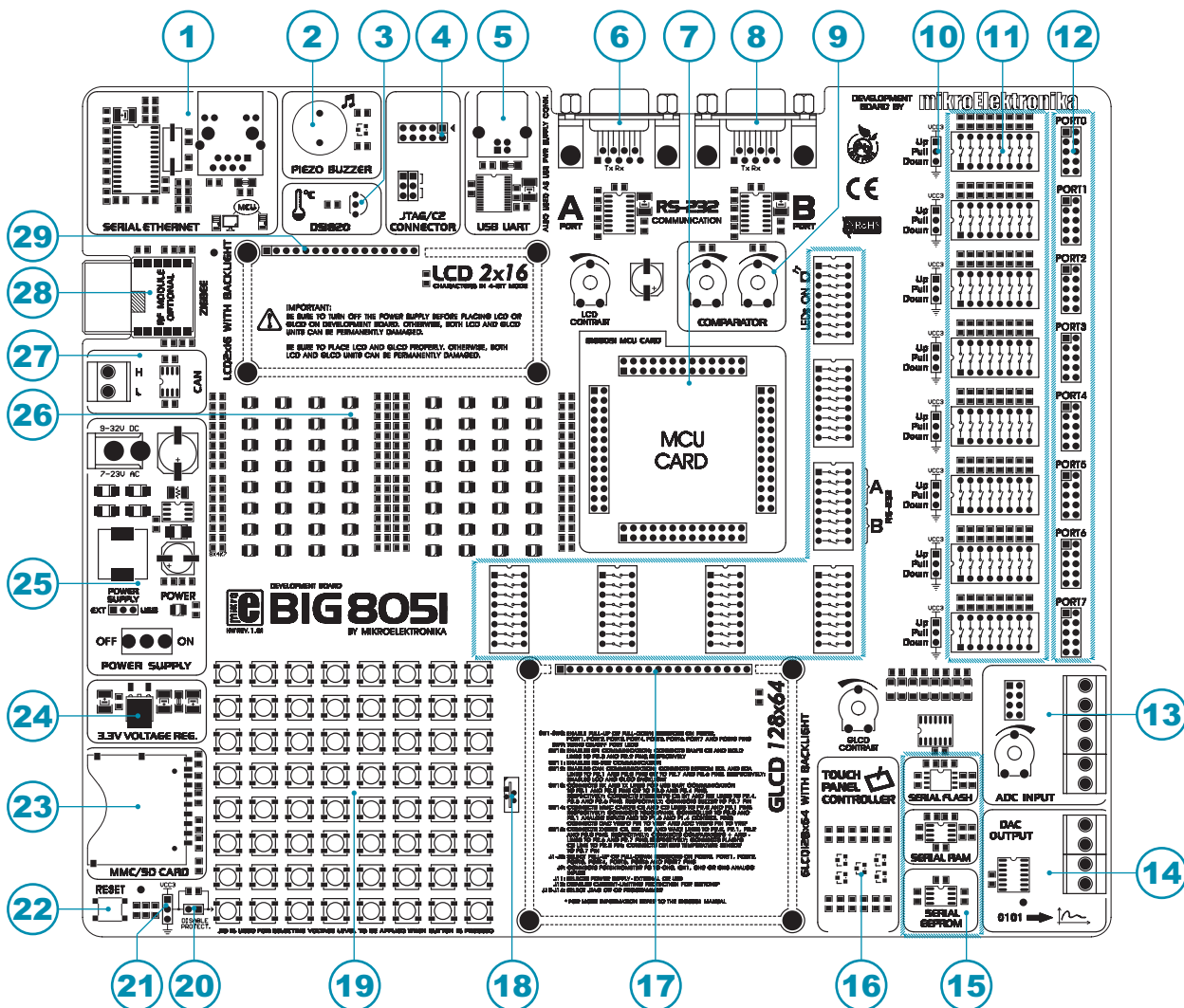
Package contains:

Development system: BIG8051
 CD: product CD with relevant software
 Cables: USB cable
 Documentation: Manual for BIG8051 and electrical schematic of the development system



System specification:

Power supply: over AC/DC connector (7-23V AC or 9-32V DC) or a USB cable (5V DC)
 Power consumption: 50mA when all on-board modules are off
 Dimensions: 26,5 x 22cm (10,4 x 8,6inch)
 Weight: ~420g (0.92lbs)



Key features

1. Ethernet module
2. Piezo buzzer
3. Connector for temperature sensor
4. Connector for programmer
5. USB UART module also used as a USB power supply connector
6. RS-232A module
7. Connector for MCU card
8. RS-232B module
9. Comparator
10. Jumpers used to select pull-up/pull-down resistors
11. DIP switches used to turn on pull-up/pull-down resistors
12. I/O ports
13. A/D converter inputs
14. D/A converter outputs
15. Memory modules
16. Touch panel controller
17. Connector for GLCD display
18. Connector for touch panel
19. Push buttons
20. Jumper used to shorten protective resistor
21. Jumper used to select push buttons' logic state
22. Reset button
23. Connector for MMC/SD cards
24. 3.3V voltage regulator
25. Power supply module
26. LEDs
27. CAN module
28. ZigBee module
29. Connector for LCD display

1.0. Connecting the development system to a power supply

In order to enable the development system to be turned on, it is necessary to provide the power supply voltage over an AC/DC connector CN19, Figure 1-1.

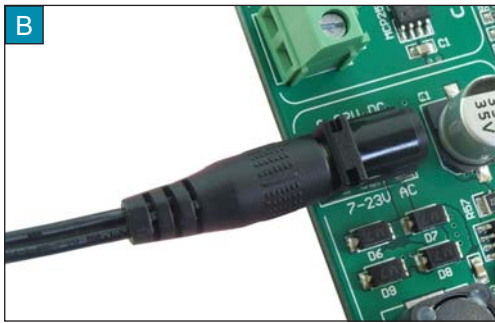
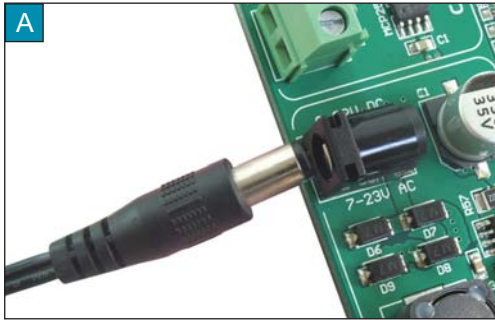


Figure 1-1: Providing power supply voltage

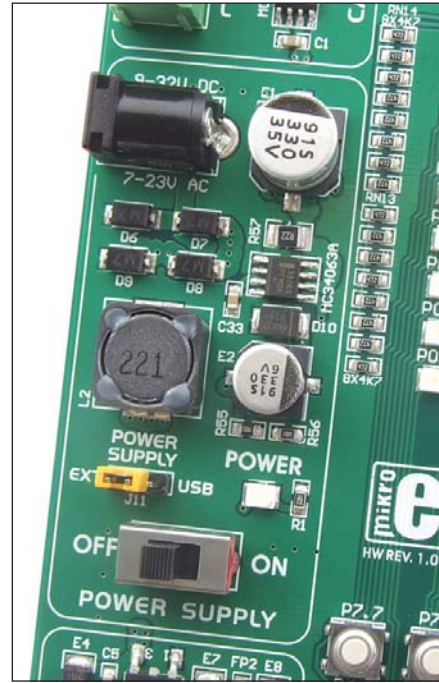


Figure 1-2: Power supply module

The power supply voltage provided via the CN19 AC/DC connector may be in a range between 9 and 32V DC or 7 and 23V AC. Before you turn the development system on, it is necessary to place jumper J11 in the EXT position.

When the development system is powered, it is necessary to set switch marked POWER SUPPLY to the ON position. As soon as the development system is turned on, a green LED marked POWER will automatically illuminate.

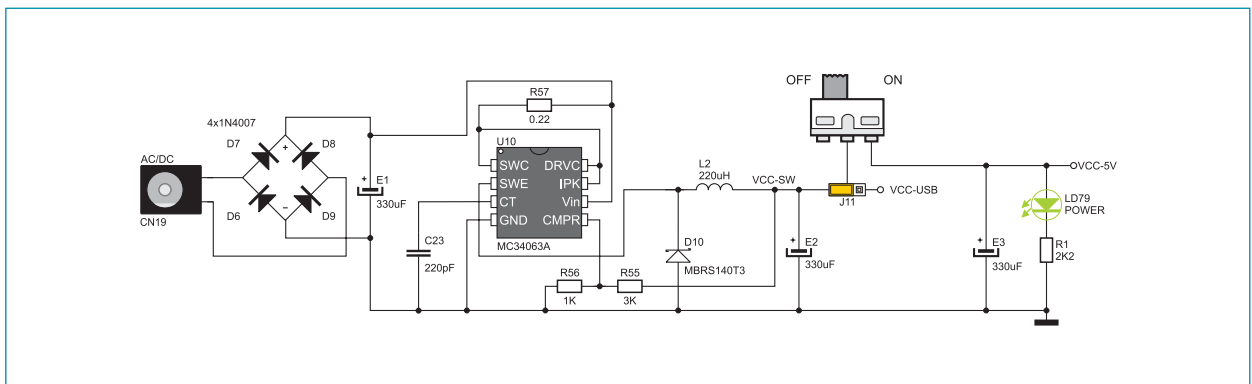


Figure 1-3: Power supply module connection schematic

2.0. C8051F040 microcontroller

The *BIG8051* development system comes with a 100-pin microcontroller C8051F040 in TQFP package. The microcontroller is soldered on the MCU card. It's key features are as follows:

- Pipelined instruction architecture executes 70% of instruction set in 1 or 2 system clocks;
- Up to 25 MIPS throughput with 25 MHz clock;
- 4352 bytes internal data RAM (4 k + 256);
- 64 kB Flash; in-system programmable in 512-byte sectors;
- Bosch Controller Area Network (CAN 2.0B), hardware SMBus™ (I2C™ Compatible), SPI™, and two UART serial ports available concurrently;
- Internal calibrated programmable oscillator: 3 to 24.5 MHz; etc.

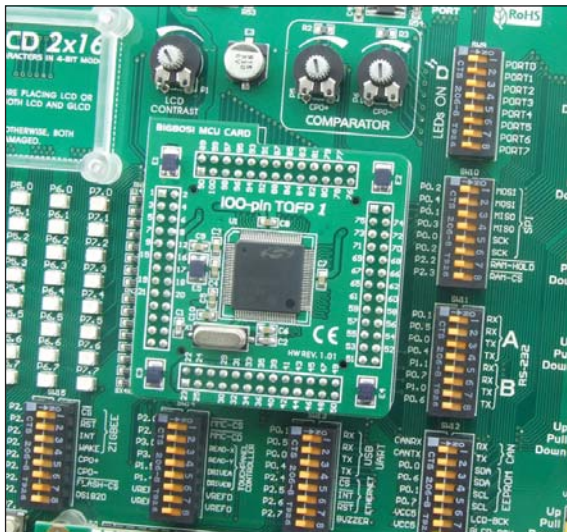


Figure 2-1: Microcontroller C8051F040

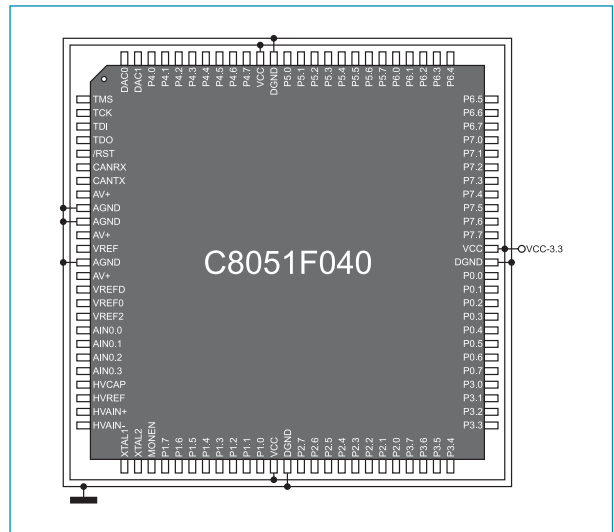


Figure 2-2: Microcontroller's pinout

The microcontroller provided on the development system can be easily replaced with another one. When choosing another microcontroller, it is very important to compare its and C8051F040's pinouts to make sure that they are compatible. If you use a microcontroller with different pinout, some on-board modules will probably not work. Anyway, if you decide to use another microcontroller, it is also necessary to provide an MCU card to solder the new microcontroller on.

3.0. Programming microcontroller

The C8051F040 microcontroller provided on the development system is programmed with a USB DEBUG ADAPTER that comes with the development system, Figure 3-1.



Figure 3-1: JTAG programmer

Before you start programming, it is necessary to turn the development system on. The external programmer is connected to the development system via a 2x5 connector CN23, Figure 3-4. In order to enable the JTAG programmer, jumpers J13 and J14 should be placed in the JTAG position, Figure 3-2A. In case the C2 programmer is used, jumpers 13 and J14 should be placed in the C2 position, Figure 3-2B.

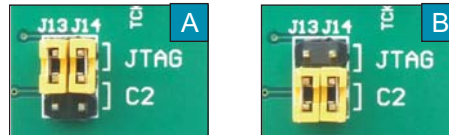


Figure 3-2: Position of jumpers 13 and 14



Figure 3-3: Connector for programmer

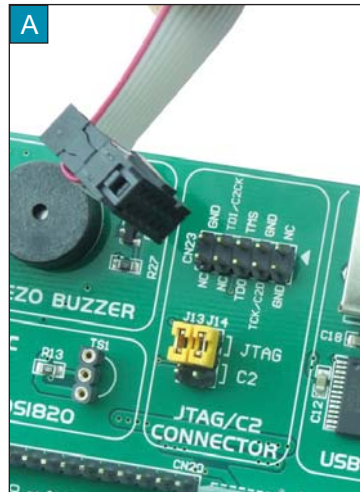
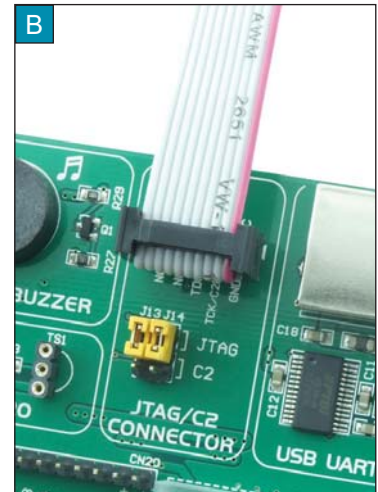


Figure 3-4: Connecting JTAG programmer with the development system



4.0. Ethernet module

The Ethernet module enables the development system to access the LAN network via the RJ45 connector. The operation of the Ethernet module is in compliance with the IEEE 802.3 standard. Communication between this module and the microcontroller is performed via the Serial Peripheral Interface (SPI). In order to enable the Ethernet module to be connected to the microcontroller, switches 1, 3 and 5 (optionally 2, 4 and 6) on the DIP switch SW10 as well as switches 5, 6 and 7 on the DIP switch SW13 should be set to the ON position, Figure 4-3.



Figure 4-1: Ethernet module

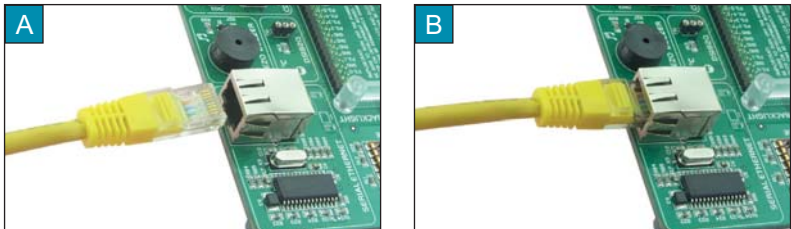


Figure 4-2: Connecting ethernet cable

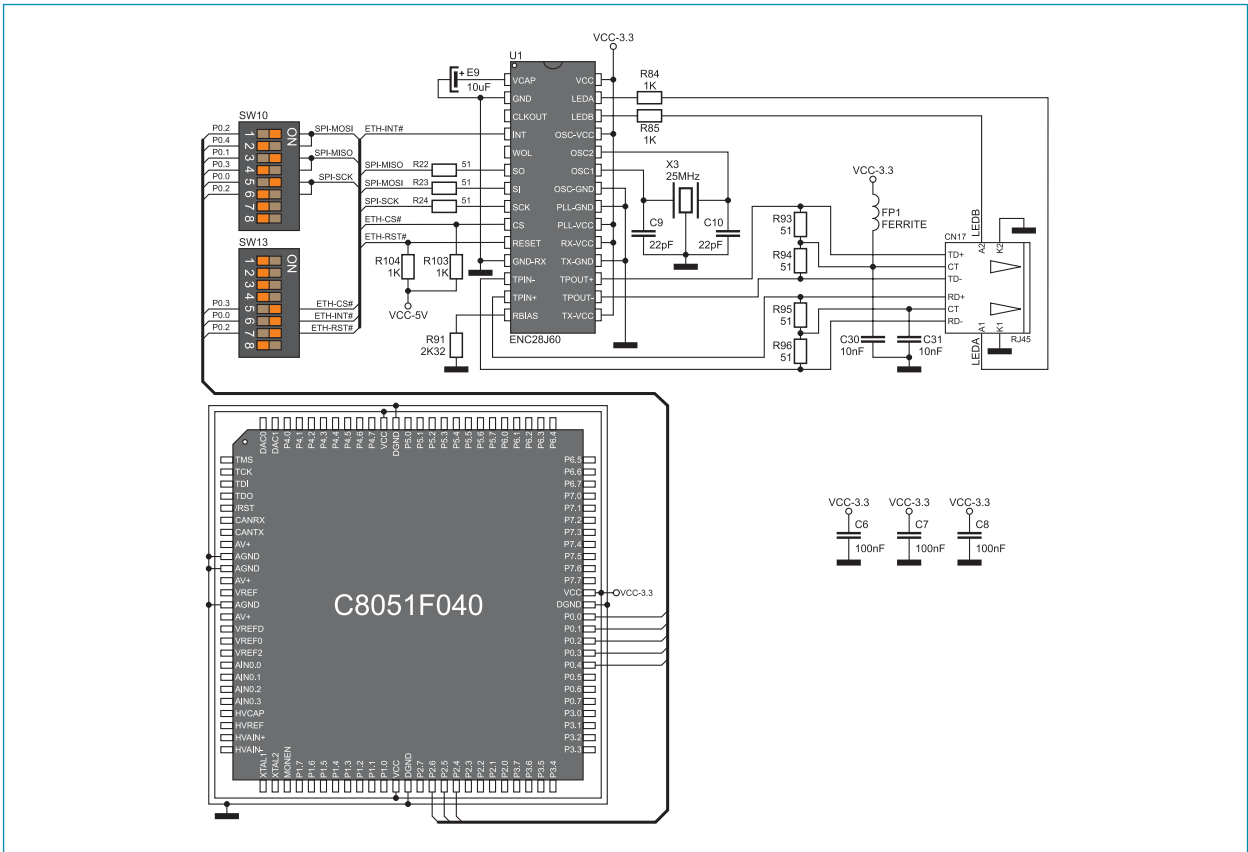


Figure 4-3: Ethernet module connection schematic

5.0. Piezo buzzer

Due to a built-in piezo buzzer, the *BIG8051* development system is capable of emitting audio signals. In order to enable the piezo buzzer to operate properly it is necessary to generate a voltage signal of specific frequency. Remember, when writing this code, that the piezo buzzer's resonant frequency is 3.8kHz. Other frequencies in the range between 20Hz and 20kHz can also be used, but the best performance is provided with frequencies ranging between 2kHz and 4kHz. To establish connection between the piezo buzzer and the microcontroller, it is necessary to set switch 8 on the DIP switch SW13 to the ON position.



Figure 5-1: Piezo buzzer

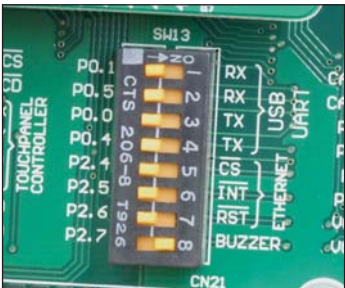


Figure 5-2: Piezo buzzer connected to the microcontroller pin P2.7

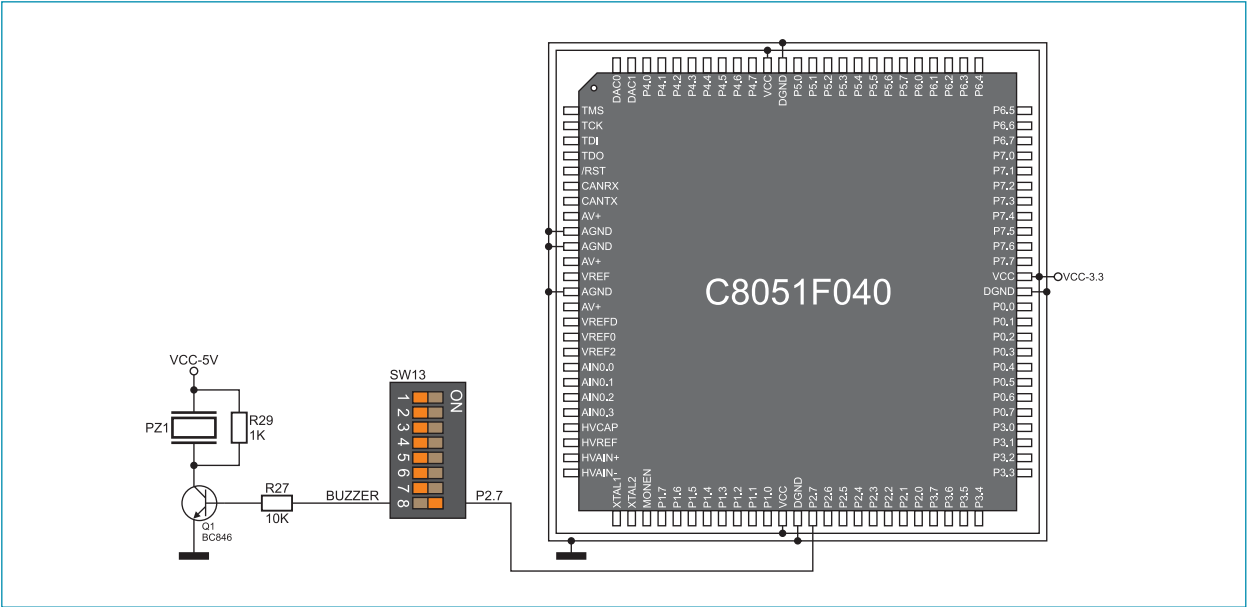


Figure 5-3: Piezo buzzer and microcontroller connection schematic

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10.0. ZigBee module

The *BIG8051* development system enables you to connect the ZigBee module that is used for wireless communication. Communication between this module and the microcontroller is performed via the Serial Peripheral Interface (SPI). To enable communication between them, it is necessary to set switches 1, 3 and 5 (optionally 2, 4 and 6) on the DIP switch SW10, as well as switches 1, 2, 3 and 4 on the DIP switch SW15 to the ON position.

The operation of the ZigBee module is in compliance with the IEEE 802.15.4-2003 standard related to wireless data transfer on short distances with low-power consumption. The MRF24J40MA ZigBee modul is used on the *BIG8051* development system. Some of its key features are: up to 250kbps baud rate, 2.4GHz operating frequency, ~20mA power consumption, up to 400m coverage, etc.



Figure 10-1: MRF24J40MA ZigBee module



Figure 10-2: Connector for ZigBee module

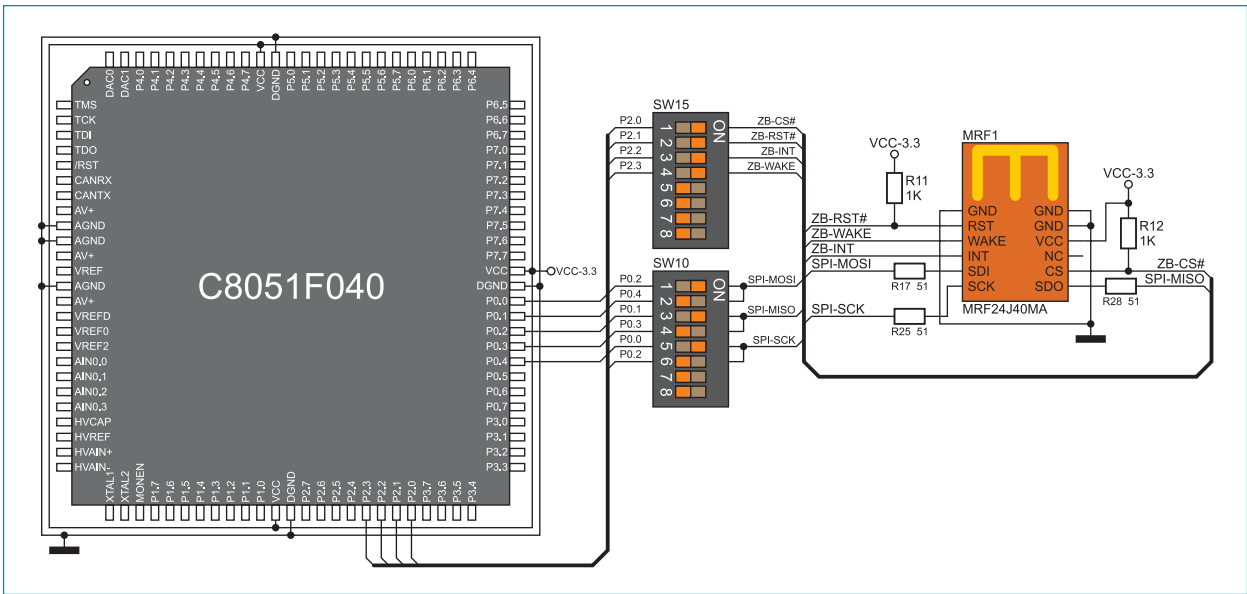


Figure 10-3: ZigBee module connection schematic

The schematic diagram illustrates the C8051F040 microcontroller interfaced with an MMC card and a voltage regulator. The microcontroller is shown with its pin connections to the MMC card and the voltage regulator. The MMC card is connected to the microcontroller via SPI and MMC signals. The voltage regulator (REG1) is used to provide a stable 5V supply (OVCC-5V) to the microcontroller.

Microcontroller Pin Connections:

- DA01-DA04:** DAC01, DAC02, DAC03, DAC04
- PA01-PA04:** PA01, PA02, PA03, PA04
- PA05-PA08:** PA05, PA06, PA07, PA08
- PA09-PA12:** PA09, PA10, PA11, PA12
- PA13-PA16:** PA13, PA14, PA15, PA16
- PA17-PA20:** PA17, PA18, PA19, PA20
- PA21-PA24:** PA21, PA22, PA23, PA24
- PA25-PA28:** PA25, PA26, PA27, PA28
- PA29-PA32:** PA29, PA30, PA31, PA32
- PA33-PA36:** PA33, PA34, PA35, PA36
- PA37-PA40:** PA37, PA38, PA39, PA40
- PA41-PA44:** PA41, PA42, PA43, PA44
- PA45-PA48:** PA45, PA46, PA47, PA48
- PA49-PA52:** PA49, PA50, PA51, PA52
- PA53-PA56:** PA53, PA54, PA55, PA56
- PA57-PA60:** PA57, PA58, PA59, PA60
- PA61-PA64:** PA61, PA62, PA63, PA64
- PA65-PA68:** PA65, PA66, PA67, PA68
- PA69-PA72:** PA69, PA70, PA71, PA72
- PA73-PA76:** PA73, PA74, PA75, PA76
- PA77-PA80:** PA77, PA78, PA79, PA80
- PA81-PA84:** PA81, PA82, PA83, PA84
- PA85-PA88:** PA85, PA86, PA87, PA88
- PA89-PA92:** PA89, PA90, PA91, PA92
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- PA161-PA164:** PA161, PA162, PA163, PA164
- PA165-PA168:** PA165, PA166, PA167, PA168
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- PA401-PA404:** PA401, PA402, PA403, PA404
- PA405-PA408:** PA405, PA406, PA407, PA408
- PA409-PA412:** PA409, PA410, PA411, PA412
- PA413-PA416:** PA413, PA414, PA415, PA416
- PA417-PA420**

Figure 11-3: MMC/SD connector and microcontroller connection schematic

12.0. Comparator

The *BIG8051* development system is capable of comparing voltage levels due to a comparator built into the microcontroller. Voltage signals are supplied via potentiometers P5 and P6. In order to enable voltage signals, to be compared, to be supplied to the microcontroller, it is necessary to set switches 5 and 6 on DIP switch SW15 to the ON position.

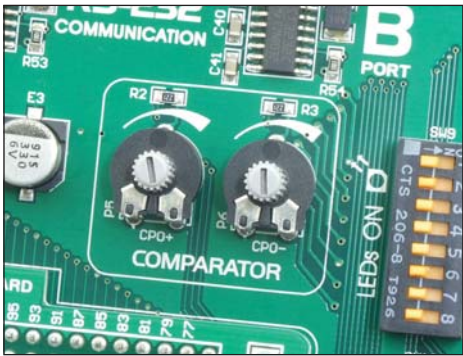
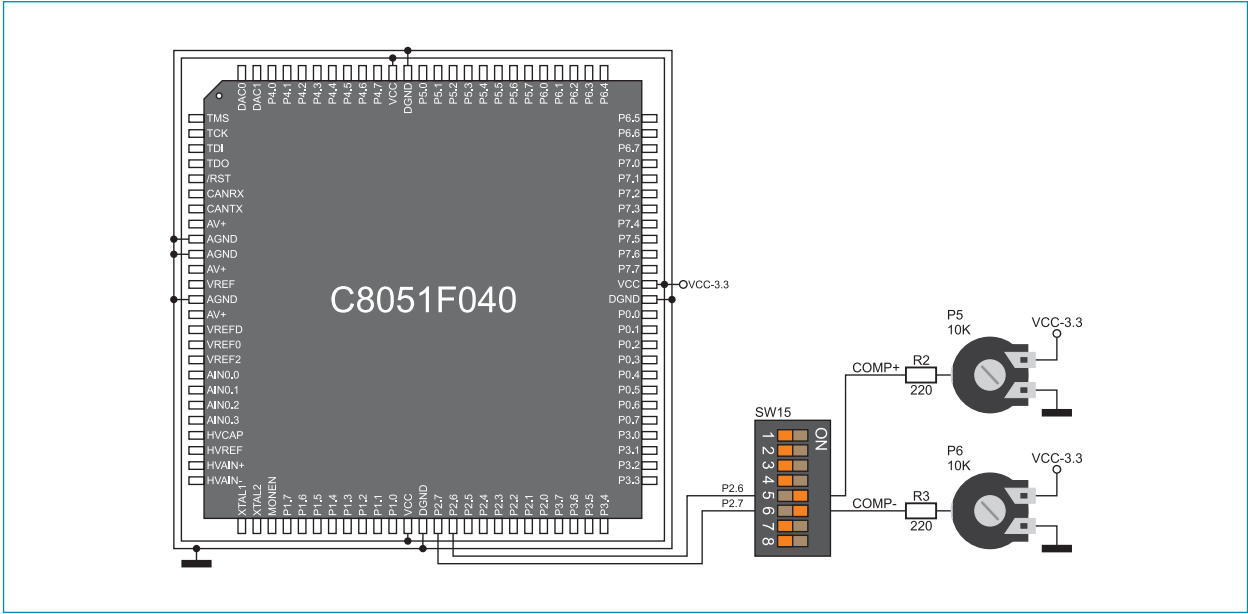


Figure 12-1: Comparator



13.0. ADC module

The ADC module is used to convert an analog voltage level into the appropriate 12-bit digital value. The analog voltage signal is supplied via screw terminals CN15 and CN16. The ADC module is built-in into the microcontroller. The voltage supplied from the VREF pin is used as a voltage reference. In order to use this voltage, switch 8 on the DIP switch SW14 should be set to the ON position.

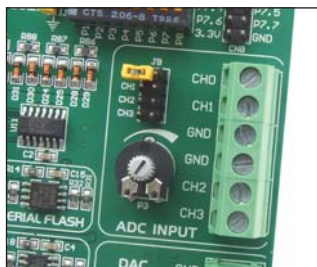


Figure 13-1: ADC module

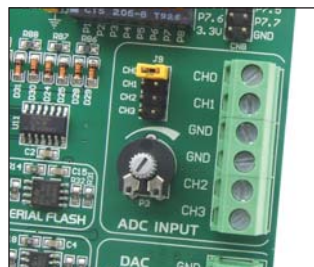


Figure 13-2: Jumper J9

If the voltage, necessary to perform A/D conversion, is not supplied from an external source, then the 3.3V power supply voltage is used. In this case, it is necessary to place jumper J9 on the appropriate pins, Figure 13-2.

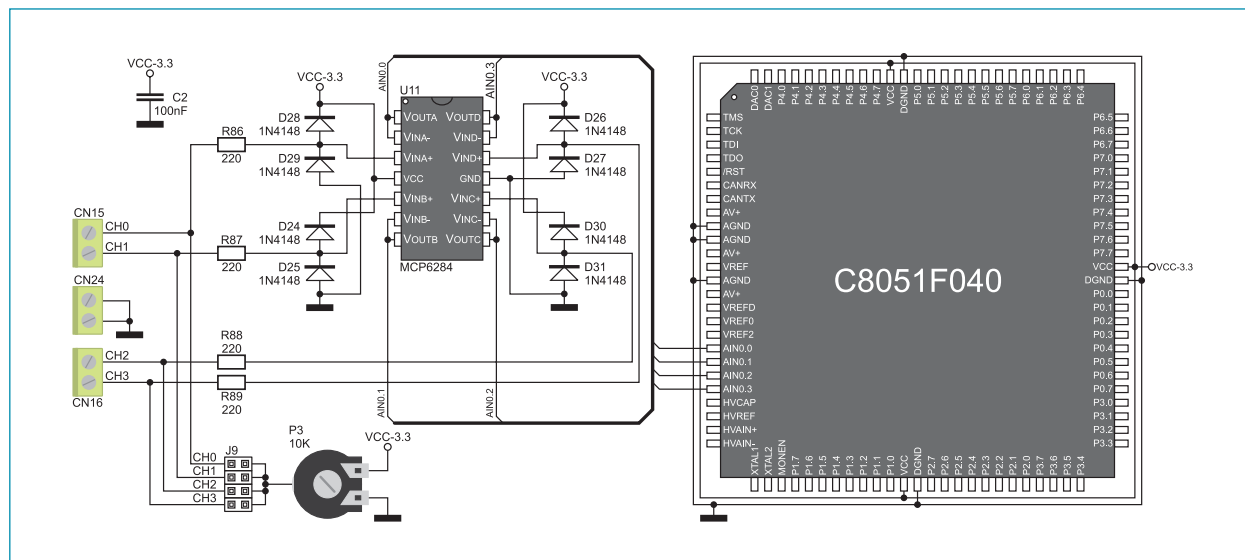


Figure 13-3: ADC module connection schematic

14.0. DAC module

A DAC module is used to convert 12-bit digital values into appropriate analog voltage values. The analog voltage signal is delivered via screw terminals CN13 and CN14. The DAC module is built into the microcontroller. The voltage supplied from the VREF pin on the microcontroller is used as a voltage reference. To enable the use of this voltage, switch 7 on the DIP switch SW14 should be set to the ON position.

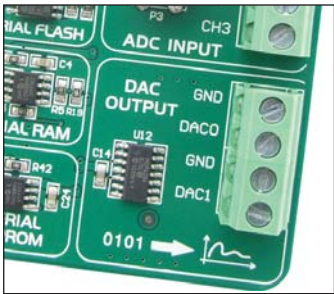


Figure 14-1: DAC module

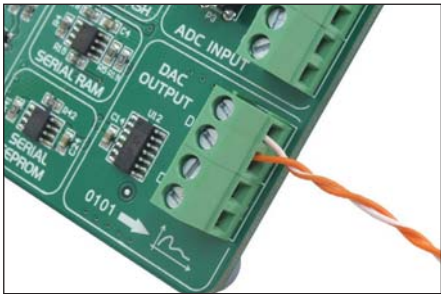


Figure 14-2: DAC module connector

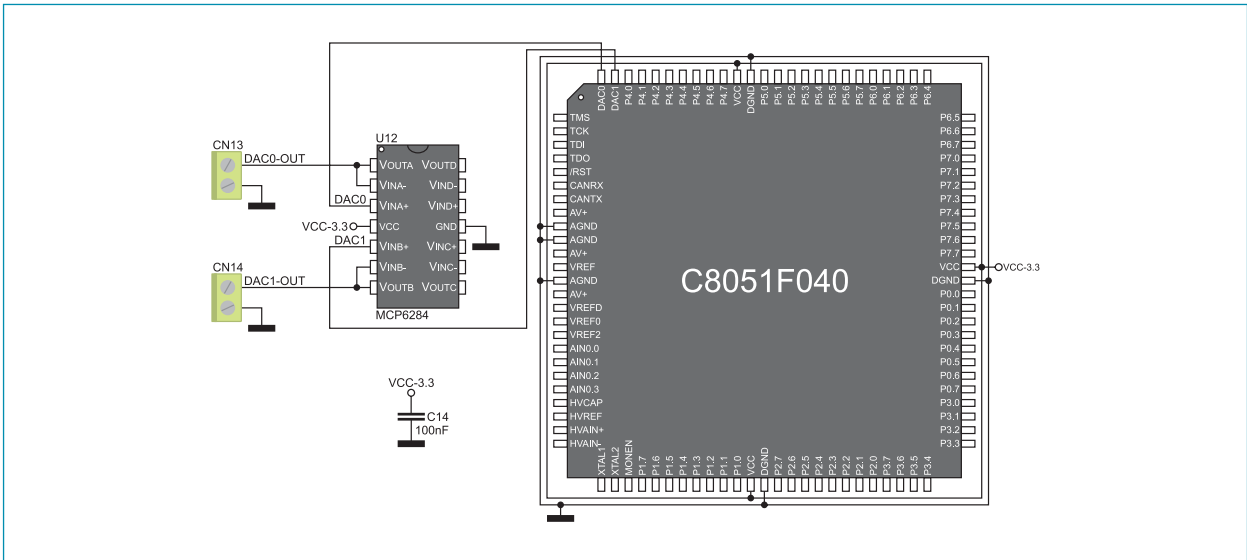


Figure 14-3: DAC module connection schematic

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RAM module enables the microcontroller to use additional 64Kbit RAM memory via the Serial Peripheral Interface (SPI). To establish connection between this module and the microcontroller, it is necessary to set switches 1, 3, 5, 7 and 8 (optionally 2, 4 and 6) on the DIP switch SW10 to the ON position.

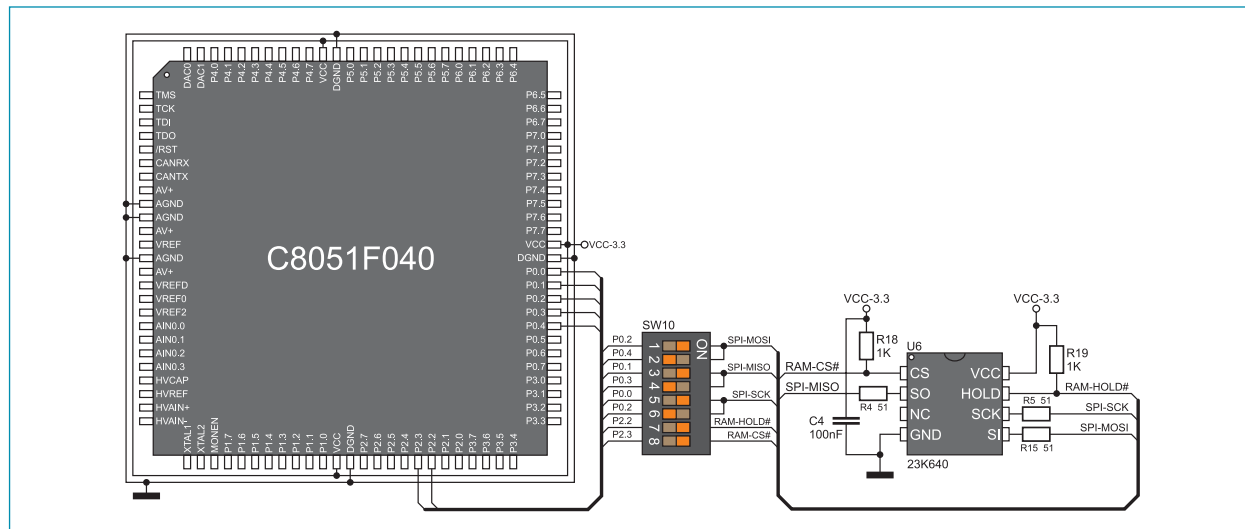


Figure 15-5: RAM module connection schematic

EEPROM module enables the microcontroller to use additional 1Kbit EEPROM memory via I2C serial connection. To establish connection between this module and the microcontroller, it is necessary to set switches 3 and 5 (optionally 4 and 6) on the DIP switch SW12 to the ON position.

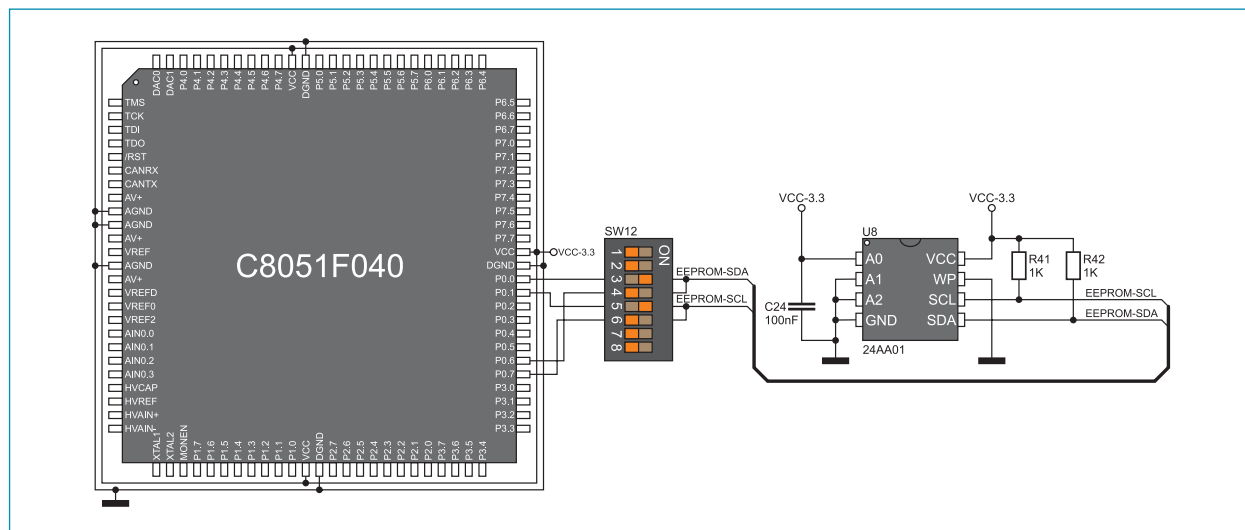


Figure 15-6: EEPROM module connection schematic

16.0. LEDs

There are 64 LEDs on the *BIG8051* development system used to visually indicate the state of each microcontroller I/O pin. An active LED indicates that a logic one (1) is present on the pin. In order to enable LEDs to illuminate, it is necessary to select the appropriate port ((PORT0, PORT1, PORT2, PORT3, PORT4, PORT5, PORT6 or PORT7) using DIP switch SW9.



Figure 16-1: LEDs

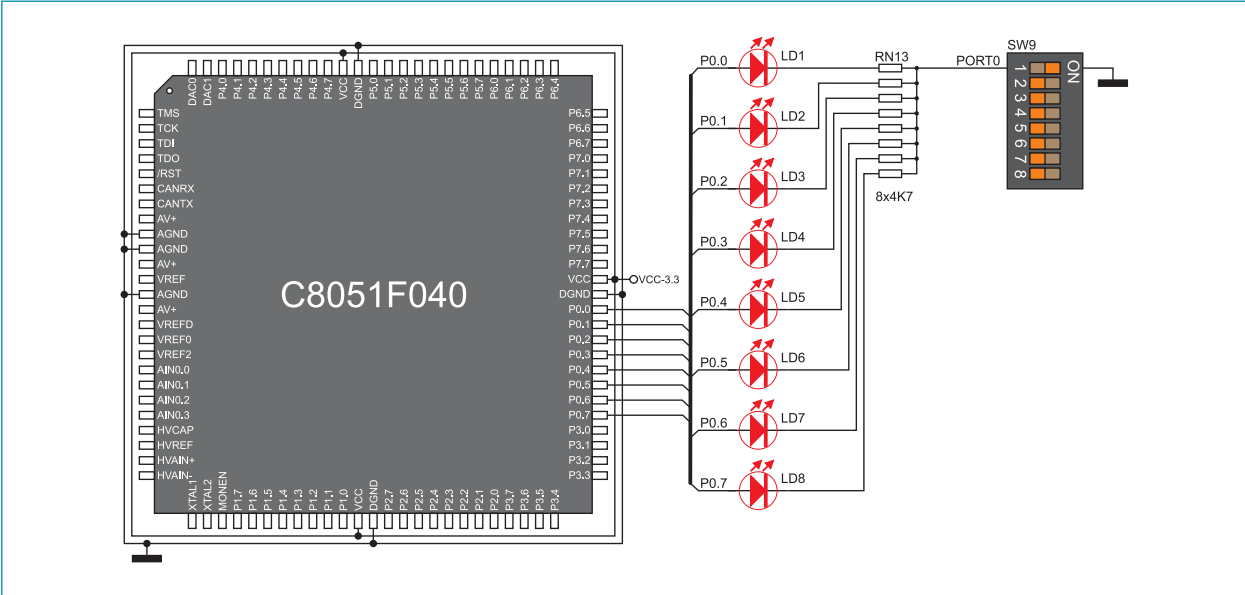
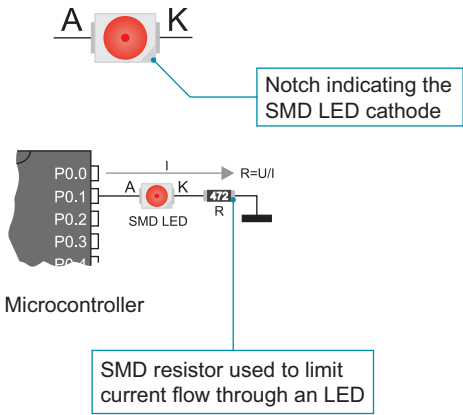


Figure 16-2: LED and port PORT0 connection schematic

17.0. Push buttons

The logic state of all microcontroller input pins may be changed by using push buttons. Jumper J10 is used to determine the logic state to be supplied on the appropriate microcontroller pin by pressing a push button. The function of the protective resistor is to limit the maximum current, thus preventing the development system and peripheral modules from being damaged in case a short circuit occurs. If needed, advanced users may shorten this resistor using jumper J12. Right next to the push buttons, there is a RESET button used to reset the microcontroller.

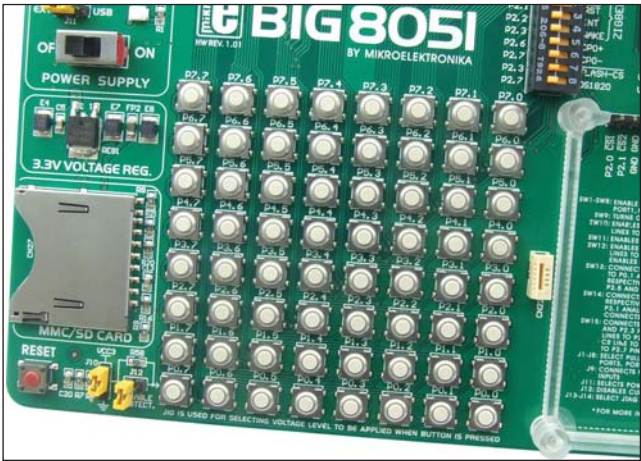


Figure 17-1: Push buttons

By pressing any push button when jumper J10 is in the VCC position, a logic one (3.3V) will be applied to the appropriate microcontroller pin, as shown in Figure 17-2.

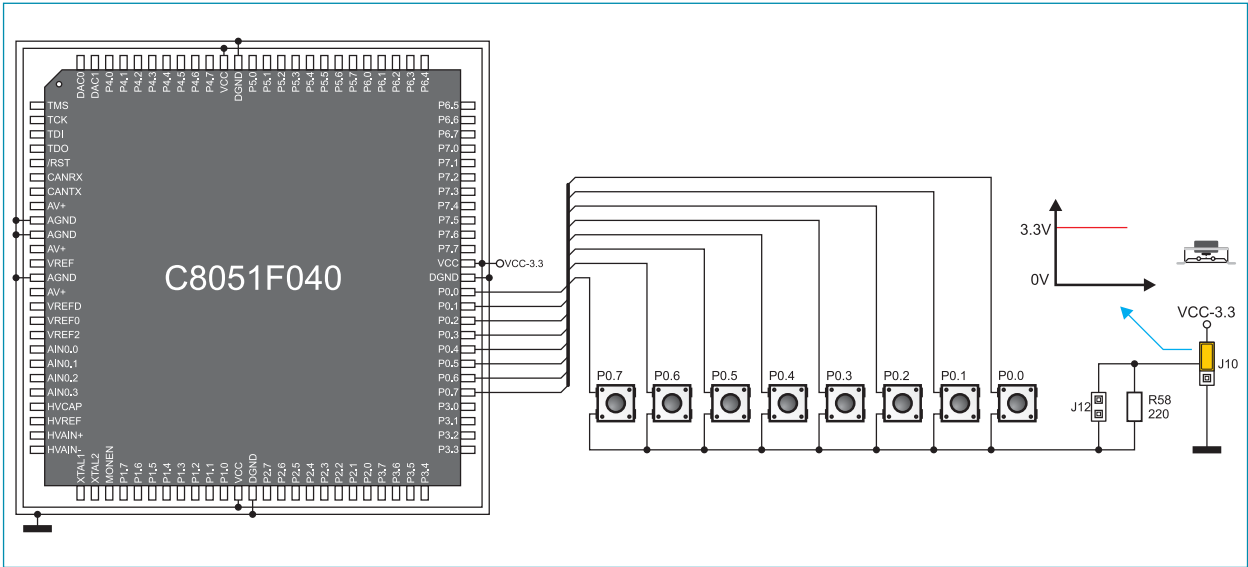


Figure 17-2: Push buttons and port PORT0 connection schematic

18.0. 2x16 LCD display

The *BIG8051* development system features an on-board connector for the alphanumeric 2x16 LCD display. This connector is linked to the microcontroller via port PORT3. Potentiometer P1 is used to adjust display contrast. The LCD-BCK switch on the DIP switch SW12 is used to turn the display backlight on/off.

Communication between this LCD and the microcontroller is performed in a 4-bit mode. Alphanumeric digits are displayed in two lines each containing up to 16 characters of 7x5 pixels.



Figure 18-1: Connector for alphanumeric LCD display



Figure 18-2: Alphanumeric 2x16 LCD display

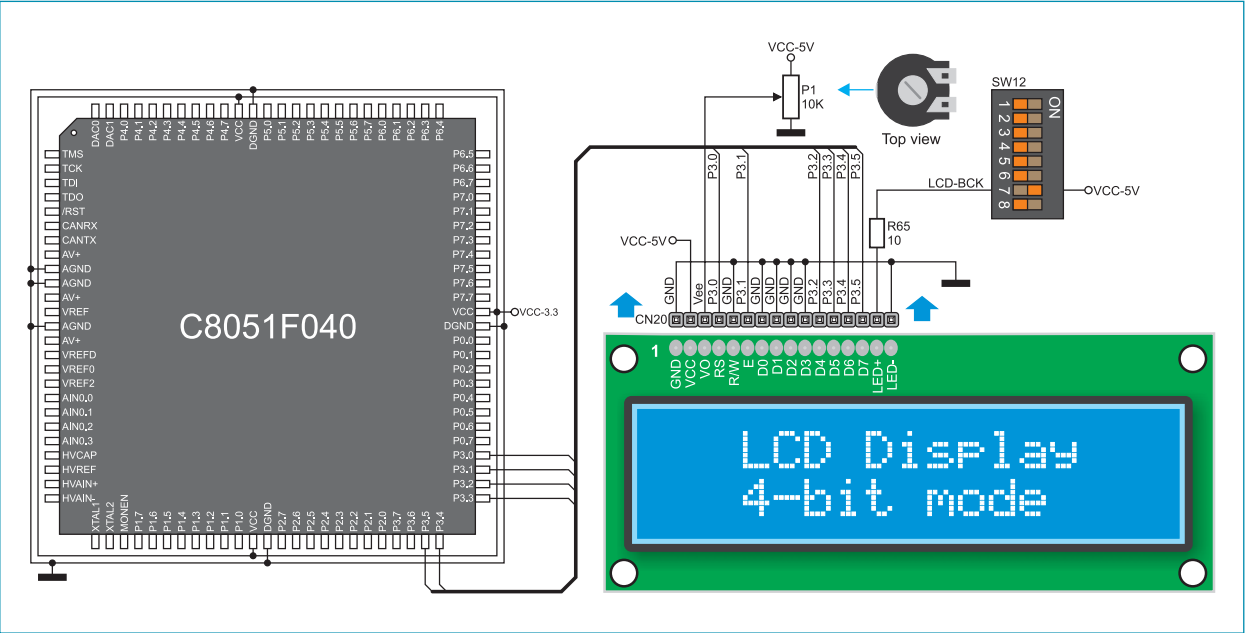


Figure 18-3: 2x16 LCD display connection schematic

page

A touch panel is a thin, self-adhesive, transparent, touch-sensitive panel. It is placed over a GLCD display. Its main function is to register pressure at some specific display point and to forward its coordinates in the form of analog voltage to the microcontroller. Switches 3, 4, 5 and 6 on the DIP switch SW14 are used to connect the microcontroller and touch panel.

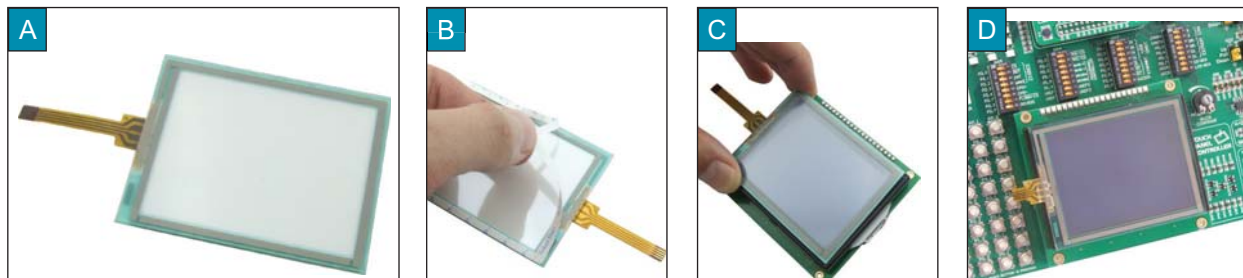


Figure 20-1: Placing touch panel over a GLCD

Figure 20-1 shows how to place a touch panel over a GLCD display. Make sure that the flat cable is to the left of the GLCD, as shown in Figure 20-1D.

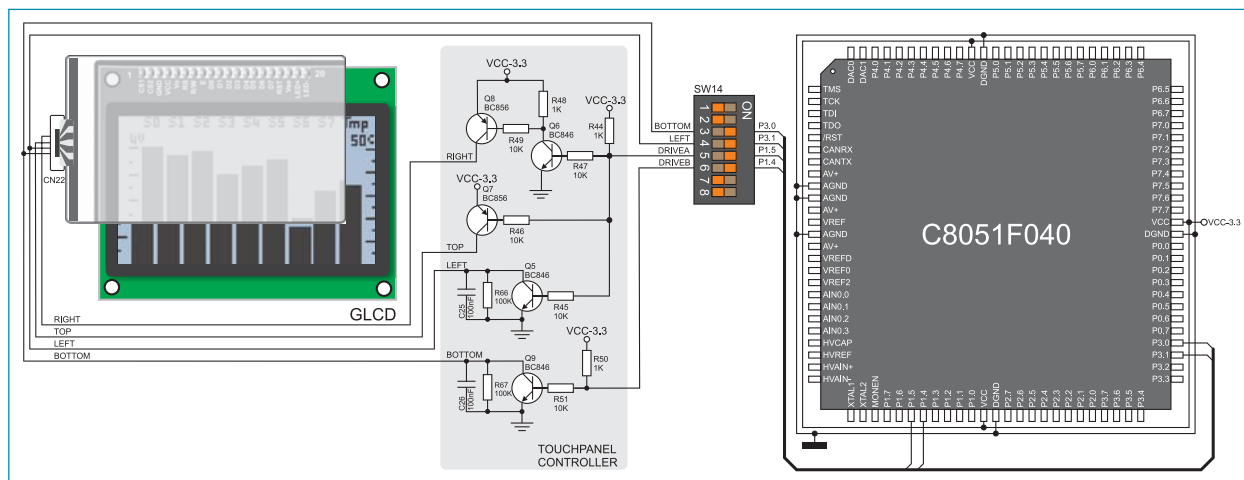


Figure 20-2: Touch panel connection schematic



Figure 20-3: Connecting touch panel

Figure 20-3 shows in detail how to connect a touch panel to the microcontroller. Bring the end of the flat cable close to the CN23 connector (Figure A). Plug the cable into the connector (Figure B) and press it easily so as to fully fit the connector (Figure C). Now, a GLCD can be plugged into the appropriate connector (Figure D).

NOTE: LEDs and pull-up/pull-down resistors on ports PORT1 and PORT3 should be off when the touch panel is in use.

21.0. Input/output ports

Along the right side of the development system, there are eight 10-pin connectors linked to the microcontroller I/O ports. Due to DIP switches SW1-SW8, every connector pin can be connected to one pull-up/pull-down resistor. It depends on the position of jumpers J1-J8 whether they will be connected to pull-up or pull-down resistor.

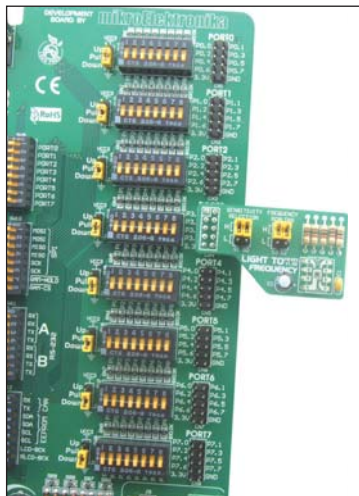


Figure 21-1: I/O ports



Figure 21-2: J9 in pull-down position

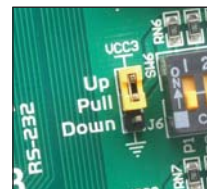


Figure 21-3: J9 in pull-up position

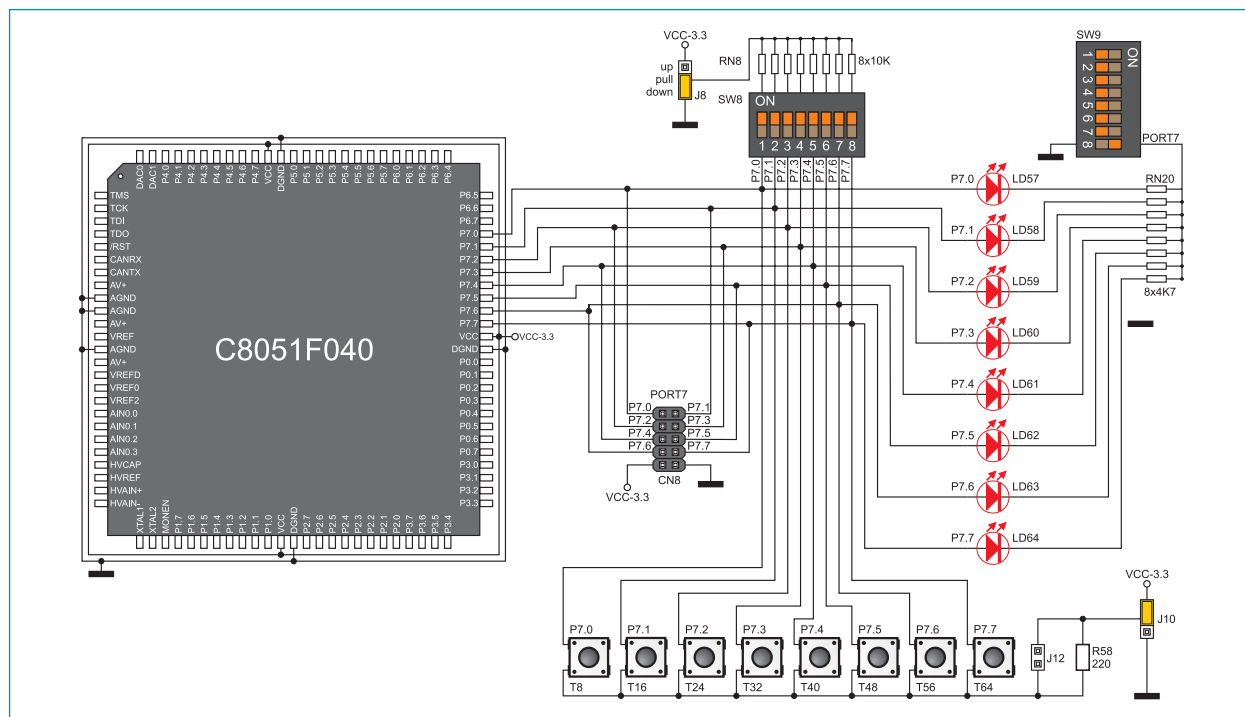


Figure 21-4: Port PORT7 connection schematic

Pull-up/pull-down resistors enable you to set the logic level on all microcontroller's input pins when they are in idle state. This level depends on the position of the pull-up/pull-down jumper. The RP7.0 pin with the relevant DIP switch SW8, jumper J8 and P7.0 push button with jumper J10 are used here for the purpose of explaining the performance of pull-up/pull-down resistors. The principle of their operation is the same for all other microcontroller pins.

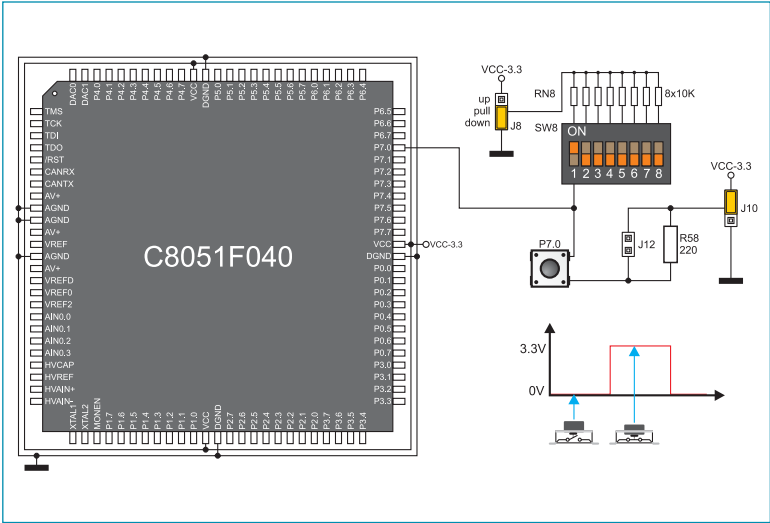


Figure 21-5: Jumper J8 in pull-down and jumper J10 in pull-up position

In order to enable the PORT7 port pins to be connected to pull-down resistors, it is necessary to place jumper J8 in the *Down* position first. This enables any PORT7 port pin to be supplied with a logic zero (0V) in idle state over jumper J8 and 8x10k resistor network. To provide the P7.0 pin with this signal, it is necessary to set switch P7.0 on the DIP switch SW8 to the ON position.

As a result, every time you press the P7.0 push button, the P7.0 pin will be fed with a logic one (VCC-3.3 voltage), provided that jumper J10 is placed in the VCC-3.3 position.

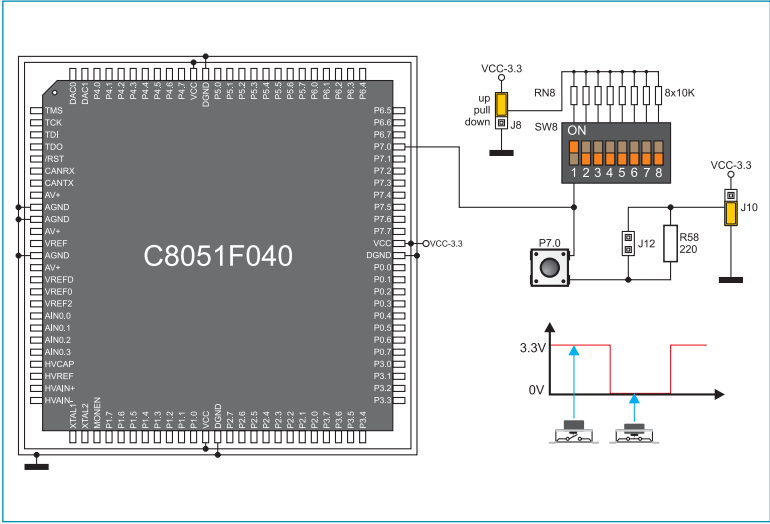


Figure 21-6: Jumper J8 in pull-up and jumper J10 in pull-down position

In order to enable port PORT7 pins to be connected to pull-up resistors and the port input pins to be supplied with a logic zero (0), it is necessary to place jumper J8 in the *Up* position and jumper J10 in the GND position. This enables any port PORT7 input pin, when it is in idle state, to be driven high (3.3V) over the 10k resistor.

As a result, every time you press the P7.0 push button, the P7.0 pin will be fed with a logic zero (0V), provided that switch P7.0 on the DIP switch SW8 is set to the ON position.

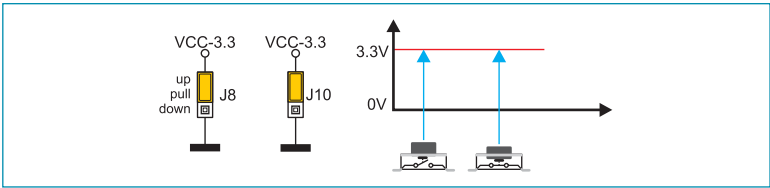


Figure 21-7: Jumpers J8 and J10 in the same positions

In case that jumpers J8 and J10 are in the same positions, pressure on any button will not cause input pins to change their logic state.

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