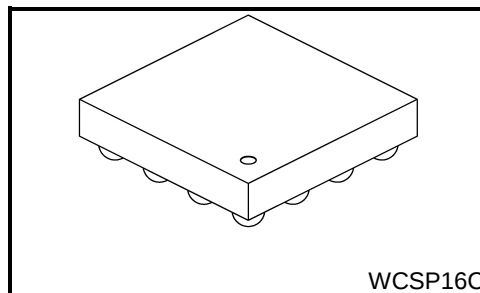


TCK321G, TCK322G

36 V, Dual Inputs – Single Output Power Multiplexer IC with Over Voltage Protection

The TCK321G and TCK322G are 36 V high input voltage Dual Inputs-Single Output multiplexer load switch ICs. It has Over Voltage Protection featuring low switch ON resistance, high output current and wide input voltage operation. Switch ON resistance is only 98 mΩ at 4.5 V, - 1.0 A load conditions. And these feature a slew rate control driver, thermal shutdown and flag function. Also it can block reverse current if switch turned off. Output current is available up to 2.0 A per channel. Thus this is suitable for power management selector such as Battery Charge application.

This device is available in 0.5 mm pitch small package WCSP16C (1.9 mm x 1.9 mm, t: 0.5 mm (typ.)). Thus this devices is ideal for portable applications that require high-density board assembly such as mobile phone.

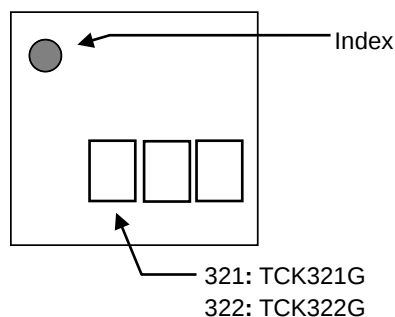


Weight : 3.9 mg (typ.)

Feature

- High output current: I_{OUT} (DC) = 2 A, per channel
- Low ON resistance : R_{ON} = 98 mΩ (typ.) at V_{IN} = 4.5 V, - 1.0 A, per channel
- Wide input voltage operation: V_{IN} = 2.3 to 36 V
- Over Voltage Lockout : 12.0 V, 15.0 V (typ.)
- Under Voltage Lockout: 2.9 V (typ.)
- Reverse current blocking per channel(SW OFF state)
- Inrush current reducing circuit.
- Auto selection mode
- Manual selection mode
- Break Before Make switch
- Thermal Shutdown function
- Small package: 0.5 mm pitch WCSP16C (1.9 mm x 1.9 mm, t: 0.5 mm (typ.)), PD = 1.65 W

Top marking



Start of commercial production
2015-10

Absolute Maximum Ratings (Ta = 25°C)

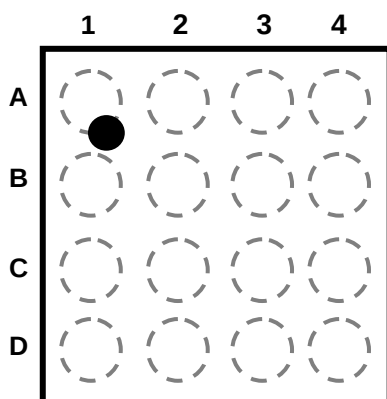
Characteristics	Symbol	Rating		Unit
Input voltage	V _{INA} , V _{INB}	-0.3 to 40		V
Control voltage	V _{CNT} , V _{SEL}	-0.3 to 6		V
Output voltage	V _{OUT}	-0.3 to 18		V
FLAG voltage	V _{FLAG}	-0.3 to 6		V
Output current	I _{OUT}	DC	2.0	A
		Pulse	3.0 (Note 1)	
Power dissipation	P _D	1.65 (Note 2)		W
Operating temperature range	T _{opr}	-40 to 85		°C
Junction temperature	T _j	150		°C
Storage temperature	T _{stg}	-55 to 150		°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: 1 ms pulse, 1% duty cycle

Note2: Rating at mounting on a board: FR4 board. (40 mm × 40 mm × 1.6 mm, Cu 4 layer)

Pin Assignment (Top view/Bottom bump)



	1	2	3	4
A	FLAG	V _{SEL}	CNT	GND
B	V _{INA}	V _{OUT}	V _{OUT}	V _{INB}
C	V _{INA}	V _{OUT}	V _{OUT}	V _{INB}
D	V _{INA}	V _{OUT}	V _{OUT}	V _{INB}

Product list

Part number	Over voltage lockout V _{INA}	Over voltage lockout V _{INB}	FLAG monitored in auto selection mode
TCK321G	12.0 V (typ.)	12.0 V (typ.)	Q1
TCK322G	15.0 V (typ.)	15.0 V (typ.)	Q1

Please ask your local retailer about the devices with other OVLO, logic and functions.

The block diagram illustrates the internal architecture of the L6920B. It features two input voltage dividers for V_{INA} and V_{INB} , each with an associated OVLO/UVLO (Overvoltage/Under Voltage Lockout) block. The V_{SEL} pin is connected to a divider with a VOP (Voltage Output Protection) block. The Control Logic block is the central hub, receiving inputs from the OVLO/UVLO blocks, the Charge Pump, and the Thermal Shutdown block. It also controls the Inrush Current Reducing block and the output transistors Q_1 and Q_2 . The output transistors Q_1 and Q_2 are connected to the output V_{OUT} and include Reverse Current Blocking. The Q_3 transistor is an Open Drain output connected to the FLAG pin. The GND pin is connected to the common ground.

PIN	Name	Description
A1	FLAG	Open drain acknowledge signal output.
A2	V _{SEL}	Input selector function. It is internally connected to VOP(Pull up).
A3	CNT	Mode control function. It is internally connected to GND(Pull down)
A4	GND	Ground
B1,C1,D1 B4,C4,D4	V _{INA} ,V _{INB}	Input. Each has Over Voltage Lock Out (OVLO) and Under Voltage Lock Out function (UVLO).
B2,C2,D2 B3,C3,D3	V _{OUT}	Output.

Operation Logic Table

		CNT Low	CNT High
V_{SEL} Low	V_{INA} Q₁	OFF	OFF
	V_{INB} Q₂	OFF	ON
	FLAG Q₃	OFF	ON (When V _{INA} or V _{INB} is out of regular voltage)
	Reverse current block	Q ₁ Active / Q ₂ Active	Q ₁ Active / Q ₂ Inactive
V_{SEL} High	V_{INA} Q₁	Auto selection mode Supplied V _{INA} ; Q ₁ and Q ₃ ON, Q ₂ OFF Supplied V _{INB} ; Q ₂ ON, Q ₁ and Q ₃ OFF Supplied V _{INA} and V _{INB} ; Q ₁ and Q ₃ ON, Q ₂ OFF	ON
	V_{INB} Q₂		OFF
	FLAG Q₃		ON (When V _{INA} or V _{INB} is out of regular voltage)
	Reverse current block		Q ₁ Inactive / Q ₂ Active

DC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol	Test Condition	Ta = 25°C			Ta = -40 to 85°C		Unit
			Min	Typ.	Max	Min	Max	
Input voltage	V _{IN}	—	2.3	—	36	2.3	36	V
VSEL, CNT High-level input voltage	V _{IH}	V _{INA} , V _{INB} = 2.3 to 36 V	1.6	—	—	1.6	—	V
VSEL, CNT Low-level input voltage	V _{IL}	V _{INA} , V _{INB} = 2.3 to 36 V	—	—	0.4	—	0.4	V
Over voltage lock out (OVLO) rising threshold	VOVL_RI	TCK321G	—	12.0	—	10.5	13.5	V
		TCK322G	—	15.0	—	13.4	16.6	
Over voltage lock out (OVLO) falling threshold	VOVL_FA	—	—	VOVL_RI - 0.5	—	—	—	V
Under voltage lock out (UVLO) rising threshold	VUVL_RI	—	—	2.9	—	2.3	3.5	V
Under voltage lock out (UVLO) falling threshold	VUVL_FA	—	—	VUVL_RI - 0.3	—	—	—	V
Quiescent current (Switch ON state)	I _{Q(ON)}	Q1 or Q2 = ON mode, I _{OUT} = 0 mA, V _{IN} = 5.0 V	—	140	—	—	200	μA
Quiescent current (Switch OFF state)	I _{Q(OFF)}	CNT and VSEL: Low, V _{INA} or V _{INB} = 5.0 V, V _{OUT} = 0 V	—	60	—	—	90	μA
Switch OFF state current	I _{OFF}	CNT and VSEL: Low, V _{IN} = Open, V _{OUT} = 5 V	—	0.1	—	—	1	μA
Reverse blocking current	I _{RB}	CNT and VSEL: Low, V _{IN} = 0 V, V _{OUT} = 5.0 V	—	0.1	—	—	10	μA
On resistance	R _{ON}	I _{OUT} = -1.0 A, V _{IN} = 4.5 V	—	98	—	—	170	mΩ
FLAG Leak current	I _{LEAK}	V _{IO} = 5.0 V	—	—	2	—	2	μA
FLAG Output low voltage	V _{OL}	I _{SINK} = 1 mA, V _{IO} = 5.0 V	—	—	0.4	—	0.4	V
VSEL, CNT Pull up resistance	R _{VC}	—	—	500	—	—	—	kΩ

AC Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition (Figure 1, 2, 3, 4)	Min	Typ.	Max	Unit
Hold time	t _{HD}	V _{UVL} < V _{IN} (5 V) < V _{OVL} , R _L = 50 Ω Initial start up V _{OUT} off state to charge-pump on state	—	15	—	ms
V _{OUT} OVP off time	t _{OVP}	V _{IN} > V _{OVLO_RI} , V _{IN} rising = 2 V/μs, R _L = 50 Ω, V _{OUT} to 80% of V _{OVLO_RI}	—	3	—	μs
V _{OUT} off time	t _{OFF}	V _{UVL} < V _{IN} (5 V) < V _{OVL} , R _L = 50 Ω, CNT low to high to V _{OUT} to 80% of V _{IN}	—	0.5	—	μs
V _{OUT} rise time	t _r	V _{IN} = 5.0 V, R _L = 50 Ω, C _L = 1.0 μF	—	2	—	ms
V _{OUT} fall time	t _f	V _{IN} = 5.0 V, R _L = 50 Ω, C _L = 1.0 μF	—	0.12	—	ms
V _{IN} selection delay time	t _{SEL}	V _{IN} = 5.0 V, R _L = 50 Ω,	—	0.5	—	μs
Break Before Make time	t _{BBM}	V _{IN} = 5.0 V, R _L = 50 Ω,	—	15	—	ms

Timing chart Manual selection mode

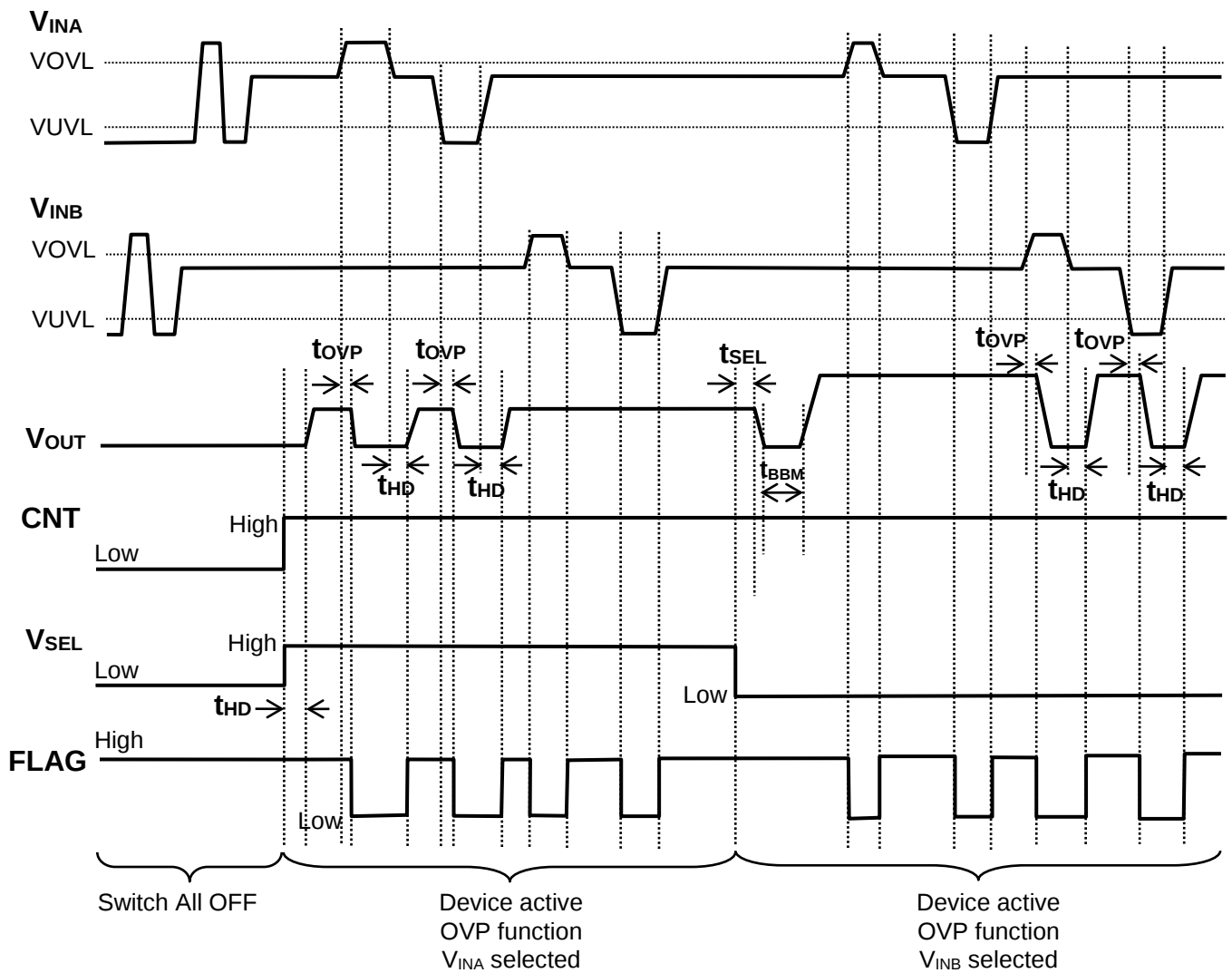


Fig.1 t_{HD} , t_{OVP} , t_{SEL}

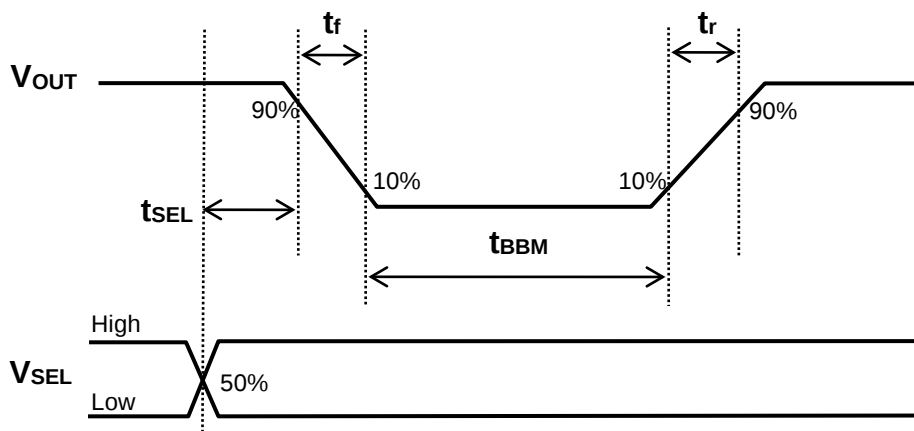


Fig.2 t_r , t_f , t_{BBM}

Timing chart
Auto selection mode

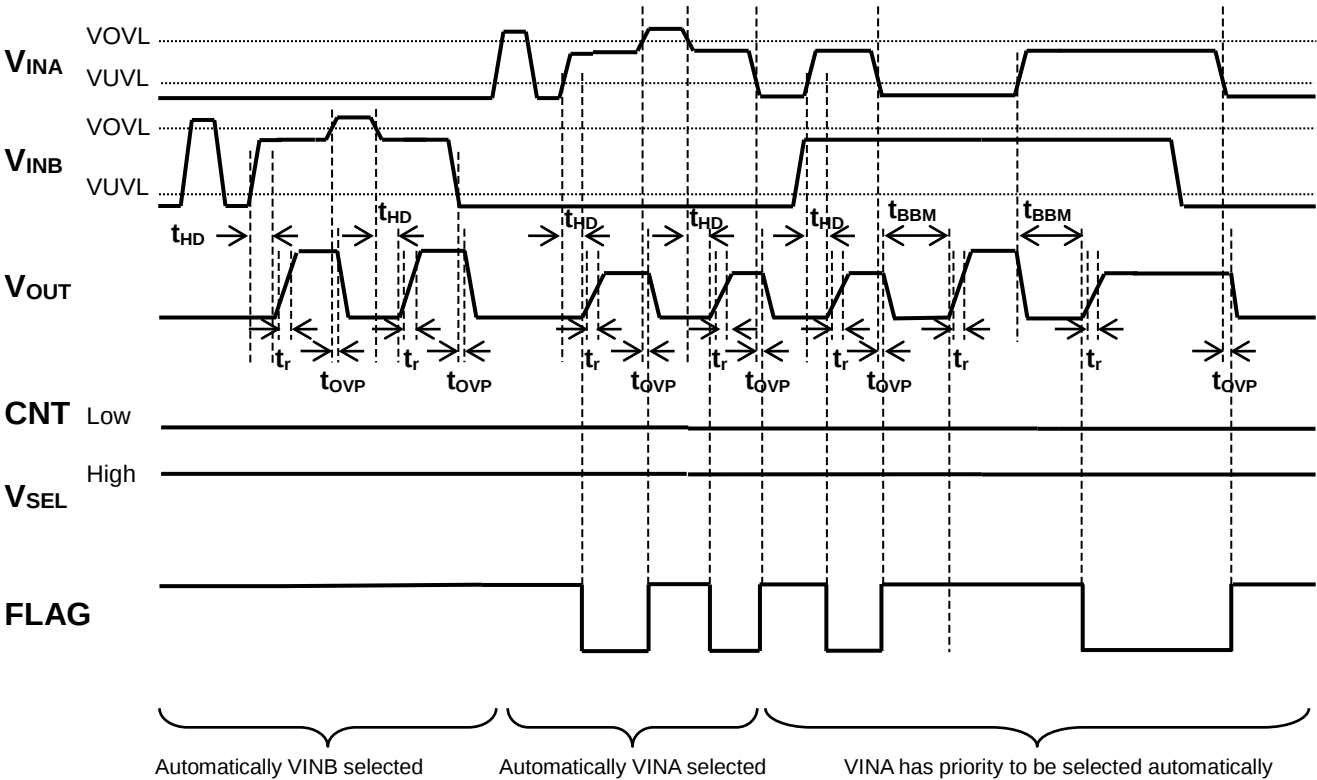


Fig.3 t_{HD} , t_{OVP}

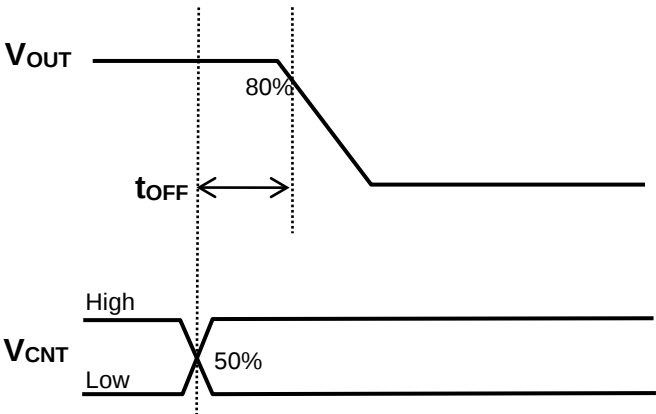
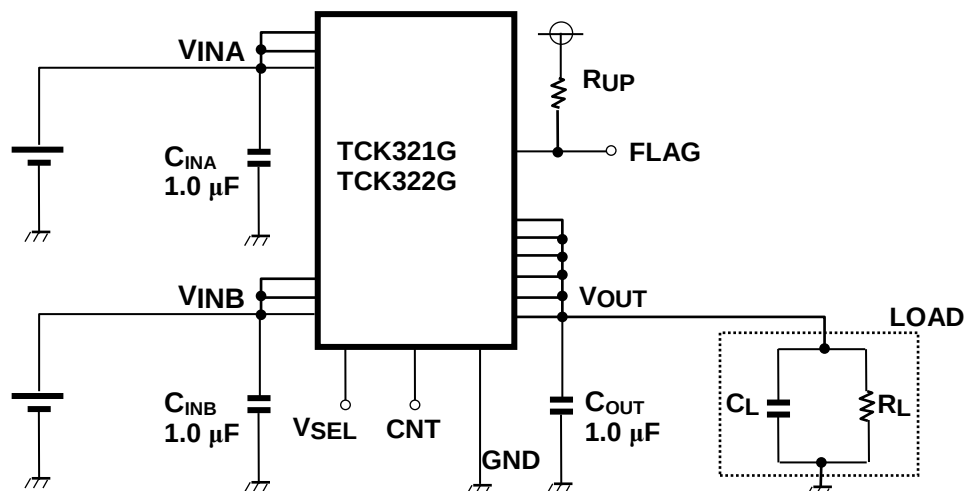


Fig.4 t_{OFF}

Application Note

1. Application circuit example (top view)



1) Input and Output capacitor

An input capacitor (C_{IN}) and an output capacitor (C_{OUT}) are necessary for the stable operation of TCK321G and TCK322G. And it is effective to reduce voltage overshoot or undershoot due to sharp changes in output current and also for improved stability of the power supply. When used, place C_{IN} and C_{OUT} more than 1.0 μF as close to V_{IN} pin to improve stability of the power supply.

2) Control pin

Control pins for TCK321G and TCK322G are operated by the control voltage and Schmitt trigger. V_{SEL} pin has a tolerant function such that it can be used even if the control voltage is higher than the input voltage.

2. Reverse current blocking

Reverse current blocking(SW OFF state) function is designed in these products. This function is active at output n-ch MOSEFT turned off.

However these does not assure for the suppression of uprising device operation. In use of these products, please read through and understand dissipation idea for absolute maximum ratings from the above mention or our 'Semiconductor Reliability Handbook'. Then use these products under absolute maximum ratings in any condition. Furthermore, Toshiba recommend inserting failsafe system into the design.

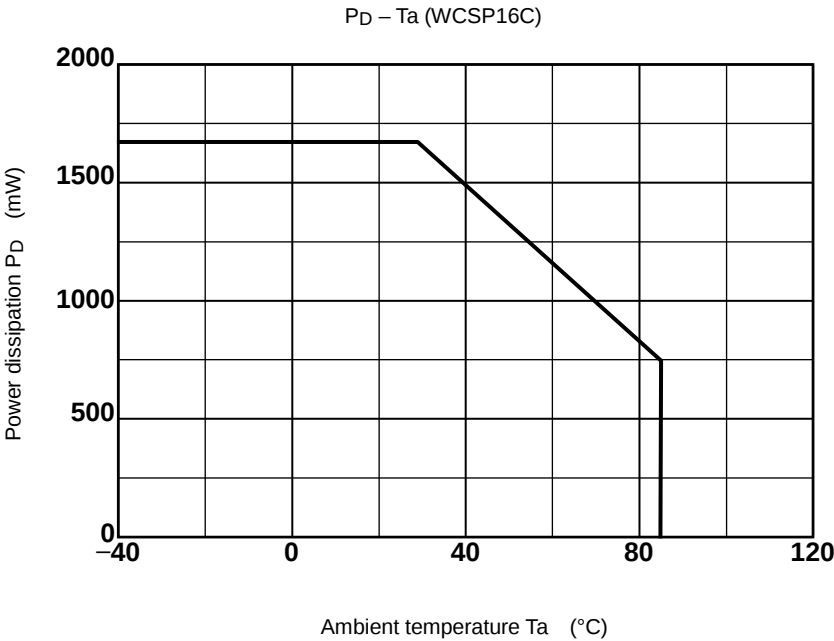
3. Thermal shut down function

Thermal shutdown function is designed in these products, but these does not assure for the suppression of uprising device operation. In use of these products, please read through and understand dissipation idea for absolute maximum ratings from the above mention or our 'Semiconductor Reliability Handbook'. Then use these products under absolute maximum ratings in any condition. Furthermore, Toshiba recommend inserting failsafe system into the design.

4. Power Dissipation

Board-mounted power dissipation ratings for TCK321G and TCK322G are available in the Absolute Maximum Ratings table.
Power dissipation is measured on the board condition shown below.

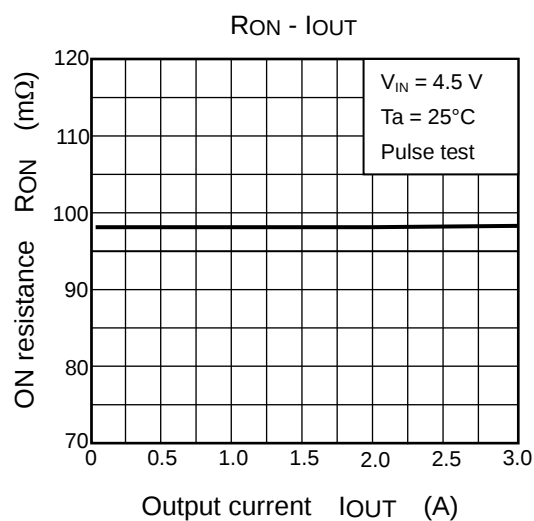
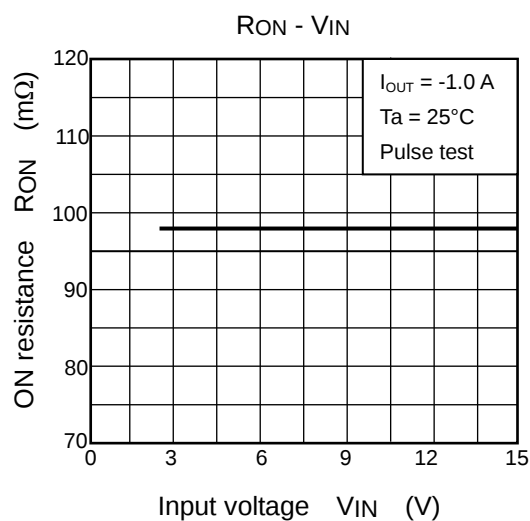
[The Board Condition]
Board material: Glass epoxy (FR4)
Board dimension: 40 mm x 40 mm (Cu 4 layer)



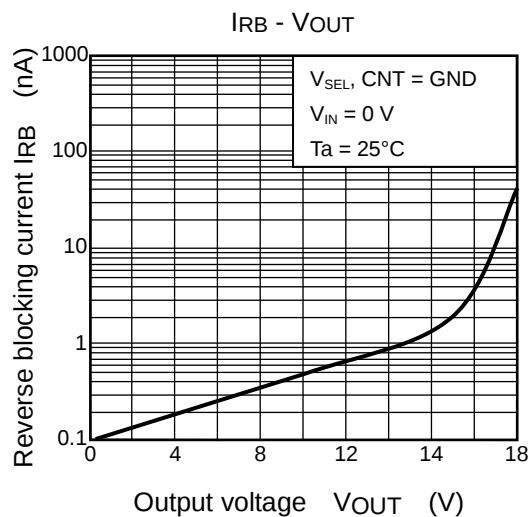
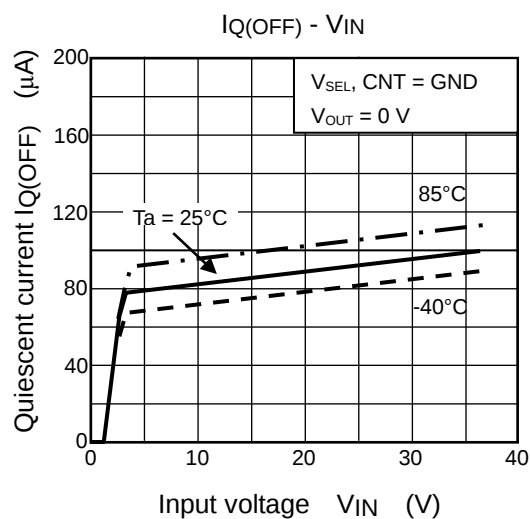
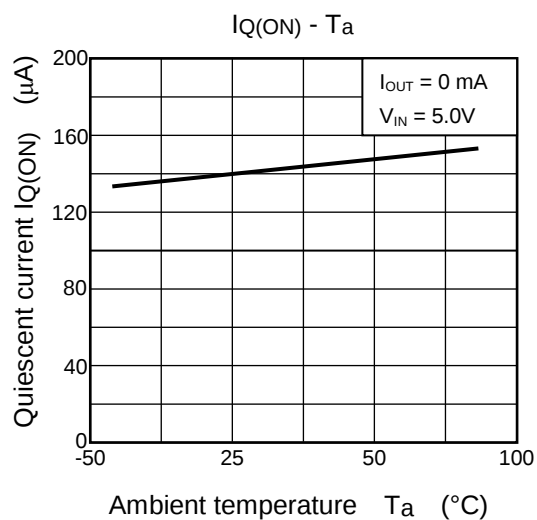
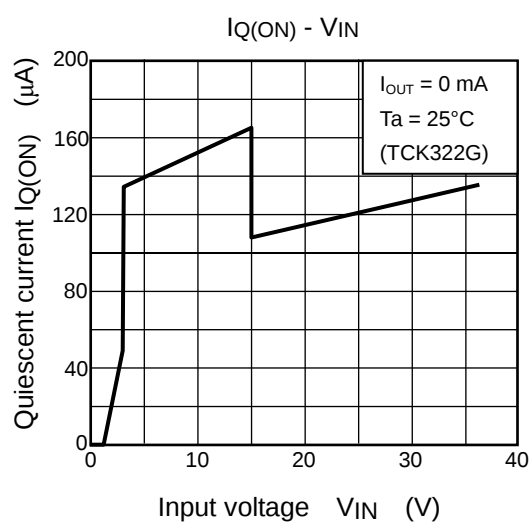
Please allow sufficient margin when designing a board pattern to fit the expected power dissipation. Also take into consideration the ambient temperature, input voltage, output current etc and applying the appropriate derating for allowable power dissipation during operation.

Representative Typical Characteristics

1) ON resistance



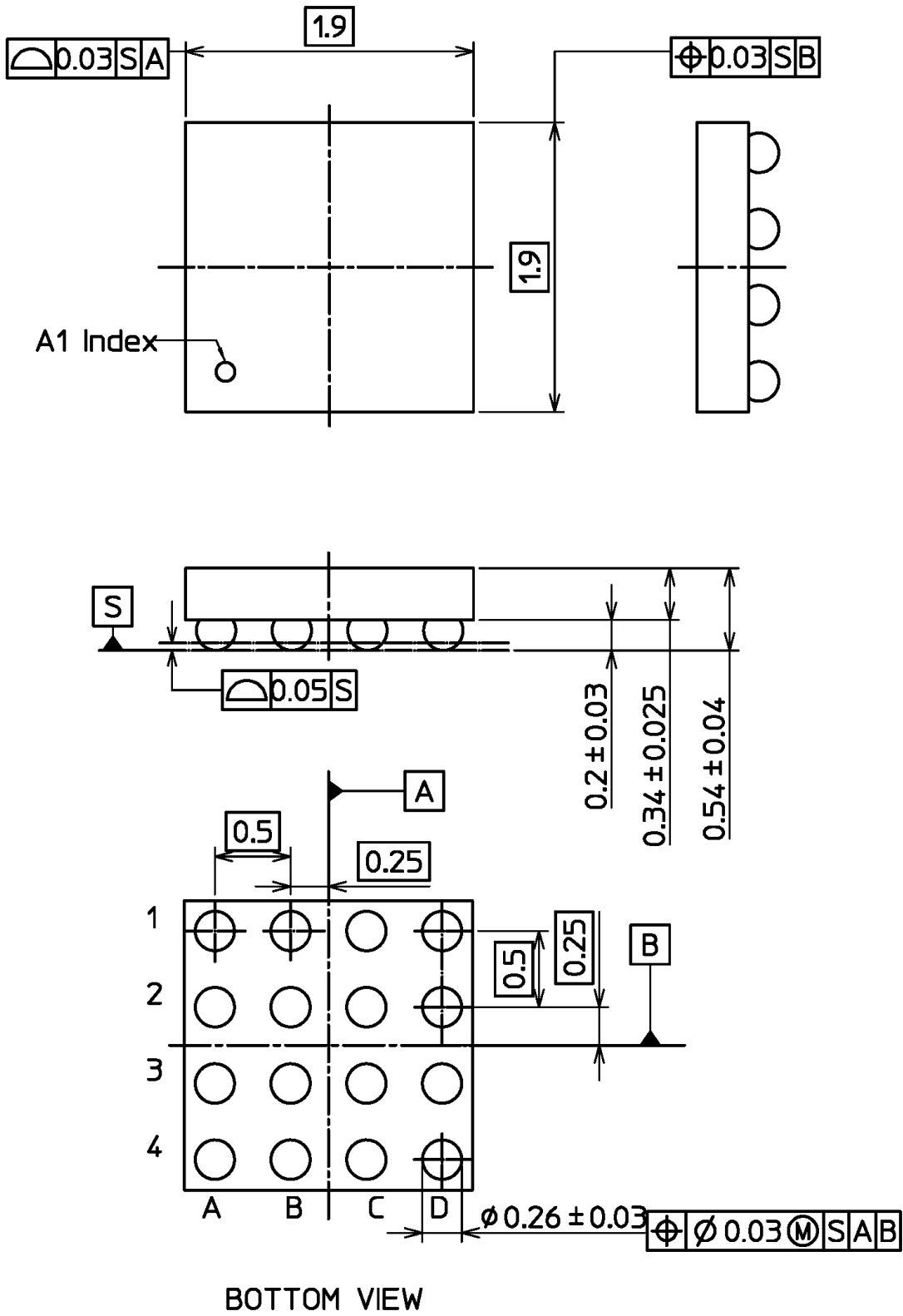
2) Quiescent current



Package Dimensions

WCSP16C

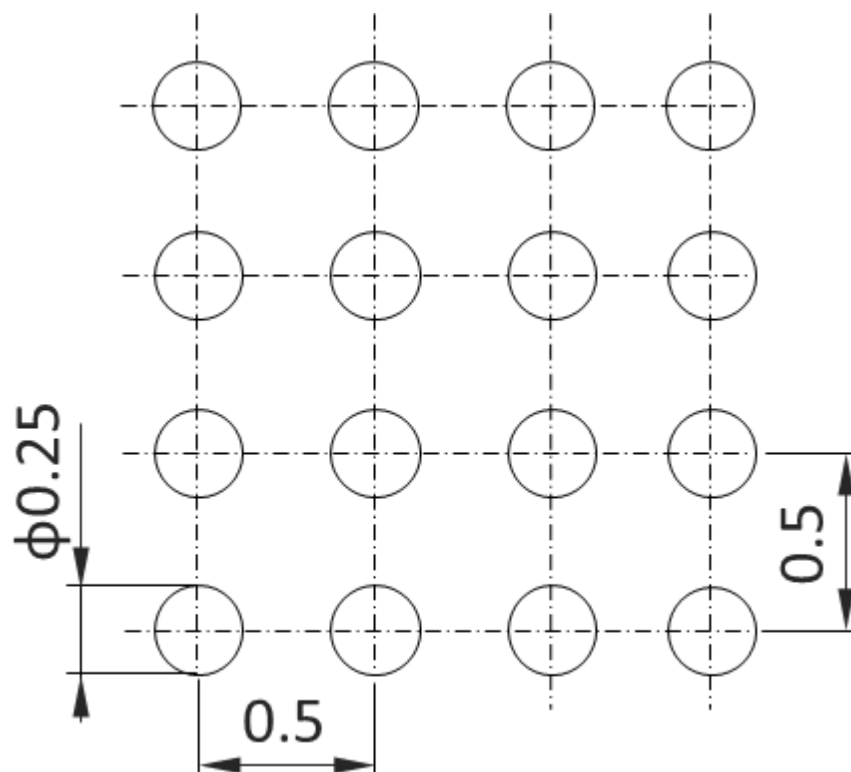
Unit: mm



Weight : 3.9 mg (typ.)

Land pattern dimensions (for reference only)

Unit: mm



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