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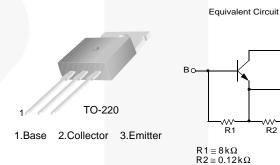
November 2014



# TIP142T NPN Epitaxial Silicon Darlington Transistor

## Features

- Monolithic Construction with Built-in Base-Emitter Shunt Resistors
- High DC Current Gain:  $h_{FE}$  = 1000 at  $V_{CE}$  = 4 V,  $I_C$  = 5 A (Minimum)
- Industrial Use
- Complement to TIP147T



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# **Ordering Information**

Part Number	Top Mark	Package	Packing Method
TIP142T	TIP142T	TO-220 3L (Single Gauge)	Bulk
TIP142TTU	TIP142T	TO-220 3L (Single Gauge)	Rail

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_c = 25^{\circ}C$  unless otherwise noted.

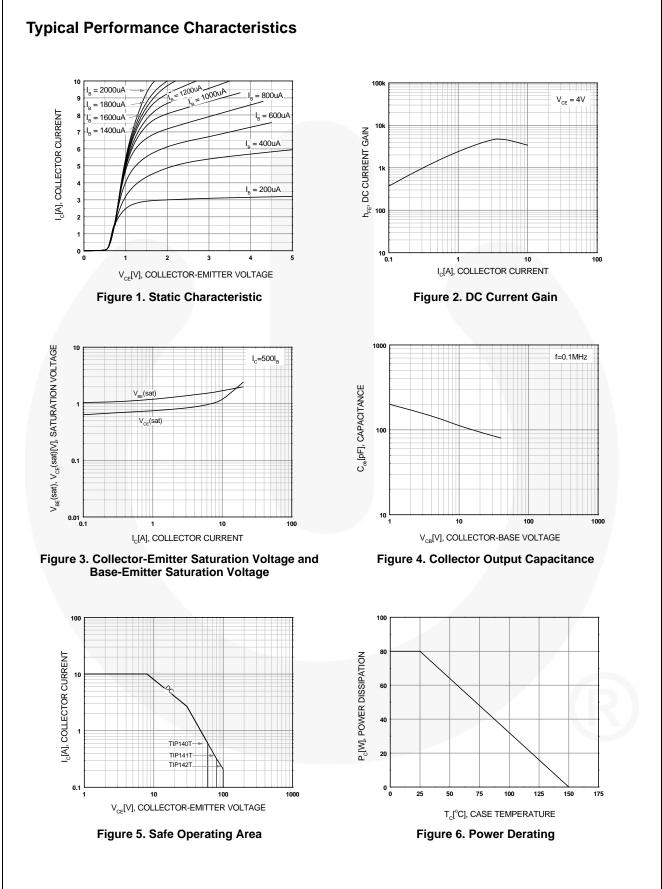
Symbol	Parameter	Value	Unit	
V <sub>CBO</sub>	Collector-Base Voltage	100	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	100	V	
V <sub>EBO</sub> Emitter-Base Voltage		5	V	
Ι <sub>C</sub>	Collector Current (DC)	10	А	
I <sub>CP</sub>	Collector Current (Pulse)	15	А	
Ι <sub>Β</sub>	Base Current (DC)	0.5	А	
P <sub>C</sub>	Collector Dissipation ( $T_C = 25^{\circ}C$ )	80	W	
TJ	Junction Temperature	150	°C	
T <sub>STG</sub>	Storage Temperature Range	-65 to 150	°C	

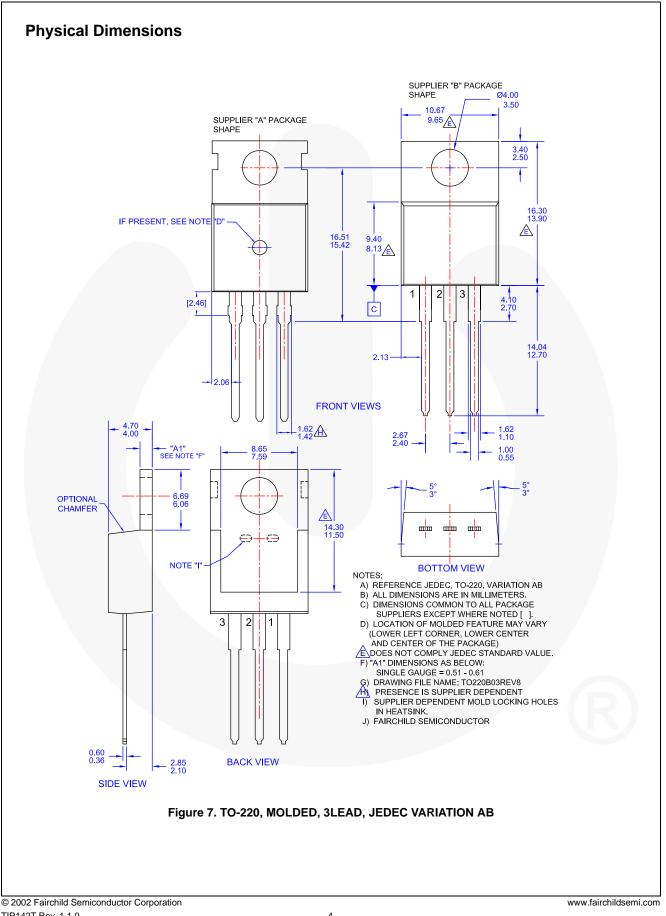
# **TIP142T — NPN Epitaxial Silicon Darlington Transistor**

# **Electrical Characteristics**

Values are at  $T_C = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>CEO</sub> (sus)	Collector-Emitter Sustaining Voltage	I <sub>C</sub> = 30 mA, I <sub>B</sub> = 0	100			V
I <sub>CEO</sub>	Collector Cut-Off Current	$V_{CE} = 50 \text{ V}, I_{B} = 0$			2	mA
I <sub>CBO</sub>	Collector Cut-Off Current	$V_{CB} = 100 \text{ V}, I_E = 0$			1	mA
I <sub>EBO</sub>	Emitter Cut-Off Current	$V_{EB} = 5 V, I_{C} = 0$			2	mA
h <sub>FE</sub>	DC Current Gain	$V_{CE} = 4 V, I_{C} = 5 A$	1000			
		$V_{CE} = 4 \text{ V}, \text{ I}_{C} = 10 \text{ A}$	500			
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 5 A, I <sub>B</sub> = 10 mA			2	V
	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 10 A, I <sub>B</sub> = 40 mA			3	v
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = 10 A, I <sub>B</sub> = 40 mA			3.5	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage	$V_{CE} = 4 \text{ V}, \text{ I}_{C} = 10 \text{ A}$			3	V
t <sub>D</sub>	Delay Time $V_{CC} = 30 \text{ V}, I_C = 5 \text{ A},$			0.15		μs
t <sub>R</sub>	Rise Time	$I_{B1} = 20 \text{ mA},$		0.55		μs
t <sub>STG</sub>	Storage Time	$I_{B2} = -20 \text{ mA},$		2.50		μs
t <sub>F</sub>	Fall Time	$R_L = 6 \Omega$		2.50		μs





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