

# **1 Channel High Side Switch ICs** 2.0A Current Limit High Side Switch ICs

# **BD82033FVJ**

# Description

BD82033FVJ is a Single Channel High Side Switch IC employing N-channel power MOSFET with low on resistance and low supply current for the power supply line of universal serial bus (USB).

This IC has a built-in over current detection circuit, thermal shutdown circuit, under voltage lockout and soft start circuits.

#### Features

- Over-Current Protection : 2.0A
- Control Input Logic : Active-Low
- **Output Discharge Function**
- Reverse Current Protection when Power Switch Off
- Thermal Shutdown
- **Open-Drain Fault Flag Output**
- Under-Voltage Lockout
- OCP Fast Response
- Soft-Start Circuit
- ESD Protection
- UL : File No.E243261
- IEC 60950-1 CB\_scheme: File No.US-20060-UL

#### Applications

USB hub in consumer appliances, PC, PC peripheral equipment, and so forth

# **Typical Application Circuit**

#### **Key Specifications**

Input Voltage Range:	4.5V to 5.5V
<ul> <li>ON Resistance: (VIN=5V)</li> </ul>	72mΩ(Typ)
Over Current Threshold:	2.0A
Standby Current:	0.01uA (Tvp)

tandby Current:

1μΑ (

-40°C to +85°C Operating Temperature Range:

Package W(Typ) D(Typ) H(Max) TSSOP-B8J 3.00mm x 4.90mm x 1.10mm



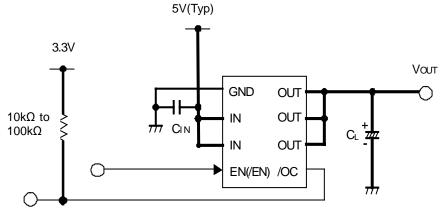


Figure 1. Typical Application Circuit

# **Block Diagram**

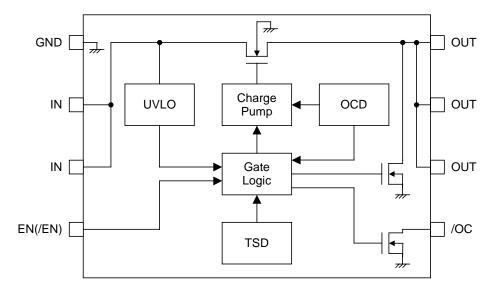


Figure 2. Block Diagram

# **Pin Configuration**

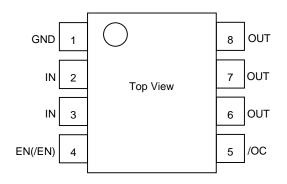


Figure 3. Pin Configuration (TOP VIEW)

# **Pin Descriptions**

Pin No.	Symbol	I/O	Function
1	GND	-	Ground
2, 3	IN	I	Power supply input. Input terminal to the power switch and power supply input terminal of the internal circuit Short these pins externally
4	EN, /EN	I	Enable input Active low power on switch High level input > 2.0V, Low level input < 0.8V
5	/OC	0	Error flag output Low when over-current or thermal shutdown is activated Open drain output
6, 7, 8	OUT	Ο	Power switch output Short these pins externally

# Absolute Maximum Ratings(Ta=25°C)

Parameter	Symbol	Rating	Unit
IN Supply Voltage	Vin	-0.3 to +6.0	V
/EN Input Voltage	V/EN	-0.3 to +6.0	V
/OC Voltage	V/oc	-0.3 to +6.0	V
/OC Sink Current	l/oc	5	mA
OUT Voltage	Vout	-0.3 to +6.0	V
Storage Temperature	Tstg	-55 to +150	°C
Power Dissipation	Pd	0.58 (1)	W

(1) Mounted on 70mm x 70mm x 1.6mm glass epoxy board. Reduce 4.7mW per 1°C above 25°C Caution: Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

# **Recommended Operating Ratings**

Parameter	Symbol		Unit		
Falameter	Symbol	Min	Тур	Max	Onit
IN Operating Voltage	Vin	4.5	5.0	5.5	V
Operating Temperature	Topr	-40	-	+85	°C

# Electrical Characteristics (V<sub>IN</sub>= 5V, Ta= 25°C, unless otherwise specified.)

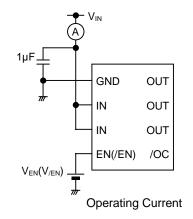
**DC Characteristics** 

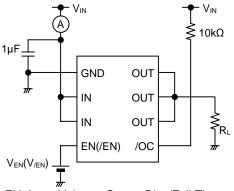
Deremeter	Parameter Symbol Limit		Unit	Conditions			
Parameter	Symbol	Min	Тур	Max	Unit	Conditions	
Operating Current	Idd	-	85	120	μA	V/EN = 0V, VOUT = open	
Standby Current	ISTB	-	0.01	5	μA	V/EN = 5V, VOUT = open	
	V/ENH	2.0	-	-	V	High input	
/EN Input Voltage	V/ENL	-	-	0.8	V	Low input	
/EN Input Leakage	l/en	-1	0.01	+1	μA	V/EN = 0V or 5V	
		-	72	90		IOUT = 0.5A	
On Resistance	Ron	-	74	93	mΩ	IOUT = 1.0A	
		-	78	98		IOUT = 1.5A	
Reverse Leak Current	IREV	-	-	1	μA	VOUT = 5.5V, VIN = 0V	
Over-Current Threshold	Ітн	1.55	2.00	2.30	А	Current Load Slew rate	
						100A/s	
						Vout=0V	
Short Circuit Output Current	Isc	0.85	1.25	1.65	A	C∟=100µF	
						RMS	
Output Discharge Resistance	RDISC	-	55	100	Ω	IOUT = 1mA, V/EN = 5V	
/OC Output Low Voltage	V/oc	-	-	0.4	V	I/OC = 1mA	
/OC Output Leak Current	IL/oc	-	0.01	1	μA	V/OC = 5V	
UVLO Threshold	ντυνη	3.4	3.7	4.0	V	VIN increasing	
	Vtuvl	3.3	3.6	3.9	V	VIN decreasing	

#### AC Characteristics

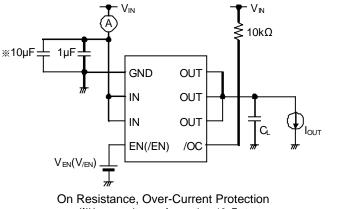
Parameter	Sumbol		Limit		Unit	Conditions
Falameter	Symbol	Min	Тур	Max	Unit	Conditions
Output Rise Time	tON1	-	0.3	10	ms	
Output Turn-on Time	ton2	-	0.5	20	ms	RI 100
Output Fall Time	tOFF1	-	2	10	μs	RL=10Ω
Output Turn-off Time	tOFF2	-	4	20	μs	
/OC Delay Time	t/oc	5	13	20	ms	

# **Measurement Circuit**





EN, Input Voltage, Output Rise/Fall Time



Wuse capacitance of more than 10µF at output short test by using external supply.

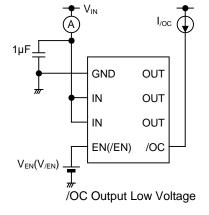


Figure 4. Measurement Circuit

# **Timing Diagram**

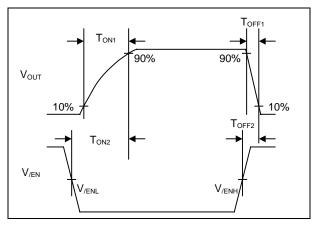
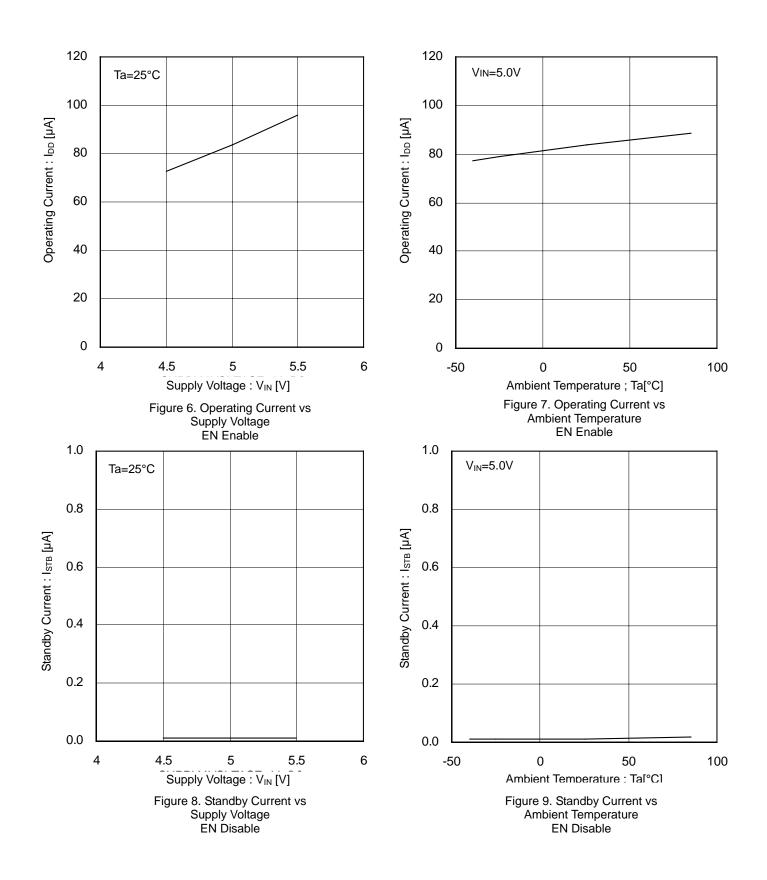
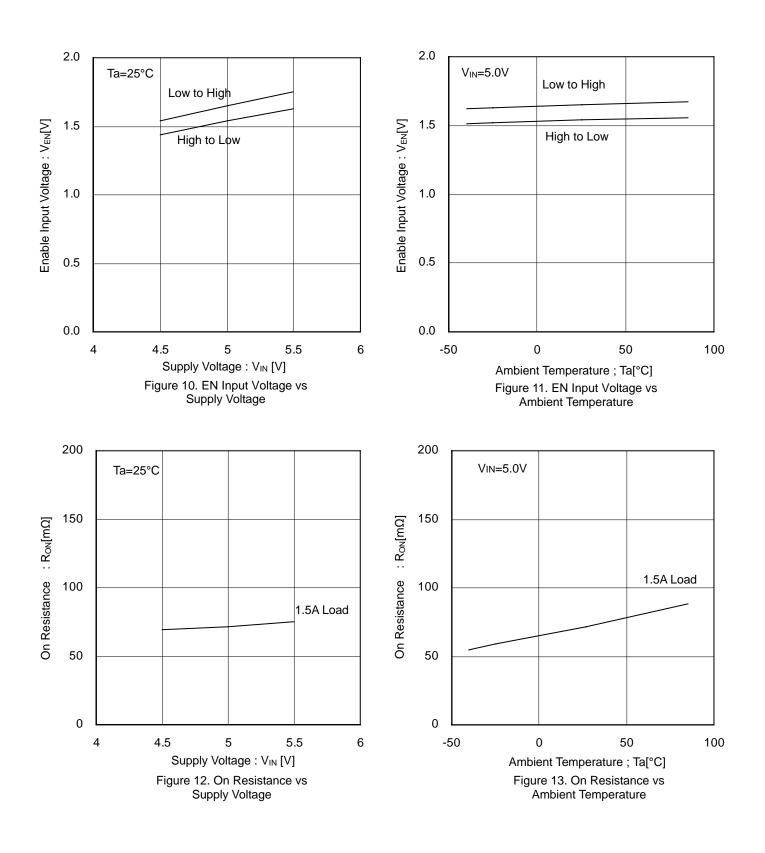


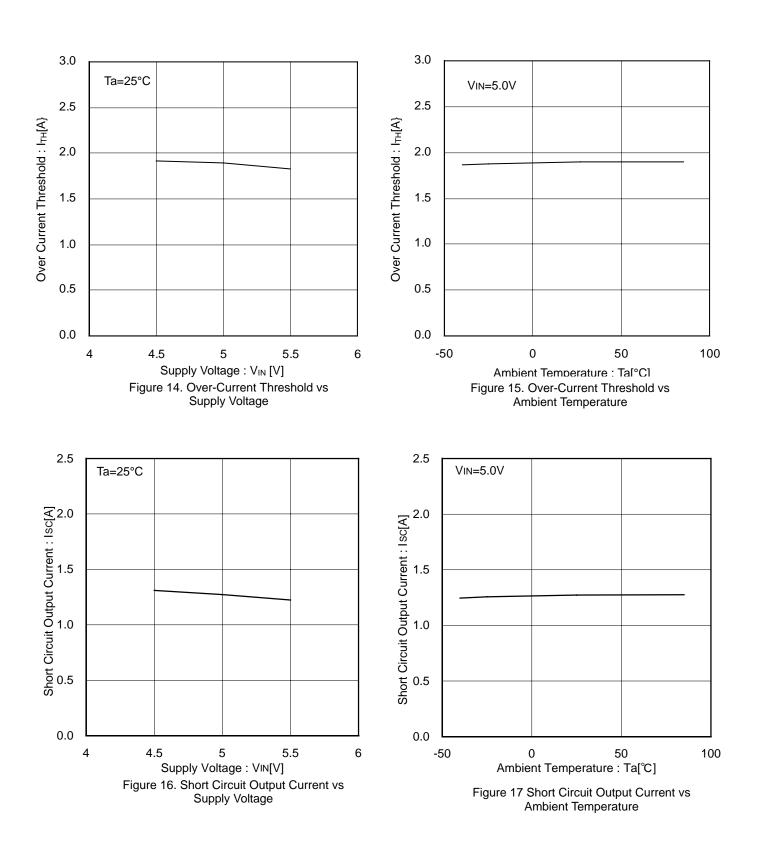
Figure 5. Output Rise/Fall Time

# **Typical Performance Curves**

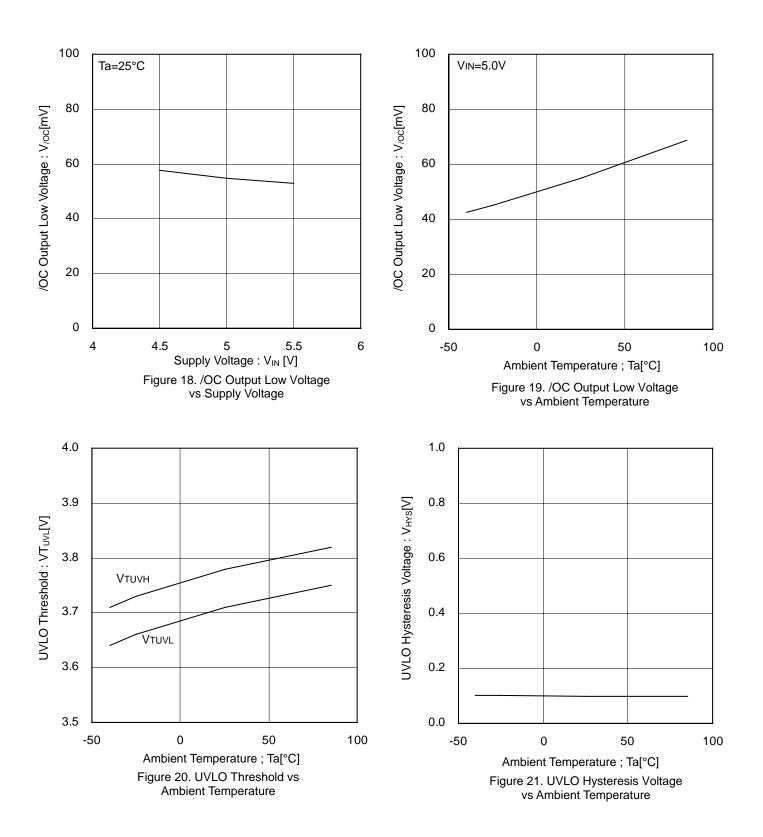




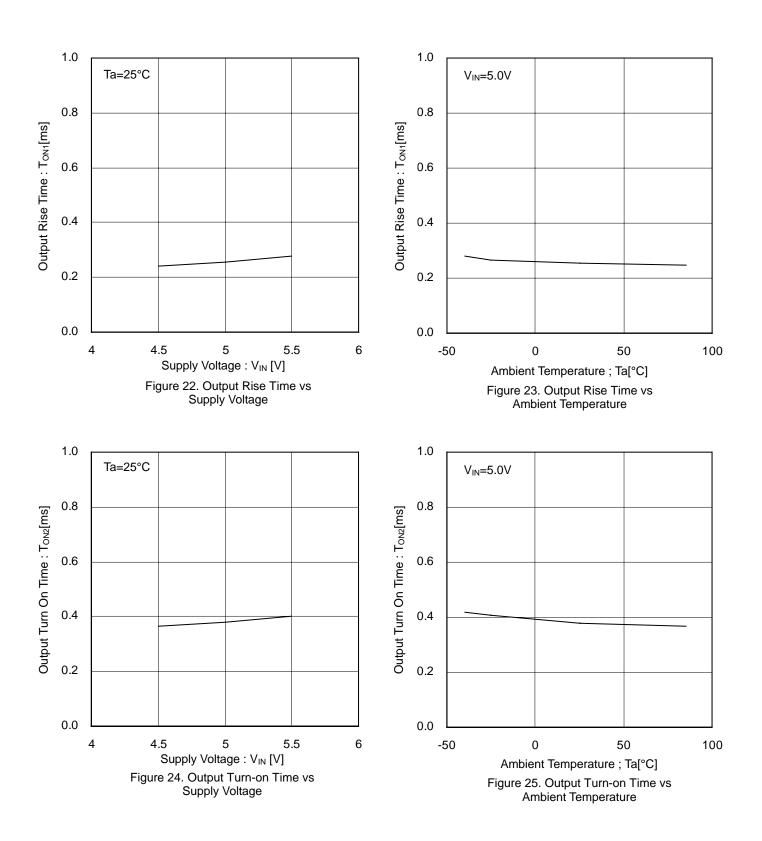
# **Typical Performance Curves - continued**

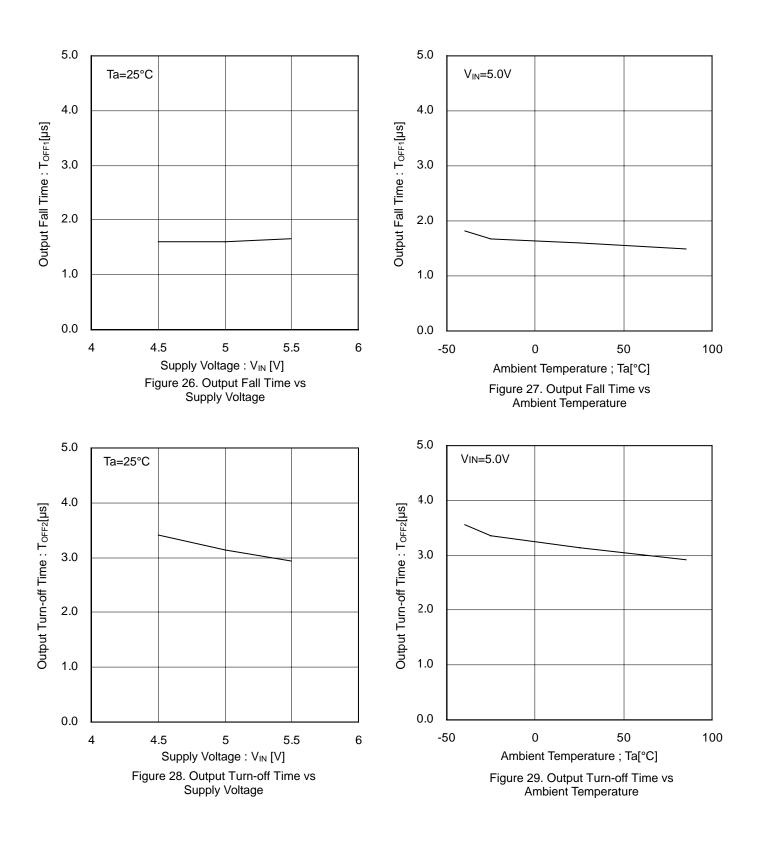


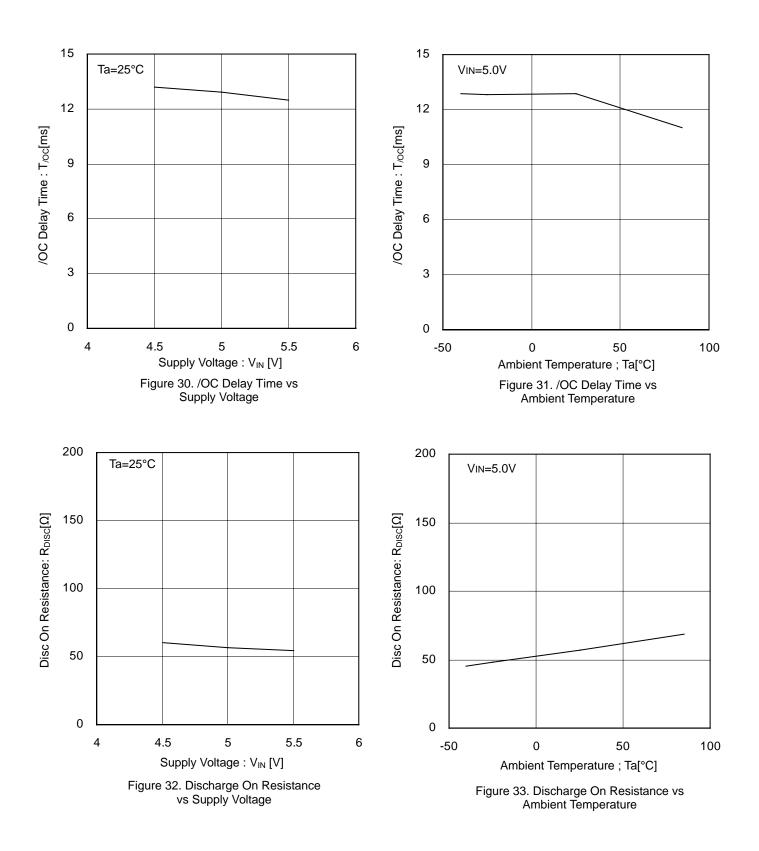
# **Typical Performance Curves - continued**



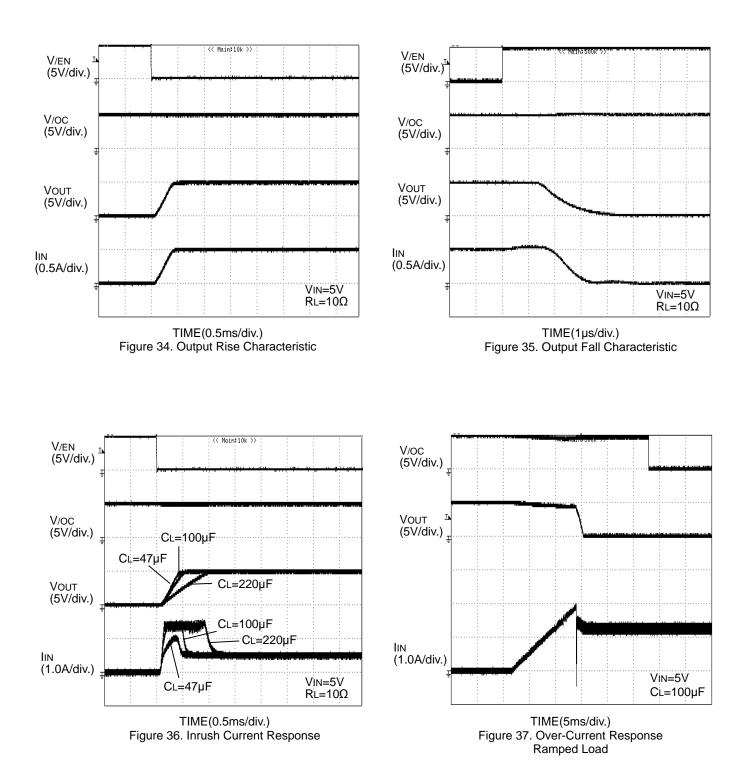
# **Typical Performance Curves - continued**



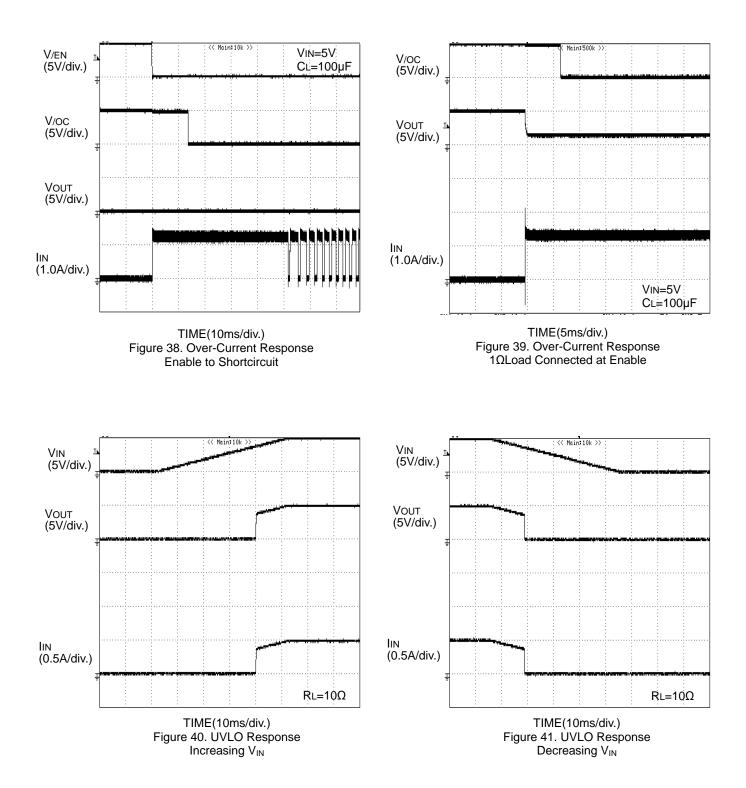




# Typical Wave Forms (BD82033FVJ)



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# **Typical Application Circuit**

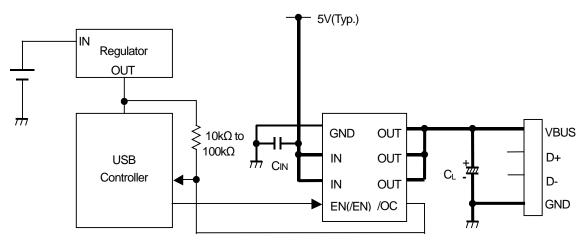


Figure 42. Typical Application Circuit

# **Application Information**

When excessive current flows due to output short-circuit or overload ringing occurs because of inductance between power source line and IC. This may cause bad effects on IC operations. In order to avoid this case, connect a bypass capacitor CIN across IN terminal and GND terminal of IC. 1 $\mu$ F or higher is recommended. In order to decrease voltage fluctuations of power source line to IC, connect a low ESR capacitor in parallel with CIN. 10 $\mu$ F to 100 $\mu$ F or higher is recommended.

Pull up /OC output via resistance value of  $10k\Omega$  to  $100k\Omega$ .

Set up a value for CL which satisfies the application.

This system connection diagram does not guarantee operation as the intended application.

When using the circuit with changes to the external circuit values, make sure to leave an adequate margin for external components including static and transitional characteristics as well as the design tolerances of the IC.

#### **Functional Description**

1. Switch Operation

IN terminal and OUT terminal are connected to the drain and the source of switch MOSFET respectively. The IN terminal is also used as power source input to internal control circuit.

When the switch is turned on from /EN control input, the IN terminal and OUT terminal are connected by a  $72m\Omega(Typ)$  switch. In ON status, the switch is bidirectional. Therefore, when the potential of OUT terminal is higher than that of the IN terminal, current flows from OUT terminal to IN terminal.

Since the parasitic diode between the drain and the source of switch MOSFET is canceled current flow from OUT to IN is prevented during off state.

2. Thermal Shutdown Circuit (TSD)

If over current would continue, the temperature of the IC would increase drastically. If the junction temperature reaches beyond  $135^{\circ}C(Typ)$  during the condition of over current detection, thermal shutdown circuit operates and turns power switch off and outputs an error flag (/OC). Then, when the junction temperature decreases below  $115^{\circ}C(Typ)$ , power switch is turned on and error flag (/OC) is cancelled. Unless the cause of the increase of the chip's temperature is removed or the output of power switch is turned off, this operation repeats.

The thermal shutdown circuit operates when the switch is on (/EN signal is active).

3. Over Current Detection (OCD)

The over current detection circuit (OCD) limits current ( $I_{SC}$ ) and outputs error flag (/OC) when current flowing in each switch MOSFET exceeds a specified value. There are three cases when the OCD circuit is activated. The OCD operates when the switch is on (/EN signal is active).

- (1) When the switch is turned on while the output is in short-circuit status, the switch gets in current limit status immediately.
- (2) When the output short-circuits or when high current load is connected while the switch is on, very large current will flow until the over-current limit circuit reacts. When this happens, the over-current limit circuit is activated and the current limitation is carried out.
- (3) When the output current increases gradually, current limitation does not work until the output current exceeds the over-current detection value. When it exceeds the detection value, current limitation is carried out.
- 4. Under-Voltage Lockout (UVLO)

UVLO circuit prevents the switch from turning on until  $V_{IN}$  exceeds 3.7V(Typ). If  $V_{IN}$  drops below 3.6V(Typ) while the switch is still on, then the UVLO will shut off the power switch. UVLO has a hysteresis of 100mV(Typ). Under-voltage lockout circuit works when the switch is on (/EN signal is active).

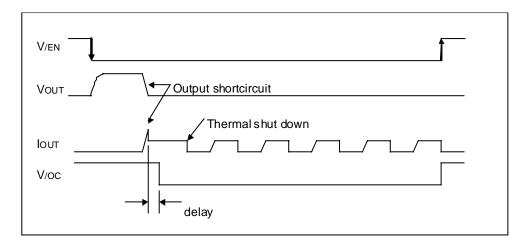
5. Error Flag (/OC) Output

Error flag output is an N-MOS open drain output. Upon detection of over current or thermal shutdown, the output level becomes low.

Over-current detection has a delay filter. This delay filter prevents current detection flags from being sent during instantaneous events such as surge current due to switching or hot plug.

6. Output Discharge Function

When the switch is turned off from disable control input or UVLO function, the  $55\Omega(Typ.)$  discharge circuit between OUT and GND turns on. By turning on this switch, electric charge at capacitive load is discharged. But when the voltage of IN declines extremely, then the OUT pin becomes Hi-Z without UVLO function.



#### Figure 43. Over-Current Detection, Thermal Shutdown Timing

# **Power Dissipation**

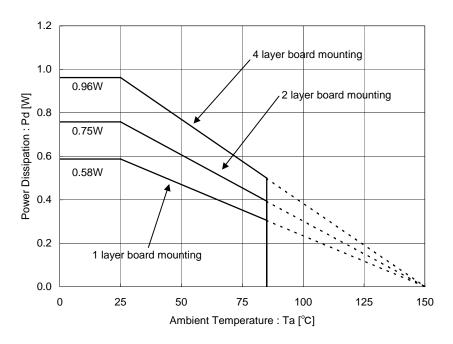
The power dissipation depends on output load, ambient temperature and PCB layout. The devices have current capacity of 1.5A respectively. Power dissipation can be calculated using the output current and the RoN of the power switch as below.

 $Pd = Ron x Iout^2$ 

The derating curve is shown below

# TSSOP-B8J

(MSOP-8 JEDEC standard)



Note: IC is Mounted on 70mmx70mmx1.6mm glass-epoxy PCB. Derating is 4.7mW/°C above Ta=25°C. Figure 44. Power Dissipation Curve (Pd-Ta Curve)

# Symbol Pin No. Equivalent Circuit EN(/EN) 4 end prime <math>(Pin) finite <math>finite <math>(Pin) finite<</td> <math>finite <math>(Pin) finite <math>finite<</td> <math>finite

# I/O Equivalent Circuit

# **Operational Notes**

## 1. Reverse Connection of Power Supply

Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply pins.

# 2. Power Supply Lines

Design the PCB layout pattern to provide low impedance supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

# 3. Ground Voltage

Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

# 4. Ground Wiring Pattern

When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

# 5. Thermal Consideration

Should by any chance the power dissipation rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. The absolute maximum rating of the Pd stated in this specification is when the IC is mounted on a 70mm x 70mm x 1.6mm glass epoxy board. In case of exceeding this absolute maximum rating, increase the board size and copper area to prevent exceeding the Pd rating.

# 6. Recommended Operating Conditions

These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

# 7. Inrush Current

When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

# 8. Operation Under Strong Electromagnetic Field

Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

# 9. Testing on Application Boards

When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

#### 10. Inter-pin Short and Mounting Errors

Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

# **Operational Notes – continued**

#### 11. Unused Input Pins

Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

#### 12. Regarding the Input Pin of the IC

This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When GND > Pin A and GND > Pin B, the P-N junction operates as a parasitic diode. When GND > Pin B, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

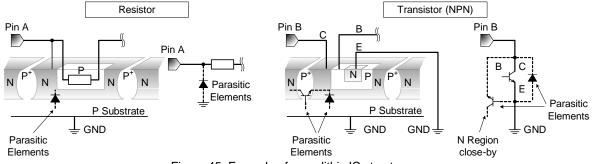


Figure 45. Example of monolithic IC structure

# 13. Ceramic Capacitor

When using a ceramic capacitor, determine the dielectric constant considering the change of capacitance with temperature and the decrease in nominal capacitance due to DC bias and others.

#### 14. Thermal Shutdown Circuit(TSD)

This IC has a built-in thermal shutdown circuit that prevents heat damage to the IC. Normal operation should always be within the IC's power dissipation rating. If however the rating is exceeded for a continued period, the junction temperature (Tj) will rise which will activate the TSD circuit that will turn OFF all output pins. When the Tj falls below the TSD threshold, the circuits are automatically restored to normal operation.

Note that the TSD circuit operates in a situation that exceeds the absolute maximum ratings and therefore, under no circumstances, should the TSD circuit be used in a set design or for any purpose other than protecting the IC from heat damage.

#### 15. Thermal design

Perform thermal design in which there are adequate margins by taking into account the power dissipation (Pd) in actual states of use.

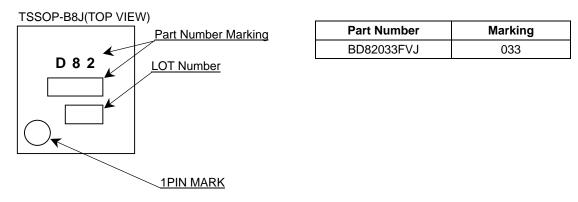
# **Ordering Information**

В	D	8	2	0	3	3	F	V	J	-	G	E2	
Part Numb	ber				Thres and	Currer hold ol Logio			age FSSOP P-8 Je		G: Halogen free package	Packaging an specification E2: Embosse	d forming d tape and reel

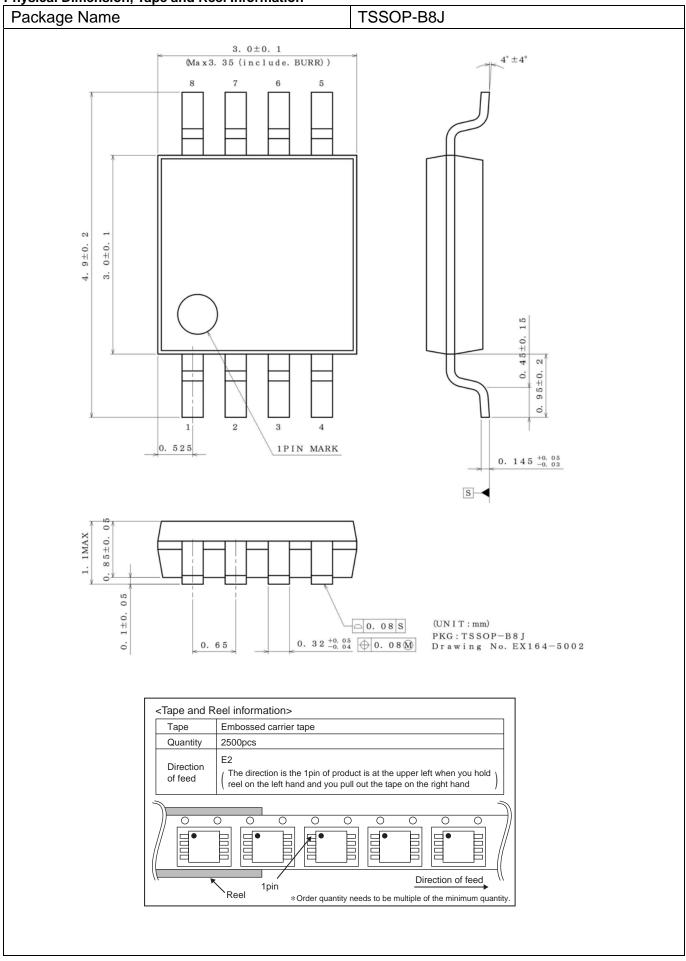
Lineup

Over-Current Threshold	Control Logic	Part Number
2.0A	Active- Low	BD82033FVJ

# Marking Diagram



# Physical Dimension, Tape and Reel Information



# **Revision History**

Date	Revision	Changes
5.SEP.2012	001	New Release
11.DEC.2012	002	UL • CB recognized. Improved grammar and presentation
18.SEP.2013	003	Revised derating of Power Dissipation
29.JAN.2014	004	Delete Marking Information Add Caution of page3 and Discharge function in Functional Description and Figure 16, 17. Revised Power Dissipation decimal and Operational Notes

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CLASSⅣ	CLASSIII	CLASSⅢ	CLASSII

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  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

## **Precautions Regarding Application Examples and External Circuits**

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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Компания «Океан Электроники» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Поставка оригинальных импортных электронных компонентов напрямую с производств Америки, Европы и Азии, а так же с крупнейших складов мира;

- Широкая линейка поставок активных и пассивных импортных электронных компонентов (более 30 млн. наименований);

- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Помощь Конструкторского Отдела и консультации квалифицированных инженеров;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Поставка электронных компонентов под контролем ВП;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;

- При необходимости вся продукция военного и аэрокосмического назначения проходит испытания и сертификацию в лаборатории (по согласованию с заказчиком):

- Поставка специализированных компонентов военного и аэрокосмического уровня качества (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Actel, Aeroflex, Peregrine, VPT, Syfer, Eurofarad, Texas Instruments, MS Kennedy, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Компания «Океан Электроники» является официальным дистрибьютором и эксклюзивным представителем в России одного из крупнейших производителей разъемов военного и аэрокосмического назначения «JONHON», а так же официальным дистрибьютором и эксклюзивным представителем в России производителя высокотехнологичных и надежных решений для передачи СВЧ сигналов «FORSTAR».



«JONHON» (основан в 1970 г.)

Разъемы специального, военного и аэрокосмического назначения:

(Применяются в военной, авиационной, аэрокосмической, морской, железнодорожной, горно- и нефтедобывающей отраслях промышленности)

«FORSTAR» (основан в 1998 г.)

ВЧ соединители, коаксиальные кабели, кабельные сборки и микроволновые компоненты:

(Применяются в телекоммуникациях гражданского и специального назначения, в средствах связи, РЛС, а так же военной, авиационной и аэрокосмической отраслях промышленности).



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