



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
INPUT					
Reverse voltage			V _R	6	V
Forward current			I _F	60	mA
Surge current			I _{FSM}	2.5	A
Power dissipation			P _{diss}	100	mW
Derate from 25 °C				1.33	mW/°C
OUTPUT					
Peak off-state voltage		VO4154D/H/M	V _{DRM}	400	V
		VO4156D/H/M	V _{DRM}	600	V
RMS on-state current			I _{TM}	300	mA
Total power dissipation			P _{diss}	500	mW
Derate from 25 °C				6.6	mW/°C
COUPLER					
Isolation test voltage (between emitter and detector, climate per DIN 500414, part 2, Nov. 74)	t = 1 min		V _{ISO}	5300	V _{RMS}
Storage temperature range			T _{stg}	- 55 to + 150	°C
Ambient temperature range			T _{amb}	- 55 to + 100	°C
Soldering temperature	max. ≤ 10 s dip soldering ≥ 0.5 mm from case bottom		T _{sld}	260	°C

Note

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.



Fig. 1 - Recommended Operating Condition

THERMAL CHARACTERISTICS				
PARAMETER	SYMBOL	VALUE	UNIT	
LED power dissipation	P_{diss}	100	mW	
Output power dissipation	P_{diss}	500	mW	
Maximum LED junction temperature	$T_{jmax.}$	125	°C	
Maximum output die junction temperature	$T_{jmax.}$	125	°C	
Thermal resistance, junction emitter to board	θ_{JEB}	150	°C/W	
Thermal resistance, junction emitter to case	θ_{JEC}	139	°C/W	
Thermal resistance, junction detector to board	θ_{JDB}	78	°C/W	
Thermal resistance, junction detector to case	θ_{JDC}	103	°C/W	
Thermal resistance, junction emitter to junction detector	θ_{JED}	496	°C/W	
Thermal resistance, case to ambient	θ_{CA}	3563	°C/W	

Note

- The thermal characteristics table above were measured at 25 °C and the thermal model is represented in the thermal network below. Each resistance value given in this model can be used to calculate the temperatures at each node for a given operating condition. The thermal resistance from board to ambient will be dependent on the type of PCB, layout and thickness of copper traces. For a detailed explanation of the thermal model, please reference Vishay's Thermal Characteristics of Optocouplers application note.

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ °C}$, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	$I_F = 10\text{ mA}$		V_F		1.2	1.4	V
Reverse current	$V_R = 6\text{ V}$		I_R		0.1	10	μA
Input capacitance	$V_F = 0\text{ V}$, $f = 1\text{ MHz}$		C_I		25		pF
OUTPUT							
Repetitive peak off-state voltage	$I_{DRM} = 100\text{ }\mu\text{A}$	VO4154D/H/M	V_{DRM}	400			V
		VO4156D/H/M	V_{DRM}	600			V
Off-state current	$V_D = V_{DRM}$, $I_F = 0\text{ A}$		I_{DRM}			100	μA
On-state voltage	$I_T = 300\text{ mA}$		V_{TM}			3	V
On-state current	$PF = 1$, $V_{T(RMS)} = 1.7\text{ V}$		I_{TM}			300	mA
Off-state current in inhibit state	$I_F = 2\text{ mA}$, V_{DRM}		I_{DINH}			200	μA
Holding current			I_H			500	μA
Zero cross inhibit voltage	$I_F = \text{rated } I_{FT}$		V_{IH}			20	V
Critical rate of rise of off-state voltage	$V_D = 0.67 V_{DRM}$, $T_J = 25\text{ °C}$		dV/dt_{cr}	5000			V/ μs
Critical rate of rise of on-state			dV/dt_{cr}	8			A/ μs
COUPLER							
LED trigger current, current required to latch output	$V_D = 3\text{ V}$	VO4154D	I_{FT}			1.6	mA
		VO4154H	I_{FT}			2	mA
		VO4154M	I_{FT}			3	mA
		VO4156D	I_{FT}			1.6	mA
		VO4156H	I_{FT}			2	mA
		VO4156M	I_{FT}			3	mA
Common mode coupling capacitance			C_{CM}		0.01		pF
Capacitance (input to output)	$f = 1\text{ MHz}$, $V_{IO} = 0\text{ V}$		C_{IO}		0.8		pF

Note

- Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.



SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification (according to IEC68 part 1)				55/100/21		
Pollution degree (DIN VDE 0109)				2		
Comparative tracking index per DIN IEC112/VDE 0303 part 1, group IIIa per DIN VDE 6110 175 399			175		399	
V_{IOTM}		V_{IOTM}	8000			V
V_{IORM}		V_{IORM}	890			V
P_{SO}		P_{SO}			500	mW
I_{SI}		I_{SI}			250	mA
T_{SI}		T_{SI}			175	°C
Creepage distance			7			mm
Clearance distance			7			mm

TYPICAL CHARACTERISTICS ($T_{amb} = 25\text{ °C}$, unless otherwise specified)



Fig. 2 - Diode Forward Voltage vs. Forward Current



Fig. 4 - Leakage Current vs. Ambient Temperature

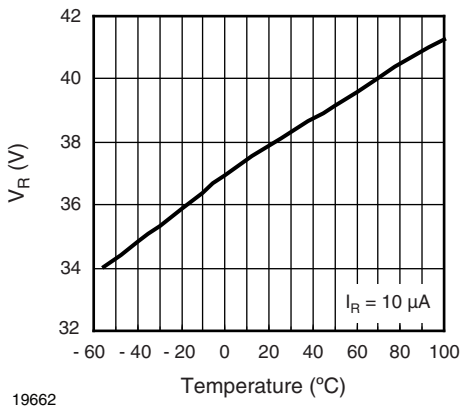


Fig. 3 - Diode Reverse Voltage vs. Temperature

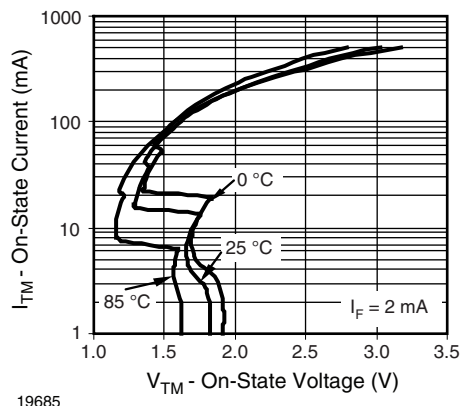
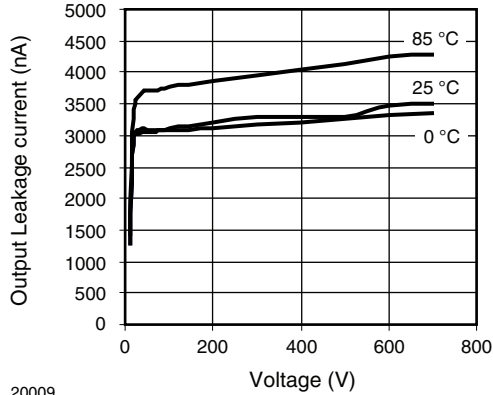
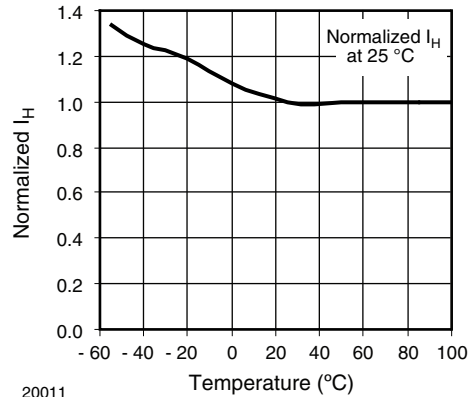


Fig. 5 - On-State Current vs. On-State Voltage



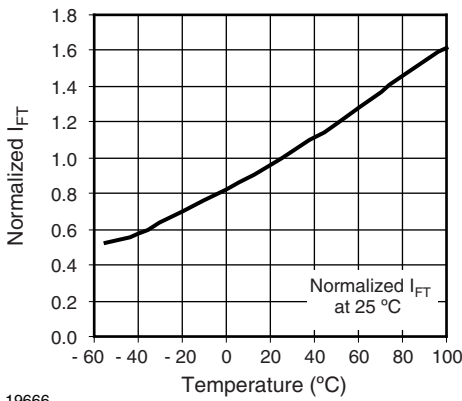
20009

Fig. 6 - Output Off Current (Leakage) vs. Voltage



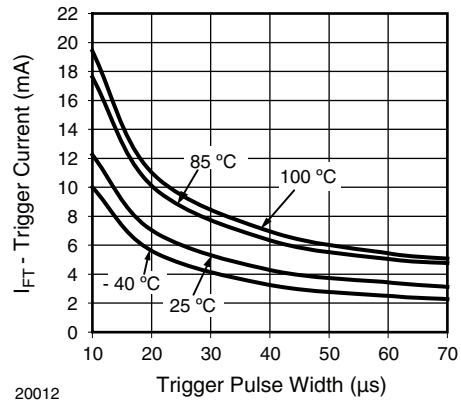
20011

Fig. 9 - Normalized Holding Current vs. Temperature



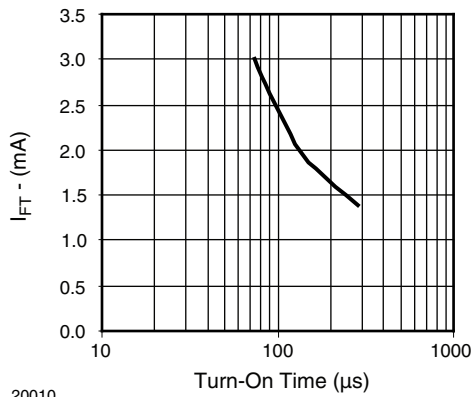
19666

Fig. 7 - Normalized Trigger Input Current vs. Temperature



20012

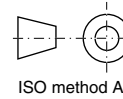
Fig. 10 - I_{FT} vs. LED Pulse Width



20010

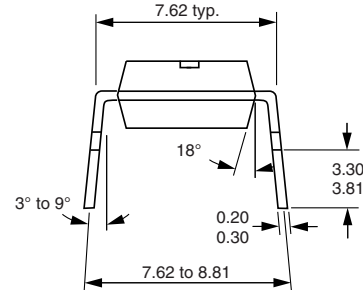
Fig. 8 - I_{FT} (mA) vs. Turn-On Time (μ s)

PACKAGE DIMENSIONS in millimeters



i178014

Option 6

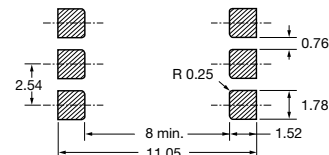
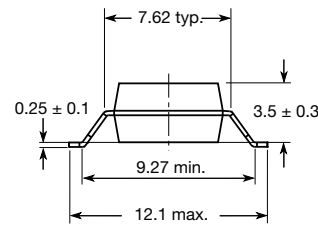
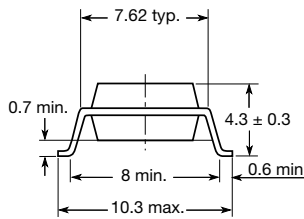


Option 7

Option 8



20802-41



PACKAGE MARKING (example)



Notes

- Only options 1, 7, and 8 are reflected in the package marking.
- The VDE Logo is only marked on option 1 parts.
- Tape and reel suffix (T) is not part of the package marking.



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