

International **IR** Rectifier

INSULATED GATE BIPOLEAR TRANSISTOR

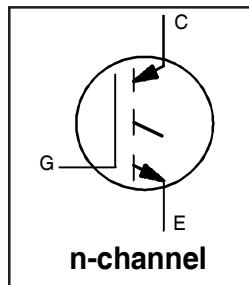
PD -95651A

IRG4BC30FPbF

Fast Speed IGBT

Features

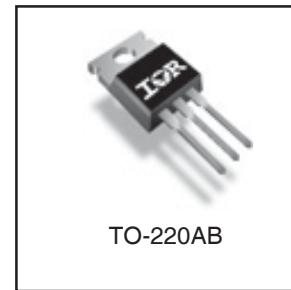
- Fast: optimized for medium operating frequencies (1-5 kHz in hard switching, >20 kHz in resonant mode).
- Generation 4 IGBT design provides tighter parameter distribution and higher efficiency than Generation 3
- Industry standard TO-220AB package
- Lead-Free



$V_{CES} = 600V$
 $V_{CE(on)} \text{ typ.} = 1.59V$
 $\text{@ } V_{GE} = 15V, I_C = 17A$

Benefits

- Generation 4 IGBTs offer highest efficiency available
- IGBTs optimized for specified application conditions
- Designed to be a "drop-in" replacement for equivalent industry-standard Generation 3 IR IGBTs



Absolute Maximum Ratings

	Parameter	Max.	Units
V_{CES}	Collector-to-Emitter Breakdown Voltage	600	V
$I_C @ T_C = 25^\circ C$	Continuous Collector Current	31	A
$I_C @ T_C = 100^\circ C$	Continuous Collector Current	17	
I_{CM}	Pulsed Collector Current ①	124	
I_{LM}	Clamped Inductive Load Current ②	124	
V_{GE}	Gate-to-Emitter Voltage	± 20	V
E_{ARV}	Reverse Voltage Avalanche Energy ③	10	mJ
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	100	W
$P_D @ T_C = 100^\circ C$	Maximum Power Dissipation	42	
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to + 150	$^\circ C$
	Soldering Temperature, for 10 seconds	300 (0.063 in. (1.6mm from case))	
	Mounting torque, 6-32 or M3 screw.	10 lbf·in (1.1N·m)	

Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	---	1.2	$^\circ C/W$
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	0.5	---	
$R_{\theta JA}$	Junction-to-Ambient, typical socket mount	---	80	
Wt	Weight	2.0 (0.07)	---	g (oz)

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Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(\text{BR})\text{CES}}$	Collector-to-Emitter Breakdown Voltage	600	—	—	V	$V_{\text{GE}} = 0\text{V}, I_C = 250\mu\text{A}$
$V_{(\text{BR})\text{ECS}}$	Emitter-to-Collector Breakdown Voltage ④	18	—	—	V	$V_{\text{GE}} = 0\text{V}, I_C = 1.0\text{A}$
$\Delta V_{(\text{BR})\text{CES}/\Delta T_J}$	Temperature Coeff. of Breakdown Voltage	—	0.69	—	V/ $^\circ\text{C}$	$V_{\text{GE}} = 0\text{V}, I_C = 1.0\text{mA}$
$V_{\text{CE}(\text{ON})}$	Collector-to-Emitter Saturation Voltage	—	1.59	1.8	V	$I_C = 17\text{A}$ $V_{\text{GE}} = 15\text{V}$
		—	1.99	—		$I_C = 31\text{A}$ See Fig.2, 5
		—	1.7	—		$I_C = 17\text{A}, T_J = 150^\circ\text{C}$
$V_{\text{GE}(\text{th})}$	Gate Threshold Voltage	3.0	—	6.0		$V_{\text{CE}} = V_{\text{GE}}, I_C = 250\mu\text{A}$
$\Delta V_{\text{GE}(\text{th})/\Delta T_J}$	Temperature Coeff. of Threshold Voltage	—	-11	—	mV/ $^\circ\text{C}$	$V_{\text{CE}} = V_{\text{GE}}, I_C = 250\mu\text{A}$
g_{fe}	Forward Transconductance ⑤	6.1	10	—	S	$V_{\text{CE}} = 100\text{V}, I_C = 17\text{A}$
I_{CES}	Zero Gate Voltage Collector Current	—	—	250	μA	$V_{\text{GE}} = 0\text{V}, V_{\text{CE}} = 600\text{V}$
		—	—	2.0		$V_{\text{GE}} = 0\text{V}, V_{\text{CE}} = 10\text{V}, T_J = 25^\circ\text{C}$
		—	—	1000		$V_{\text{GE}} = 0\text{V}, V_{\text{CE}} = 600\text{V}, T_J = 150^\circ\text{C}$
I_{GES}	Gate-to-Emitter Leakage Current	—	—	± 100	nA	$V_{\text{GE}} = \pm 20\text{V}$

Switching Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q_g	Total Gate Charge (turn-on)	—	51	77	nC	$I_C = 17\text{A}$
Q_{ge}	Gate - Emitter Charge (turn-on)	—	7.9	12		$V_{\text{CC}} = 400\text{V}$ See Fig. 8
Q_{gc}	Gate - Collector Charge (turn-on)	—	19	28		$V_{\text{GE}} = 15\text{V}$
$t_{d(\text{on})}$	Turn-On Delay Time	—	21	—	ns	$T_J = 25^\circ\text{C}$ $I_C = 17\text{A}, V_{\text{CC}} = 480\text{V}$ $V_{\text{GE}} = 15\text{V}, R_G = 23\Omega$
t_r	Rise Time	—	15	—		
$t_{d(\text{off})}$	Turn-Off Delay Time	—	200	300		
t_f	Fall Time	—	180	270	mJ	Energy losses include "tail" See Fig. 10, 11, 13, 14
E_{on}	Turn-On Switching Loss	—	0.23	—		
E_{off}	Turn-Off Switching Loss	—	1.18	—		
E_{ts}	Total Switching Loss	—	1.41	2.0		
$t_{d(\text{on})}$	Turn-On Delay Time	—	20	—	ns	$T_J = 150^\circ\text{C},$ $I_C = 17\text{A}, V_{\text{CC}} = 480\text{V}$ $V_{\text{GE}} = 15\text{V}, R_G = 23\Omega$
t_r	Rise Time	—	16	—		
$t_{d(\text{off})}$	Turn-Off Delay Time	—	290	—		
t_f	Fall Time	—	350	—	mJ	Energy losses include "tail" See Fig. 13, 14
E_{ts}	Total Switching Loss	—	2.5	—		
L_E	Internal Emitter Inductance	—	7.5	—	nH	Measured 5mm from package
C_{ies}	Input Capacitance	—	1100	—	pF	$V_{\text{GE}} = 0\text{V}$ $V_{\text{CC}} = 30\text{V}$ See Fig. 7 $f = 1.0\text{MHz}$
C_{oes}	Output Capacitance	—	74	—		
C_{res}	Reverse Transfer Capacitance	—	14	—		

Notes:

- ① Repetitive rating; $V_{\text{GE}} = 20\text{V}$, pulse width limited by max. junction temperature. (See fig. 13b)
- ② $V_{\text{CC}} = 80\%(V_{\text{CES}})$, $V_{\text{GE}} = 20\text{V}$, $L = 10\mu\text{H}$, $R_G = 23\Omega$, (See fig. 13a)
- ③ Repetitive rating; pulse width limited by maximum junction temperature.
- ④ Pulse width $\leq 80\mu\text{s}$; duty factor $\leq 0.1\%$.
- ⑤ Pulse width $5.0\mu\text{s}$, single shot.

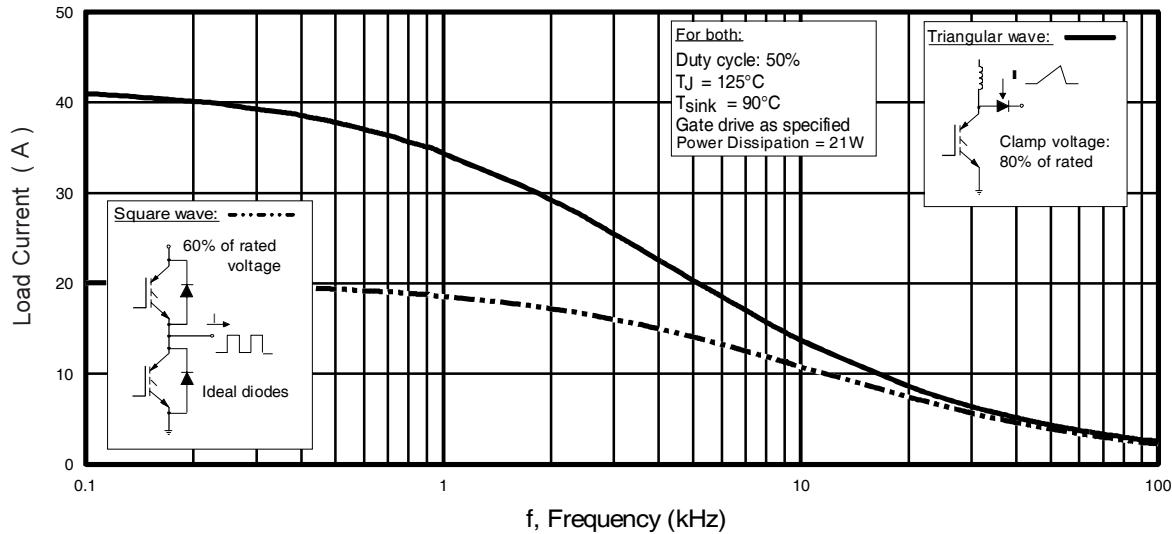


Fig. 1 - Typical Load Current vs. Frequency
 (For square wave, $I=I_{\text{RMS}}$ of fundamental; for triangular wave, $I=I_{\text{PK}}$)

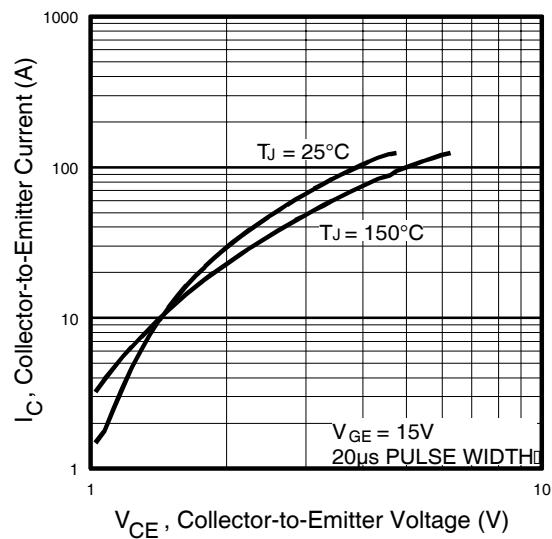


Fig. 2 - Typical Output Characteristics

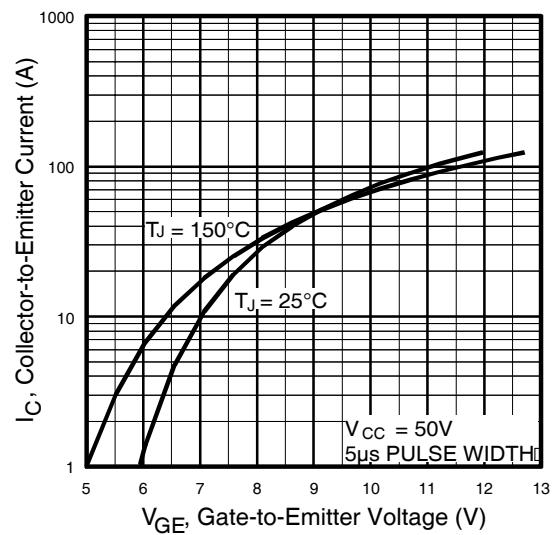


Fig. 3 - Typical Transfer Characteristics

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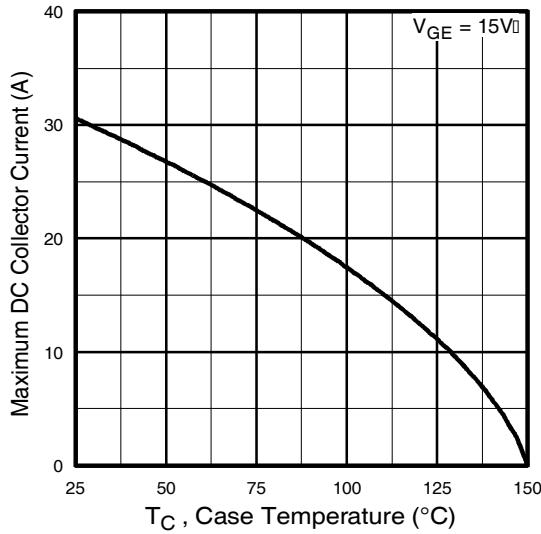


Fig. 4 - Maximum Collector Current vs. Case Temperature

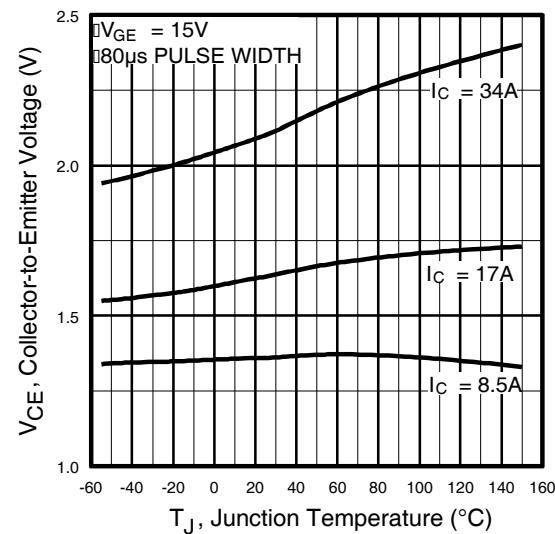


Fig. 5 - Typical Collector-to-Emitter Voltage vs. Junction Temperature

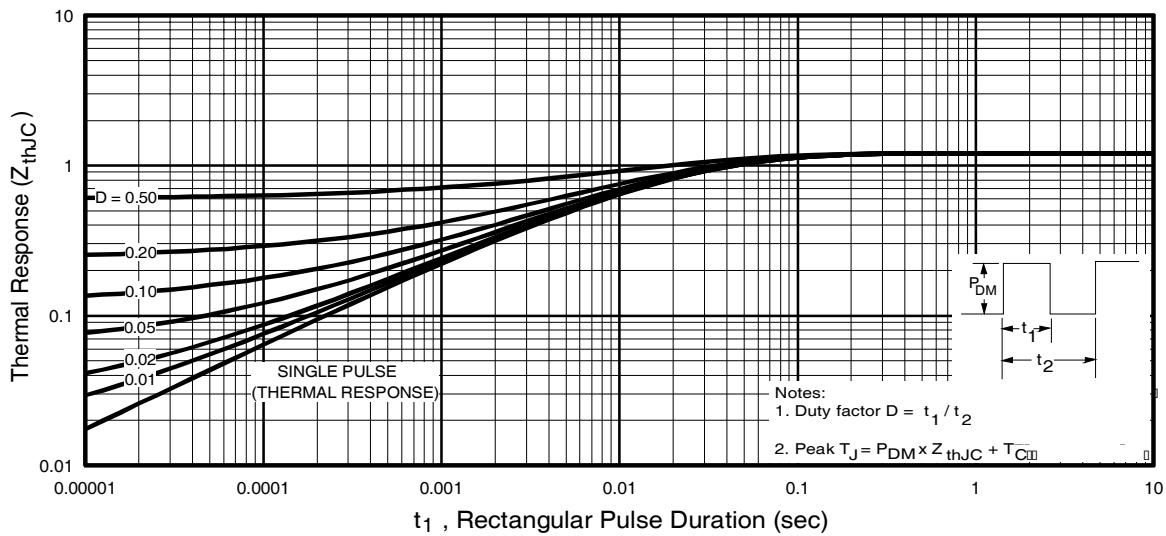
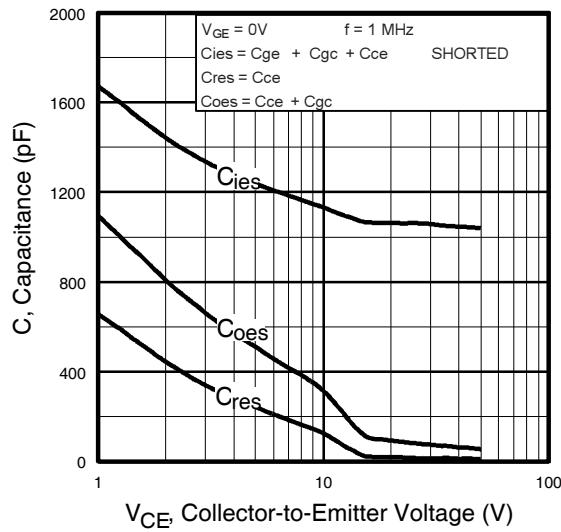
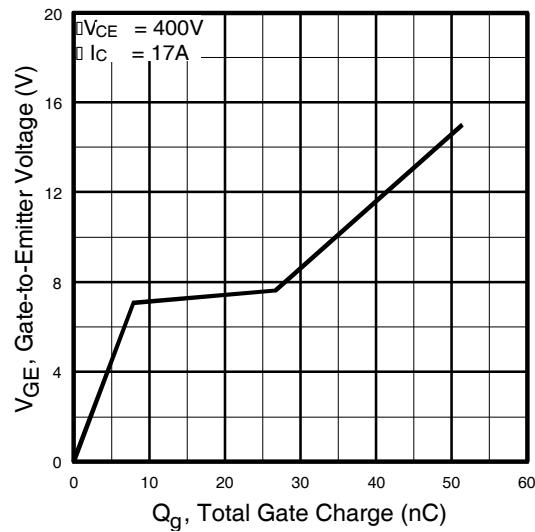


Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

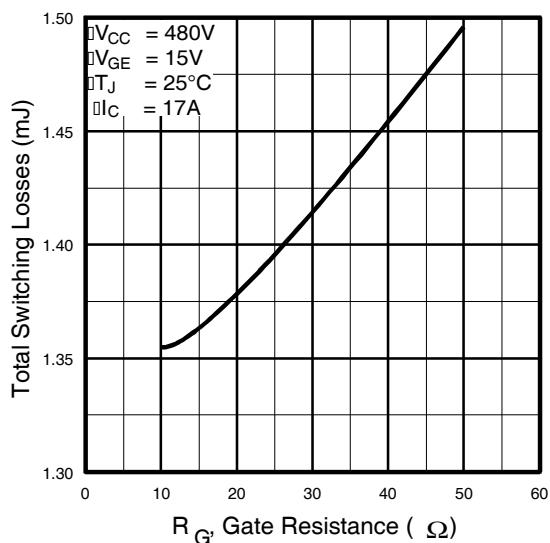
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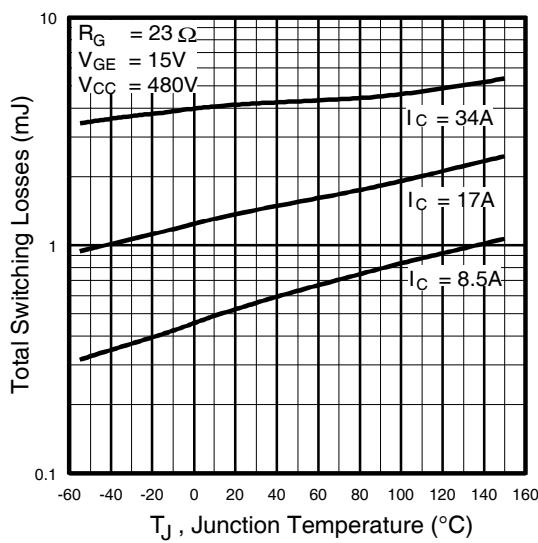
**Fig. 7 - Typical Capacitance vs.
Collector-to-Emitter Voltage**



**Fig. 8 - Typical Gate Charge vs.
Gate-to-Emitter Voltage**



**Fig. 9 - Typical Switching Losses vs. Gate
Resistance**



**Fig. 10 - Typical Switching Losses vs.
Junction Temperature**

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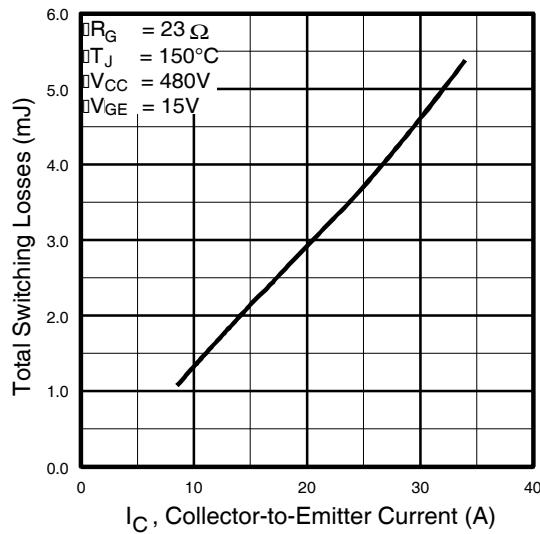


Fig. 11 - Typical Switching Losses vs.
Collector-to-Emitter Current

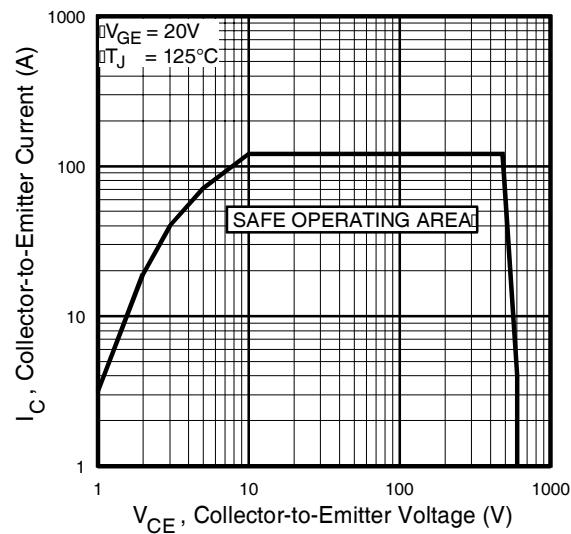
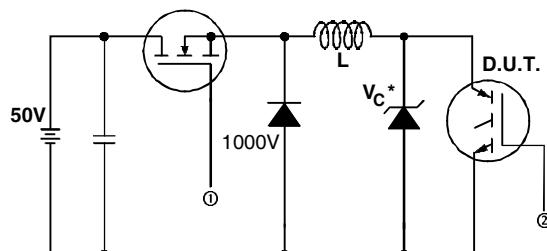


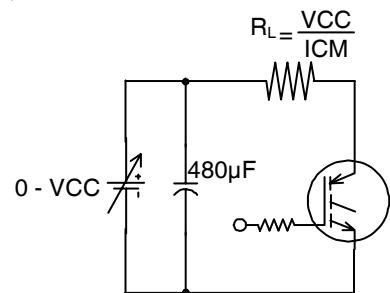
Fig. 12 - Turn-Off SOA

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* Driver same type as D.U.T.; $V_C = 80\%$ of $V_{ce(max)}$
 * Note: Due to the 50V power supply, pulse width and inductor will increase to obtain rated I_d .

Fig. 13a - Clamped Inductive Load Test Circuit



Pulsed Collector Current Test Circuit

Fig. 13b - Pulsed Collector Current Test Circuit

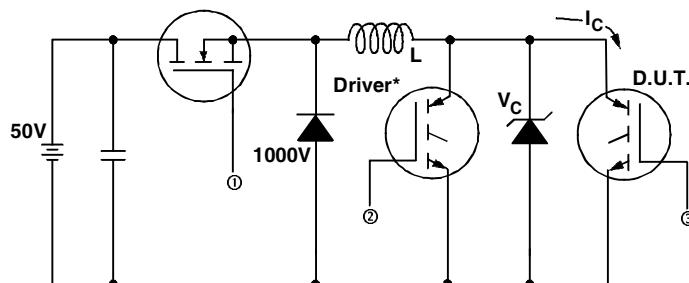


Fig. 14a - Switching Loss Test Circuit

* Driver same type as D.U.T., $V_C = 480V$

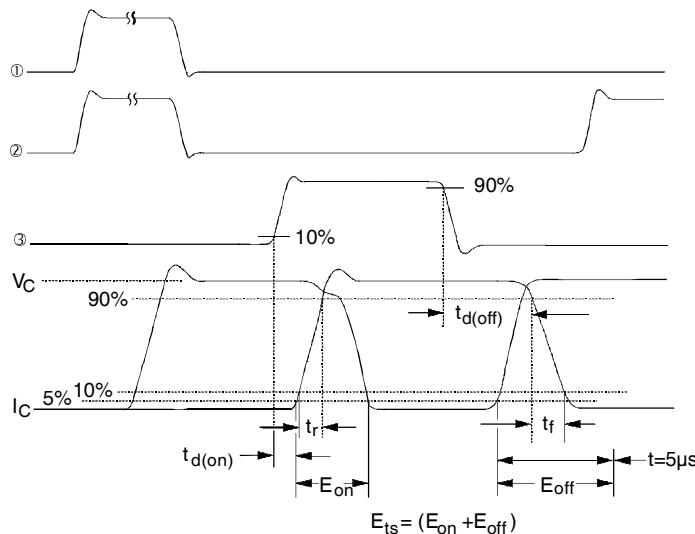
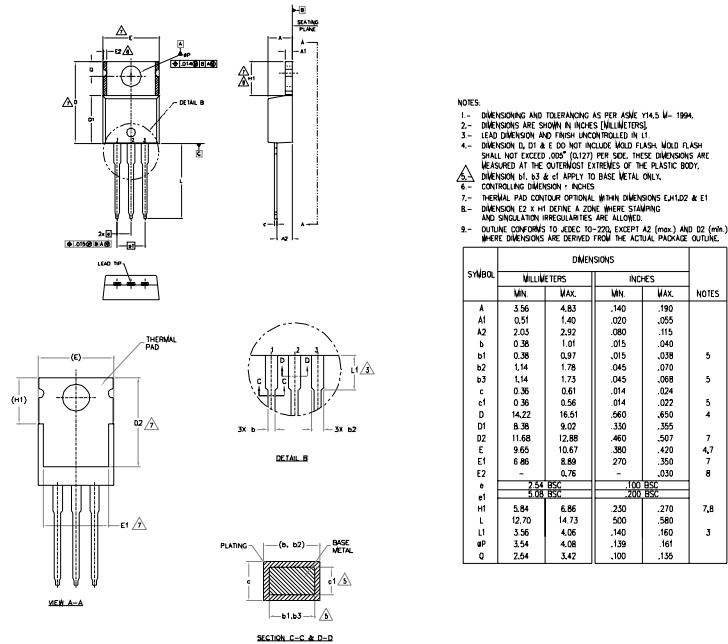


Fig. 14b - Switching Loss Waveforms

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TO-220AB Package Outline (Dimensions are shown in millimeters (inches))

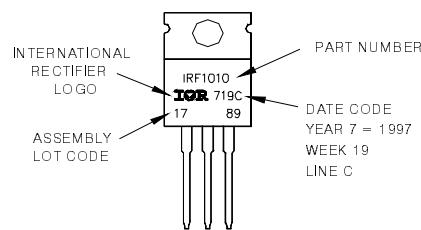


SYMBOL	DIMENSIONS			
	MILLIMETERS		INCHES	
MN	MAX.	MN	MAX.	NOTES
A	4.5	4.83	.140	.185
A1	0.51	1.40	.020	.065
A2	2.03	2.92	.080	.115
b	0.38	1.01	.015	.040
b1	0.38	0.97	.015	.038
b2	1.14	1.78	.045	.070
b3	1.38	1.73	.045	.068
c	0.35	0.61	.014	.024
c1	0.36	0.56	.014	.022
D	14.22	16.51	.560	.650
D1	8.38	9.02	.350	.355
D2	11.68	12.88	.460	.507
E	9.65	10.67	.390	.420
E1	6.66	8.59	.270	.330
E2	-	0.76	-	.030
e	2.51 BSC	2.51 BSC	100 BSC	100 BSC
e1	5.08 BSC	5.08 BSC	200 BSC	200 BSC
H1	5.84	6.66	.230	.270
L	12.70	14.73	.500	.580
L1	3.38	4.05	.140	.160
aP	3.54	4.08	.139	.161
Q	2.54	3.42	.100	.155

LEAD ASSIGNMENTS
HEIGHT
1 - GATE
2 - DRAIN
3 - SOURCE
LEAD GROUP
1 - GATE
2 - COLLECTOR
3 - EMITTER
INDEXES
1 - ANODE
2 - CATHODE
3 - ABST

TO-220AB Part Marking Information

EXAMPLE: THIS IS AN IRF1010
LOT CODE 1789
ASSEMBLED ON WW 19, 1997
IN THE ASSEMBLY LINE 'C'
Note: "P" in assembly line
position indicates "Lead-Free"



Note: For the most current drawing please refer to IR website at <http://www.irf.com/package/>

Data and specifications subject to change without notice.

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