Unit: mm

TOSHIBA Field-Effect Transistor Silicon N Channel MOS Type

SSM6N39TU

- Power Management Switch Applications
- High-Speed Switching Applications
- 1.5-V drive
- N-ch 2-in-1
- Low ON-resistance: $R_{on} = 247 \text{m}\Omega \text{ (max) (@V_{GS} = 1.5 V)}$

 R_{on} = 190m Ω (max) (@V_{GS} = 1.8 V) R_{on} = 139m Ω (max) (@V_{GS} = 2.5 V) R_{on} = 119m Ω (max) (@V_{GS} = 4.0 V)

Absolute Maximum Ratings (Ta = 25 °C) (Q1,Q2 Common)

Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	20	V	
Gate-source voltage		V_{GSS}	± 10	V	
Drain current	DC	I _D	1.6	Α	
	Pulse	I _{DP}	3.2		
Drain power dissipation		P _D (Note1)	500	mW	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in

2.1±0.1 1.7±0.1 1.7±0.1 1.90°0°C°0 2.0°0°C°0 2.0°0°C°0 3.0°0°C°0 4.5°0°C°0 2.0°0°C°0 4.5°0°C°0 2.0°0°C°0 3.0°0°C°0 3.0°0 3.0°0 3.0°0 3.0°0 3.0°0 3.0°0 3.0°0 3.0°0

Weight: 7.0mg (typ.)

temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: Mounted on an FR4 board. (total dissipation) (25.4 mm \times 25.4 mm \times 1.6 mm, Cu Pad : 645 mm²)

Electrical Characteristics (Ta = 25°C) (Q1,Q2 Common)

Char	acteristics	Symbol	Test Conditions	Min	Тур.	Max	Unit
Drain source breakdown voltage	V (BR) DSS	$I_D = 1 \text{ mA}, V_{GS} = 0 \text{ V}$	20		_	V	
Drain-source breakdown voltage		V (BR) DSX	$I_D = 1$ mA, $V_{GS} = -10$ V	12	_		_
Drain cutoff curren	t	I _{DSS}	V _{DS} =20 V, V _{GS} = 0 V	_	_	1	μΑ
Gate leakage curre	ent	I _{GSS}	$V_{GS} = \pm 10 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Gate threshold volt	tage	V _{th}	$V_{DS} = 3 \text{ V}, I_D = 1 \text{ mA}$	0.35	_	1.0	V
Forward transfer a	dmittance	Y _{fs}	$V_{DS} = 3 \text{ V}, I_D = 1A$ (Note 2)	2.5	5.0	_	S
	R _{DS} (ON)	$I_D = 1 \text{ A}, V_{GS} = 4.0 \text{ V}$ (Note 2)	_	87	119	- mΩ	
Drain-source ON-resistance		$I_D = 1 \text{ A}, V_{GS} = 2.5 \text{ V}$ (Note 2)	_	105	139		
		I _D = 0.8 A, V _{GS} = 1.8 V (Note 2)	_	125	190		
		$I_D = 0.3 \text{ A}, V_{GS} = 1.5 \text{ V}$ (Note 2)	_	145	247		
Input capacitance Output capacitance Reverse transfer capacitance		C _{iss}		_	260	_	pF
		Coss	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	45	_	
		C _{rss}		_	37	_	
Total Gate Charge Gate-Source Charge		Qg		_	7.5	_	nC
		Q _{gs}	V _{DS} = 10 V, I _D = 1.6 A, V _{GS} = 4 V	_	5.6	_	
Gate-Drain Charge		Q _{gd}		_	1.9	_	
Switching time	Turn-on time	t _{on}	V _{DD} = 10 V, I _D = 0.5 A	_	8.3		ns
	Turn-off time	t _{off}	V_{GS} = 0 to 2.5 V, R_G = 4.7 Ω	_	11.5	_	
Drain-source forward voltage		V _{DSF}	$I_D = -1.6 \text{ A}, V_{GS} = 0 \text{ V}$ (Note 2)	_	-0.8	-1.2	V

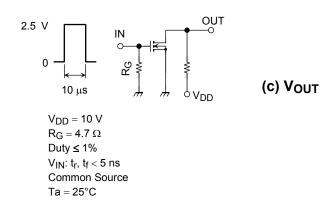
Note 2: Pulse test

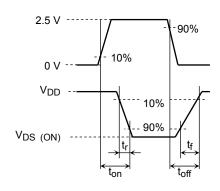
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Switching Time Test Circuit

(a) Test Circuit

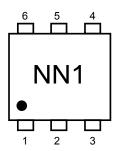


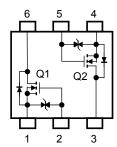




Marking

Equivalent Circuit (top view)





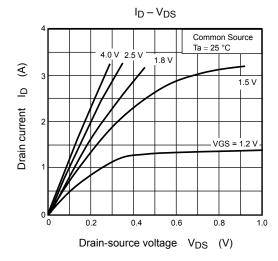
Usage Considerations

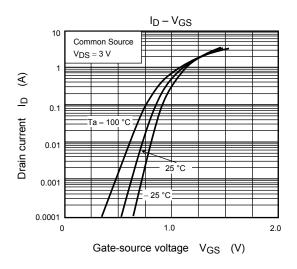
Let V_{th} be the voltage applied between gate and source that causes the drain current (I_D) to below (1 mA for the SSM6N39TU). Then, for normal switching operation, $V_{GS(on)}$ must be higher than V_{th} , and $V_{GS(off)}$ must be lower than V_{th} . This relationship can be expressed as: $V_{GS(off)} < V_{th} < V_{GS(on)}$.

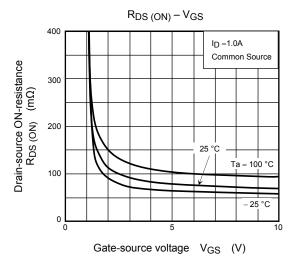
Take this into consideration when using the device.

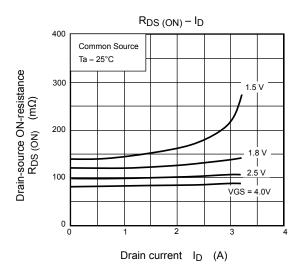
Handling Precaution

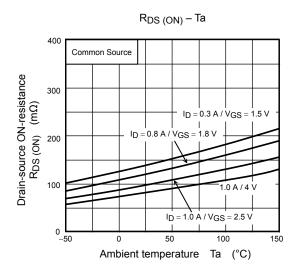
When handling individual devices that are not yet mounted on a circuit board, make sure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

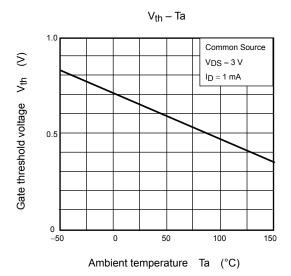


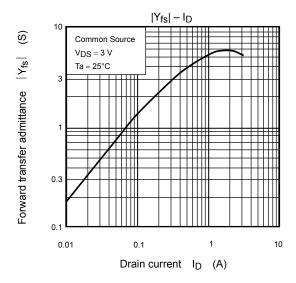


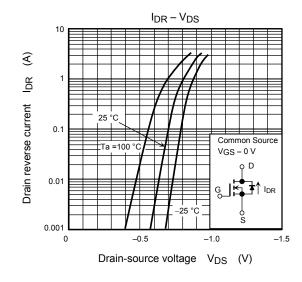


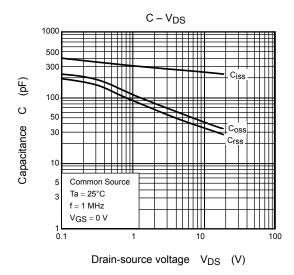


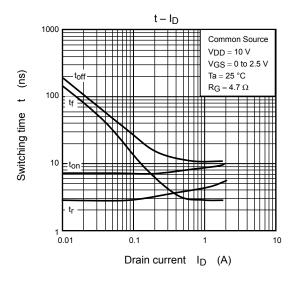


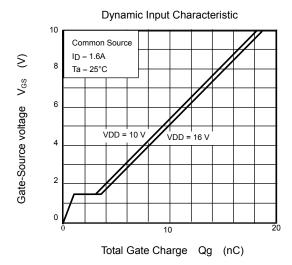


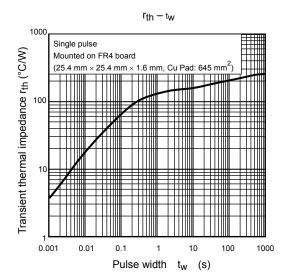


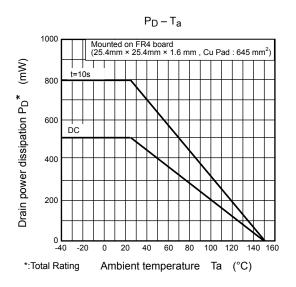












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