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EFC6604R

N-Channel Power MOSFET 12V, 13A, 9.0mΩ, Dual EFCP

Features

- 2.5V drive
- Common-drain type
- 2KV ESD HBM
- Protection diode in
- Halogen free compliance

Applications

- Lithium-ion battery charging and discharging switch

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Source to Source Voltage	V _{SSS}		12	V
Gate to Source Voltage	V _{GSS}		±12	V
Source Current (DC)	I _S		13	A
Source Current (Pulse)	I _{SP}	PW≤10μs, duty cycle≤1%	60	A
Total Dissipation	P _T	When mounted on ceramic substrate (5000mm ² ×0.8mm)	1.6	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		- 55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Electrical Characteristics at Ta = 25°C

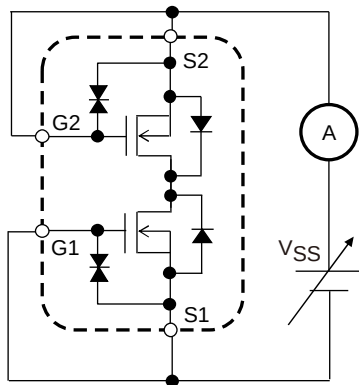
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Source to Source Breakdown Voltage	V(BR) _{SSS}	I _S =1mA, V _{GS} =0V Test Circuit 1	12			V
Zero-Gate Voltage Source Current	I _{SSS}	V _{SS} =10V, V _{GS} =0V Test Circuit 1			1	μA
Gate to Source Leakage Current	I _{GSS}	V _{GS} =±8V, V _{SS} =0V Test Circuit 2			±1.0	μA
Cutoff Voltage	V _{GS(off)}	V _{SS} =6V, I _S =1mA Test Circuit 3	0.5		1.3	V
Forward Transfer Admittance	y _{fs}	V _{SS} =6V, I _S =3A Test Circuit 4		13.7		S
Static Source to Source On-State Resistance	R _{SS(on)1}	I _S =3A, V _{GS} =4.5V Test Circuit 5	6.0	7.5	9.0	mΩ
	R _{SS(on)2}	I _S =3A, V _{GS} =4.0V Test Circuit 5	6.4	8.1	9.7	mΩ
	R _{SS(on)3}	I _S =3A, V _{GS} =3.8V Test Circuit 5	6.7	8.4	10.0	mΩ
	R _{SS(on)4}	I _S =3A, V _{GS} =3.1V Test Circuit 5	7.8	9.8	12.7	mΩ
	R _{SS(on)5}	I _S =3A, V _{GS} =2.5V Test Circuit 5	10.0	12.6	17.7	mΩ
Turn-ON Delay Time	t _{d(on)}	V _{SS} =6V, V _{GS} =4.5V, I _S =3A Test Circuit 6		300		ns
Rise Time	t _r			1200		ns
Turn-OFF Delay Time	t _{d(off)}			5200		ns
Fall Time	t _f			3900		ns
Total Gate Charge	Q _g	V _{SS} =6V, V _{GS} =4.5V, I _S =13A Test Circuit 7		29		nC
Forward Source to Source Voltage	V _{F(S-S)}	I _S =3A, V _{GS} =0V Test Circuit 8		0.75	1.2	V

ORDERING INFORMATION

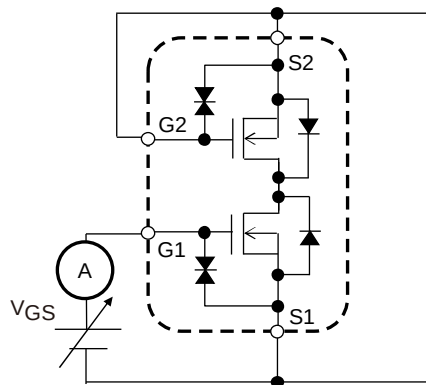
See detailed ordering and shipping information on page 5 of this data sheet.

Test circuits are example of measuring FET1 side

Test Circuit 1
 I_{SSS}

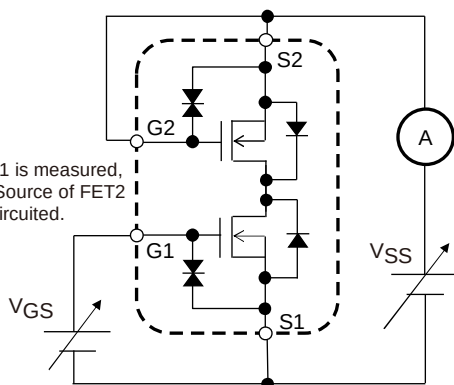


Test Circuit 2
 I_{GSS}



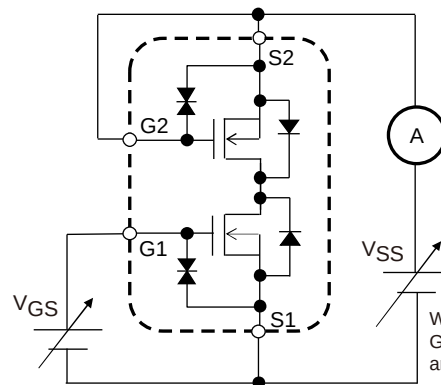
When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 3
 $V_{GS(off)}$



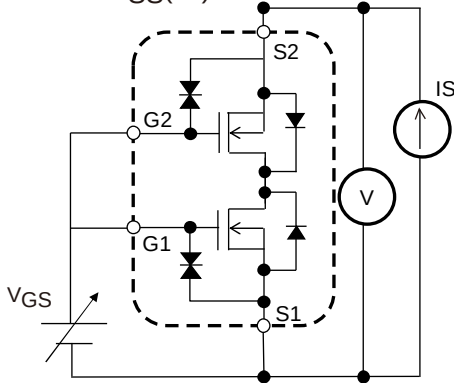
When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 4
 $|y_{fs}|$

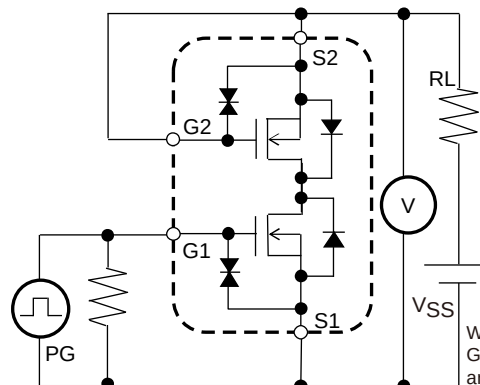


When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 5
 $R_{SS(on)}$

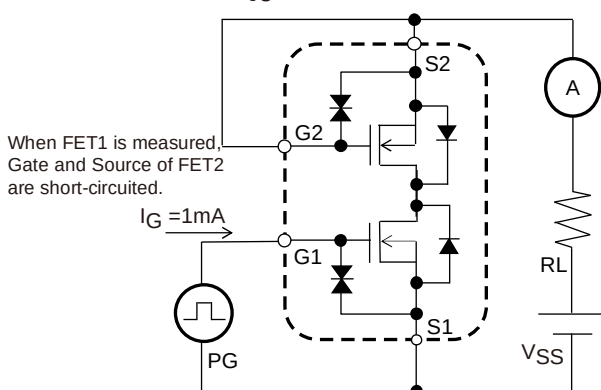


Test Circuit 6
 $t_d(on), t_r, t_d(off), t_f$



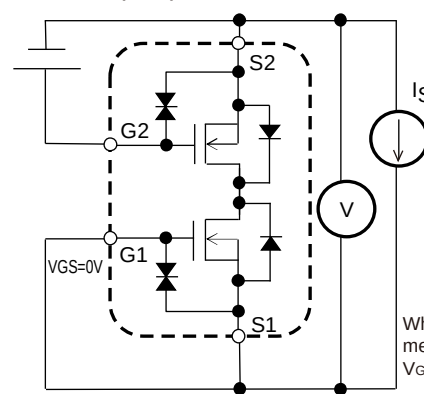
When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 7
 Q_g

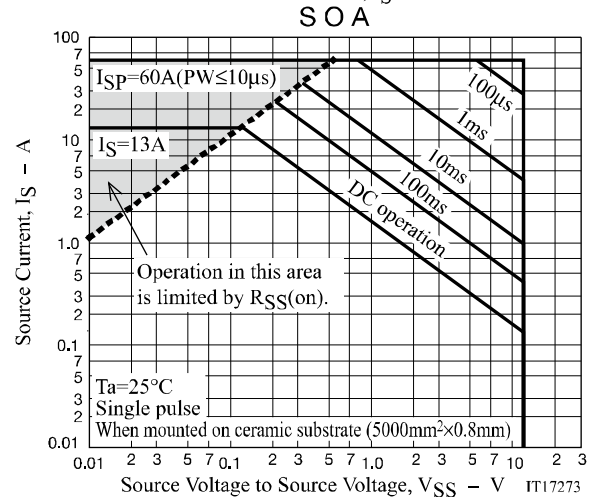
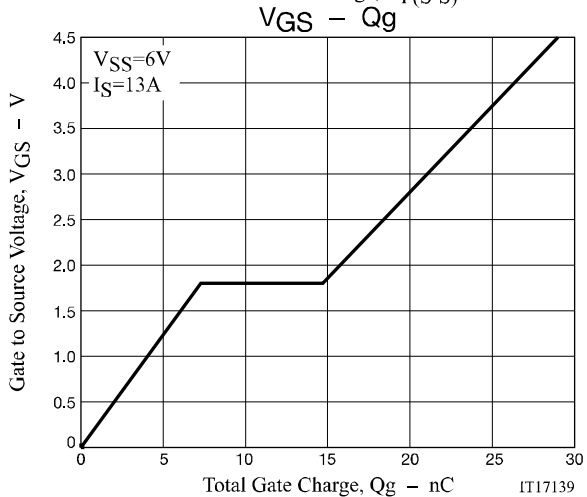
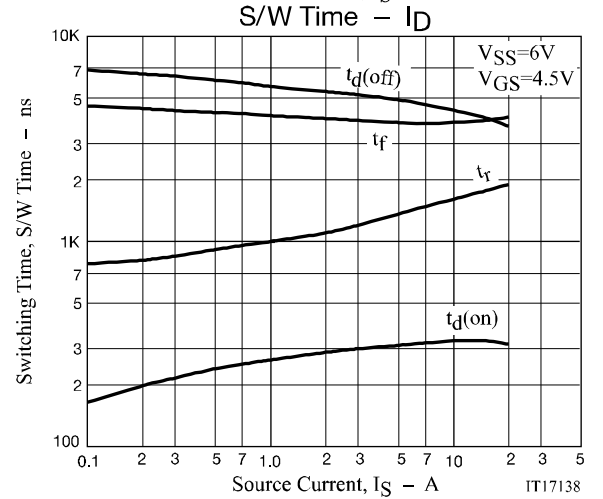
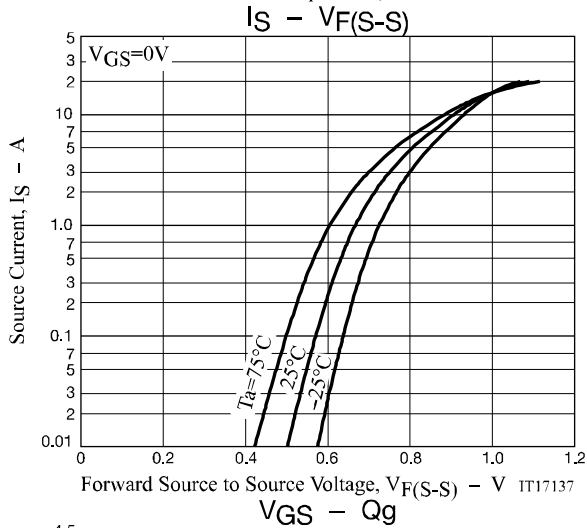
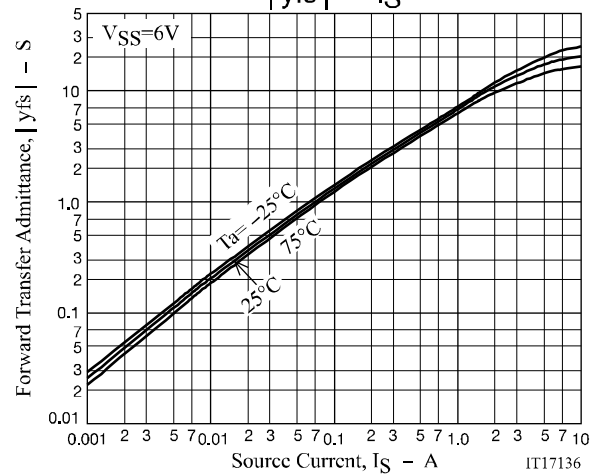
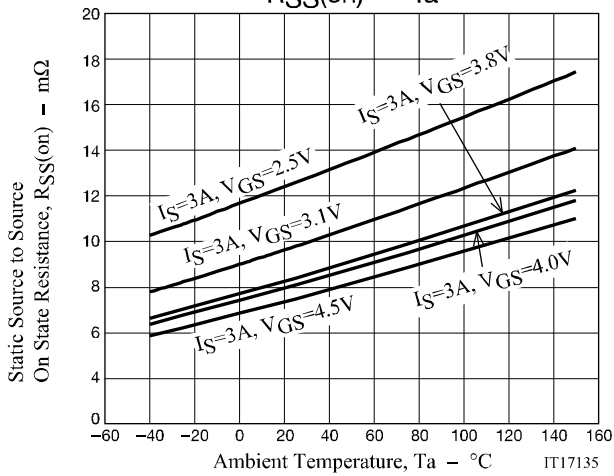
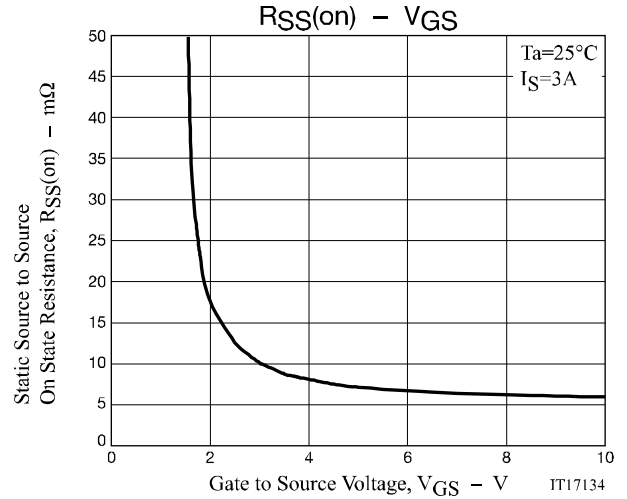
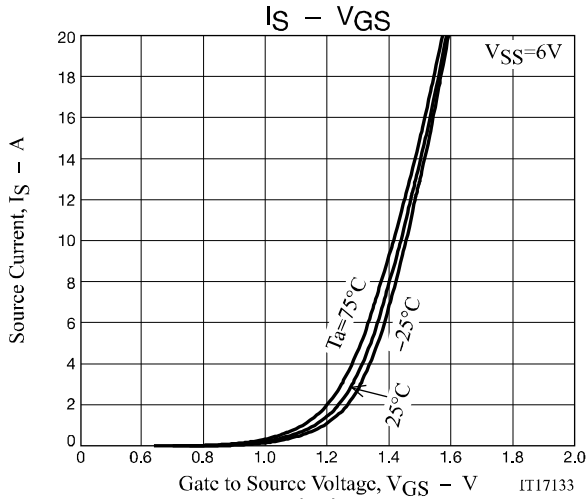


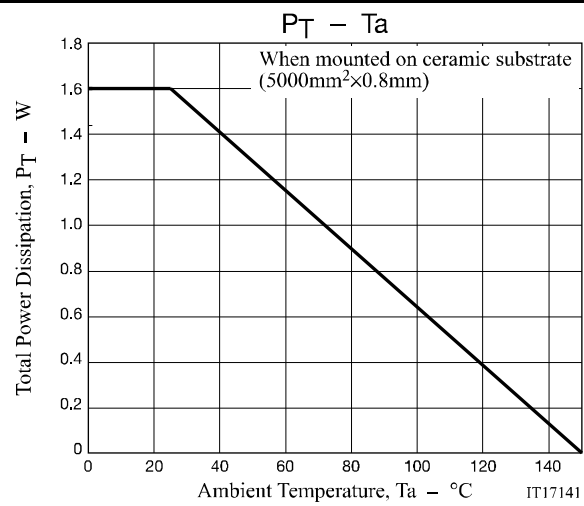
When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 8
 $V_F(S-S)$



When FET1 is measured, +4.5V is added to V_{GS} of FET2.





Note on usage : Since the EFC6604R is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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