

# 10V Drive Nch MOSFET

# RCD075N20

#### Structure

Silicon N-channel MOSFET

#### Features

- 1) Low on-resistance.
- 2) High-speed switching.
- 3) Wide range of SOA.
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.

# Application

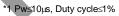
Switching

# Packaging specifications

	• .	
	Package	Taping
Type	Code	TL
	Basic ordering unit (pieces)	2500
RCD075N2	0	

# • Absolute maximum ratings (Ta = 25°C)

Paramete	Symbol	Limits	Unit	
Drain-source voltage	$V_{\rm DSS}$	200	V	
Gate-source voltage		$V_{GSS}$	±30	V
Drain current	Continuous	I <sub>D</sub> *3	±7.5	Α
Diam current	Pulsed	I <sub>DP</sub> *1	±30	Α
Source current	Continuous	l <sub>s</sub> *3	7.5	Α
(Body Diode)	Pulsed	I <sub>SP</sub> *1	30	Α
Avalanche current		I <sub>AS</sub> *2	3.75	Α
Avalanche energy		E <sub>AS</sub> *2	4.13	mJ
Power dissipation		$P_D^{*4}$	52	W
Channel temperature	Tch	150	°C	
Range of storage temper	erature	Tstg	-55 to +150	°C



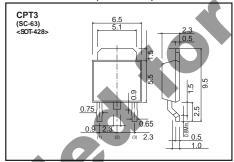
<sup>\*2</sup> L  $\stackrel{\bullet}{=}$  500 $\mu$ H,  $V_{DD}$ =50V,  $R_G$ =25 $\Omega$ ,  $T_{ch}$ =25 $^{\circ}$ C

# • Thermal resistance

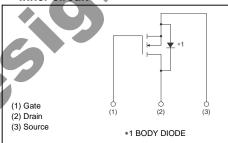
Parameter	Symbol	Limits	Unit
Channel to Case	Rth (j-c) *	2.36	°C/W

<sup>\*</sup> T<sub>C</sub>=25°C

# • Dimensions (Unit : mm)



#### Inner circuit



<sup>\*3</sup> Limited only by maximum channel temperature allowed.

<sup>\*4</sup> T<sub>C</sub>=25°C

<sup>\*</sup> Limited only by maximum channel temperature allowed.

# • Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	$I_{GSS}$	-	-	±100	nA	$V_{GS}=\pm30V, V_{DS}=0V$
Drain-source breakdown voltage	$V_{(BR)DSS}$	200	-	-	V	I <sub>D</sub> =1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>	-	-	10	μA	V <sub>DS</sub> =200V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS (th)</sub>	3.25	-	5.25	V	$V_{DS}$ =10V, $I_{D}$ =1mA
Static drain-source on-state resistance	R <sub>DS (on)</sub> *	-	250	325	mΩ	I <sub>D</sub> =3.75A, V <sub>GS</sub> =10V
Forward transfer admittance	IY <sub>fs</sub> I*	1.5	3.0	-	S	V <sub>DS</sub> =10V, I <sub>D</sub> =3.75A
Input capacitance	C <sub>iss</sub>	-	755	-	pF	V <sub>DS</sub> =25V
Output capacitance	C <sub>oss</sub>	-	55	-	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	C <sub>rss</sub>	-	25	-	pF	f=1MHz
Turn-on delay time	t <sub>d(on)</sub> *	-	20	-	ns	V <sub>DD</sub> ≒100V, I <sub>D</sub> =3.75A
Rise time	t <sub>r</sub> *	-	22	-	ns	V <sub>GS</sub> =10V
Turn-off delay time	t <sub>d(off)</sub> *	-	24	-	ns	$R_L = 26.67\Omega$
Fall time	t <sub>f</sub> *	-	12	-	ns	$R_G=10\Omega$
Total gate charge	Q <sub>g</sub> *	-	15		nC	V <sub>DD</sub> ≒100V, I <sub>D</sub> =7.5A
Gate-source charge	Q <sub>gs</sub> *	-	6		nC	V <sub>GS</sub> =10V
Gate-drain charge	Q <sub>gd</sub> *	-	6	-	nC	

<sup>\*</sup>Pulsed

# ●Body diode characteristics (Source-Drain)

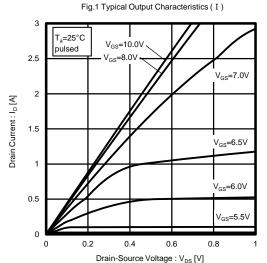
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward Voltage	V <sub>SD</sub> *	-		1.5	V	$I_s$ =7.5A, $V_{GS}$ =0V
*Pulsed	C			O	<b>%</b>	
			6			

<sup>\*</sup>Pulsed



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# ●Electrical characteristic curves (Ta=25°C)



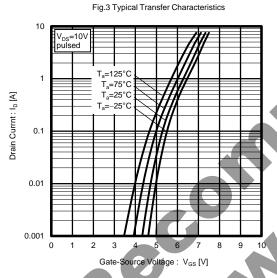
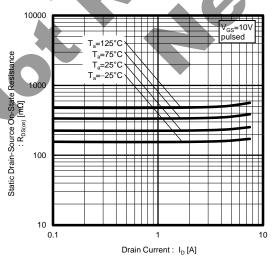


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current



-V<sub>GS</sub>=10.0V T<sub>a</sub>=25°C V<sub>GS</sub>=8.0V pulsed 6 Drain Current : I<sub>D</sub> [A] V<sub>GS</sub>=6.5V 2 V<sub>GS</sub>=6.0V V<sub>GS</sub>=5.5V

Drain-Source Voltage : V<sub>DS</sub> [V]

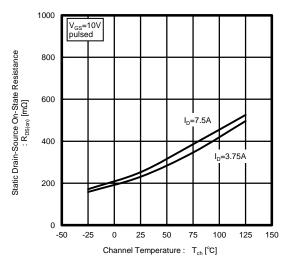
g.4 Gate Threshold Voltage vs. Channel Temperature

Fig.2 Typical Output Characteristics ( II )

V<sub>DS</sub>=10V I<sub>D</sub>=1mA pulsed

-50 -25 150 125 Channel Temperature :  $T_{ch}$  [°C]

Fig.6 Static Drain-Source On-State Resistance vs. Channel Temperature



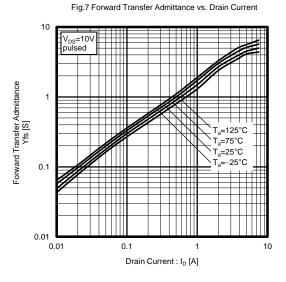


Fig.8 Source Current vs. Source-Drain Voltage

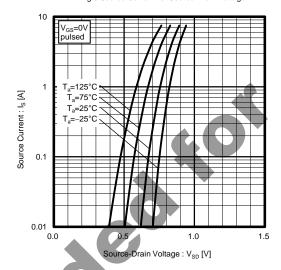


Fig.9 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

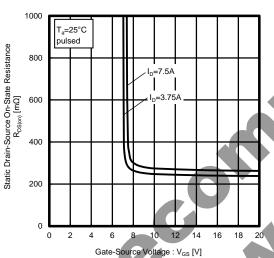


Fig.10 Switching Characteristics

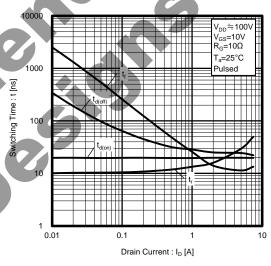


Fig.11 Dynamic Input Characteristics

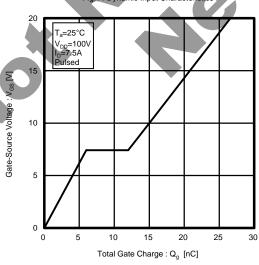
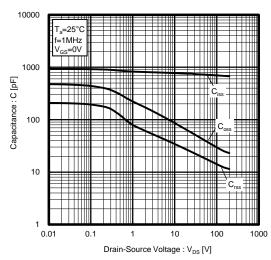


Fig.12 Typical Capacitance vs. Drain-Source Voltage



# Measurement circuits

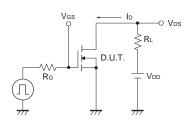


Fig.1-1 Switching Time Measurement Circuit

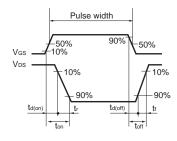


Fig.1-2 Switching Waveforms

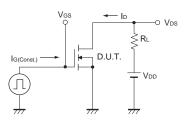


Fig.2-1 Gate Charge Measurement Circuit

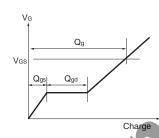


Fig.2-2 Gate Charge Waveform

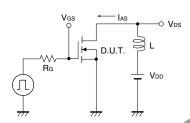


Fig.3-1 Avalanche Measurement Circuit

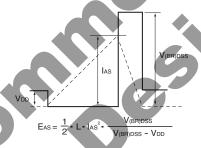


Fig.3-2 Avalanche Waveform

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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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