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April 1st, 2010 Renesas Electronics Corporation

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MOS FIELD EFFECT TRANSISTOR 2SK3482

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The 2SK3482 is N-channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

· Low on-state resistance

 $R_{DS(on)1}=33~m\Omega$ MAX. (VGs = 10 V, ID = 18 A)

RDS(on)2 = 39 m Ω MAX. (VGS = 4.5 V, ID = 18 A)

- Low Ciss: Ciss = 3600 pF TYP.
- · Built-in gate protection diode
- TO-251/TO-252 package

ORDERING INFORMATION

PART NUMBER	ER PACKAGE		
2SK3482	TO-251 (MP-3)		
2SK3482-Z	TO-252 (MP-3Z)		

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

	(
Drain to Source Voltage (Vgs = 0 V)	VDSS	100	V
Gate to Source Voltage (Vps = 0 V)	Vgss	±20	V
Drain Current (DC)	I _{D(DC)}	±36	Α
Drain Current (Pulse) Note1	D(pulse)	±100	Α
Total Power Dissipation (Tc = 25°C)	PT	50	W
Total Power Dissipation (T _A = 25°C)	PT	1.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C
Single Avalanche Current Note2	las	30	Α
Single Avalanche Energy Note2	Eas	90	mJ

(TO-251)



(TO-252)



Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Starting T_{ch} = 25°C, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V

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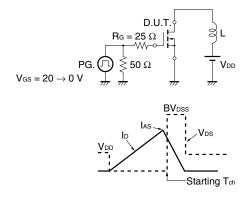
ELECTRICAL CHARACTERISTICS (TA = 25°C)

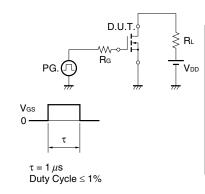
				i e		
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 100 V, V _{GS} = 0 V			10	μA
Leakage Current	Igss	Vgs = ±20 V, Vps = 0 V			±10	μΑ
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	1.5	2.0	2.5	V
Forward Transfer Admittance Note	yfs	V _{DS} = 10 V, I _D = 18 A	12	23		S
Drain to Source On-state Resistance Note	RDS(on)1	Vgs = 10 V, Ip = 18 A		27	33	mΩ
	R _{DS(on)2}	Vgs = 4.5 V, lp = 18 A		29	39	mΩ
Input Capacitance	Ciss	V _{DS} = 10 V		3600		pF
Output Capacitance	Coss	Ves = 0 V		360		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		190		pF
Turn-on Delay Time	td(on)	V _{DD} = 50 V, I _D = 18 A		15		ns
Rise Time	tr	V _G s = 10 V		10		ns
Turn-off Delay Time	td(off)	$R_G = 0 \Omega$		68		ns
Fall Time	t f			6		ns
Total Gate Charge	Q _G	V _{DD} = 80 V		72		nC
Gate to Source Charge	Qgs	Vcs = 10 V		10		nC
Gate to Drain Charge	Q _{GD}	ID = 36 A		19		nC
Body Diode Forward Voltage Note	V _F (S-D)	IF = 36 A, Vgs = 0 V		1.0		V
Reverse Recovery Time	trr	IF = 36 A, Vgs = 0 V		70		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		180		nC

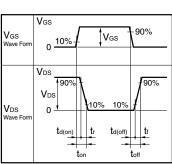
Note Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY

<R> TEST CIRCUIT 2 SWITCHING TIME



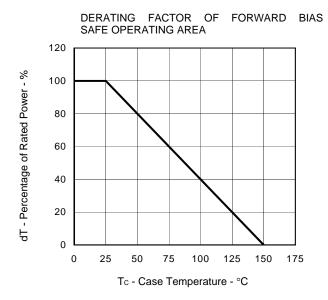


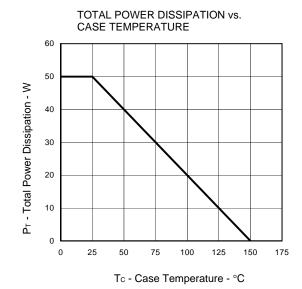


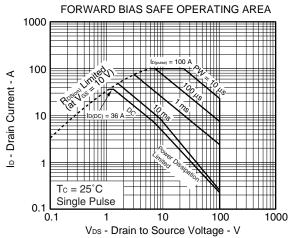
TEST CIRCUIT 3 GATE CHARGE

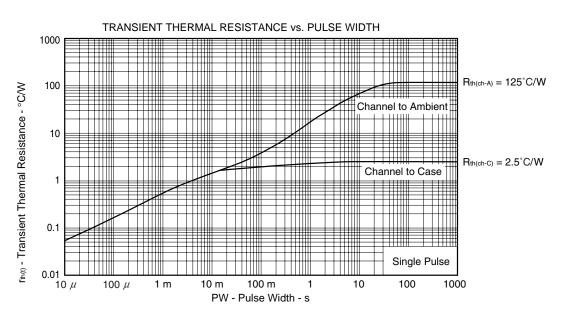


TYPICAL CHARACTERISTICS (TA = 25°C)



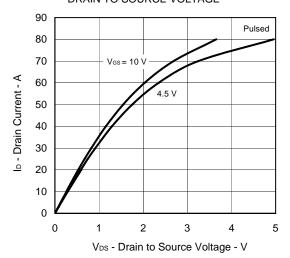




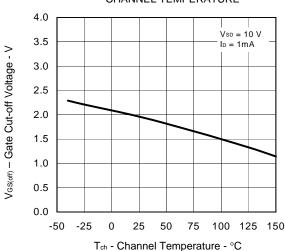


3

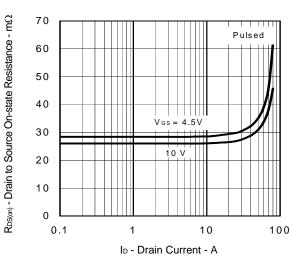
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



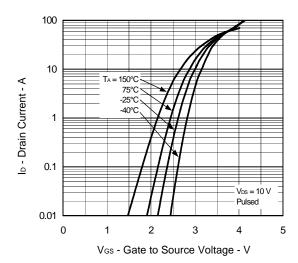
GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



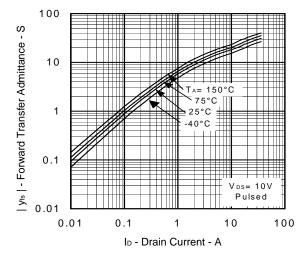
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



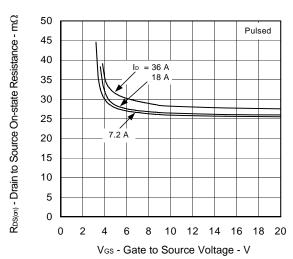
FORWARD TRANSFER CHARACTERISTICS



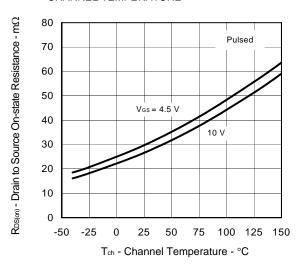
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



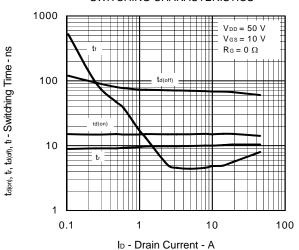
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



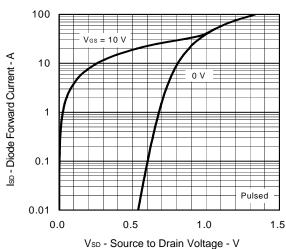
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



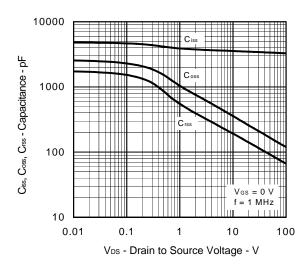
SWITCHING CHARACTERISTICS



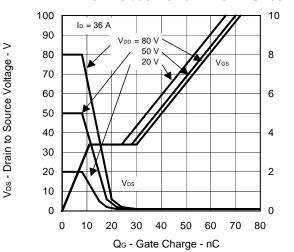
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



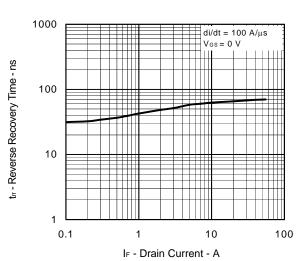
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



DYNAMIC INPUT/OUTPUT CHARACTERISTICS



REVERSE RECOVERY TIME vs. DRAIN CURRENT



Ves - Gate to Drain Voltage - V

1

0.001

INDUCTIVE LOAD 1000 Vob = 50 V Vos = $20 \rightarrow 0$ V Re = 25Ω 100 IAS = 30 AEAS = 90 mJ

0.01

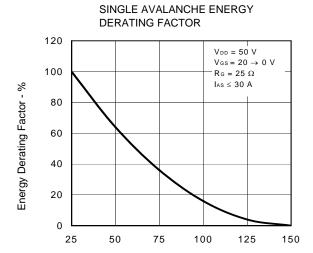
0.1

L - Inductive Load - mH

10

1

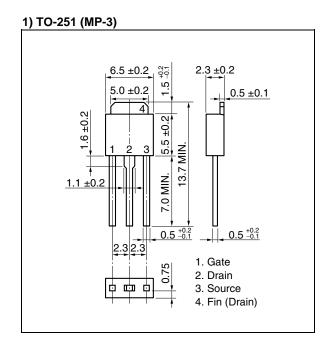
SINGLE AVALANCHE CURRENT vs.

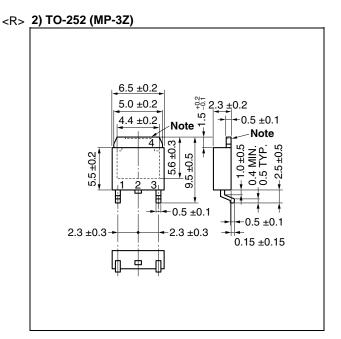


Starting T_{ch} - Starting Channel Temperature - $^{\circ}C$



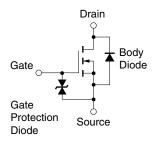
PACKAGE DRAWINGS (Unit: mm)





Note The depth of notch at the top of the fin is from 0 to 0.2 mm.

EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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