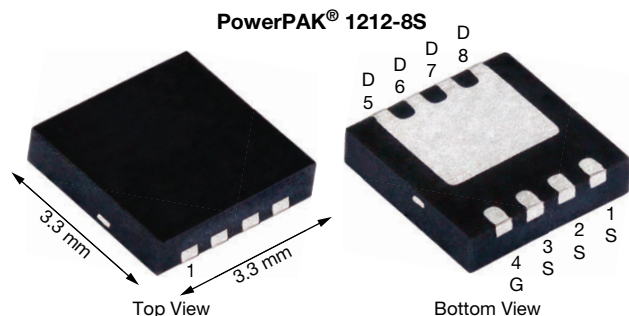


# N-Channel 40 V (D-S) MOSFET



| PRODUCT SUMMARY                                    |                    |
|----------------------------------------------------|--------------------|
| $V_{DS}$ (V)                                       | 40                 |
| $R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 10$ V  | 0.00198            |
| $R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 4.5$ V | 0.00274            |
| $Q_g$ typ. (nC)                                    | 28.7               |
| $I_D$ (A)                                          | 60 <sup>a, g</sup> |
| Configuration                                      | Single             |

## FEATURES

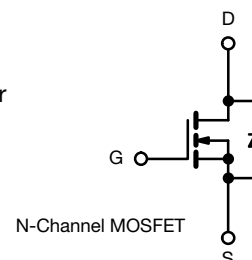
- TrenchFET® Gen IV power MOSFET
- Very low  $R_{DS(on)}$  in a compact and thermally enhanced package
- Optimized  $Q_g$ ,  $Q_{gd}$ , and  $Q_{gd}/Q_{gs}$  ratio reduces switching related power loss
- 100 %  $R_g$  and UIS tested
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
FREE

## APPLICATIONS

- Synchronous rectification
- Synchronous buck converter
- High power density DC/DC
- OR-ing
- Load switching



| ORDERING INFORMATION            |                  |
|---------------------------------|------------------|
| Package                         | PowerPAK 1212-8S |
| Lead (Pb)-free and halogen-free | SiSS12DN-T1-GE3  |

| ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C, unless otherwise noted) |               |                |                      |      |
|-------------------------------------------------------------------|---------------|----------------|----------------------|------|
| PARAMETER                                                         |               | SYMBOL         | LIMIT                | UNIT |
| Drain-source voltage                                              |               | $V_{DS}$       | 40                   | V    |
| Gate-source voltage                                               |               | $V_{GS}$       | +20 / -16            |      |
| Continuous drain current ( $T_J = 150$ °C)                        | $T_C = 25$ °C | $I_D$          | 60 <sup>a</sup>      | A    |
|                                                                   | $T_C = 70$ °C |                | 60 <sup>a</sup>      |      |
|                                                                   | $T_A = 25$ °C |                | 37.5 <sup>b, c</sup> |      |
|                                                                   | $T_A = 70$ °C |                | 30 <sup>b, c</sup>   |      |
| Pulsed drain current ( $t = 100$ $\mu$ s)                         |               | $I_{DM}$       | 200                  |      |
| Continuous source-drain diode current                             | $T_C = 25$ °C | $I_S$          | 59.7                 |      |
|                                                                   | $T_A = 25$ °C |                | 4.5 <sup>b, c</sup>  |      |
| Single pulse avalanche current                                    | L = 0.1 mH    | $I_{AS}$       | 30                   | mJ   |
| Single pulse avalanche energy                                     |               | $E_{AS}$       | 45                   |      |
| Maximum power dissipation                                         | $T_C = 25$ °C | $P_D$          | 65.7                 | W    |
|                                                                   | $T_C = 70$ °C |                | 42                   |      |
|                                                                   | $T_A = 25$ °C |                | 5 <sup>b, c</sup>    |      |
|                                                                   | $T_A = 70$ °C |                | 3.2 <sup>b, c</sup>  |      |
| Operating junction and storage temperature range                  |               | $T_J, T_{stg}$ | -55 to +150          | °C   |
| Soldering recommendations (peak temperature) <sup>c</sup>         |               |                | 260                  |      |

| THERMAL RESISTANCE RATINGS               |               |            |         |         |      |
|------------------------------------------|---------------|------------|---------|---------|------|
| PARAMETER                                |               | SYMBOL     | TYPICAL | MAXIMUM | UNIT |
| Maximum junction-to-ambient <sup>b</sup> | $t \leq 10$ s | $R_{thJA}$ | 20      | 25      | °C/W |
| Maximum junction-to-case (drain)         | Steady state  | $R_{thJC}$ | 1.5     | 1.9     |      |

### Notes

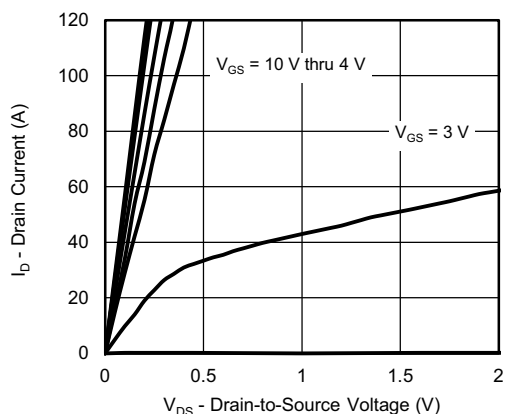
- Package limited
- Surface mounted on 1" x 1" FR4 board
- $t =$



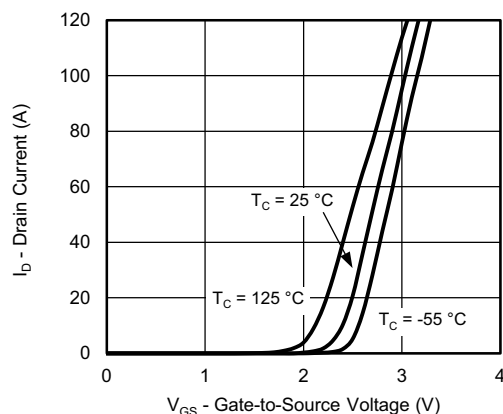
| <b>SPECIFICATIONS</b> ( $T_J = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted) |                         |                                                                                                                                  |      |         |           |                        |
|--------------------------------------------------------------------------------------|-------------------------|----------------------------------------------------------------------------------------------------------------------------------|------|---------|-----------|------------------------|
| PARAMETER                                                                            | SYMBOL                  | TEST CONDITIONS                                                                                                                  | MIN. | TYP.    | MAX.      | UNIT                   |
| <b>Static</b>                                                                        |                         |                                                                                                                                  |      |         |           |                        |
| Drain-source breakdown voltage                                                       | $V_{DS}$                | $V_{GS} = 0\text{ V}$ , $I_D = 250\text{ }\mu\text{A}$                                                                           | 40   | -       | -         | V                      |
| $V_{DS}$ temperature coefficient                                                     | $\Delta V_{DS}/T_J$     | $I_D = 250\text{ }\mu\text{A}$                                                                                                   | -    | 22      | -         | mV/ $^{\circ}\text{C}$ |
| $V_{GS(th)}$ temperature coefficient                                                 | $\Delta V_{GS(th)}/T_J$ |                                                                                                                                  | -    | -6.3    | -         |                        |
| Gate-source threshold voltage                                                        | $V_{GS(th)}$            | $V_{DS} = V_{GS}$ , $I_D = 250\text{ }\mu\text{A}$                                                                               | 1.1  | -       | 2.4       | V                      |
| Gate-source leakage                                                                  | $I_{GSS}$               | $V_{DS} = 0\text{ V}$ , $V_{GS} = +20, -16\text{ V}$                                                                             | -    | -       | $\pm 100$ | nA                     |
| Zero gate voltage drain current                                                      | $I_{DSS}$               | $V_{DS} = 40\text{ V}$ , $V_{GS} = 0\text{ V}$                                                                                   | -    | -       | 1         | $\mu\text{A}$          |
|                                                                                      |                         | $V_{DS} = 40\text{ V}$ , $V_{GS} = 0\text{ V}$ , $T_J = 55\text{ }^{\circ}\text{C}$                                              | -    | -       | 10        |                        |
| On-state drain current <sup>a</sup>                                                  | $I_{D(on)}$             | $V_{DS} \geq 5\text{ V}$ , $V_{GS} = 10\text{ V}$                                                                                | 30   | -       | -         | A                      |
| Drain-source on-state resistance <sup>a</sup>                                        | $R_{DS(on)}$            | $V_{GS} = 10\text{ V}$ , $I_D = 10\text{ A}$                                                                                     | -    | 0.00161 | 0.00198   | $\Omega$               |
|                                                                                      |                         | $V_{GS} = 4.5\text{ V}$ , $I_D = 5\text{ A}$                                                                                     | -    | 0.00230 | 0.00274   |                        |
| Forward transconductance <sup>a</sup>                                                | $g_{fs}$                | $V_{DS} = 10\text{ V}$ , $I_D = 20\text{ A}$                                                                                     | -    | 151     | -         | S                      |
| <b>Dynamic <sup>b</sup></b>                                                          |                         |                                                                                                                                  |      |         |           |                        |
| Input capacitance                                                                    | $C_{iss}$               | $V_{DS} = 20\text{ V}$ , $V_{GS} = 0\text{ V}$ , $f = 1\text{ MHz}$                                                              | -    | 4270    | -         | pF                     |
| Output capacitance                                                                   | $C_{oss}$               |                                                                                                                                  | -    | 680     | -         |                        |
| Reverse transfer capacitance                                                         | $C_{rss}$               |                                                                                                                                  | -    | 90      | -         |                        |
| $C_{rss}/C_{iss}$ ratio                                                              |                         |                                                                                                                                  | -    | 0.021   | 0.042     |                        |
| Total gate charge                                                                    | $Q_g$                   | $V_{DS} = 20\text{ V}$ , $V_{GS} = 10\text{ V}$ , $I_D = 10\text{ A}$                                                            | -    | 59      | 89        | nC                     |
| Gate-source charge                                                                   | $Q_{gs}$                | $V_{DS} = 20\text{ V}$ , $V_{GS} = 4.5\text{ V}$ , $I_D = 10\text{ A}$                                                           | -    | 28.7    | 43        |                        |
| Gate-drain charge                                                                    | $Q_{gd}$                |                                                                                                                                  | -    | 10.2    | -         |                        |
| Output charge                                                                        | $Q_{oss}$               |                                                                                                                                  | -    | 7.6     | -         |                        |
| Gate resistance                                                                      | $R_g$                   | $V_{DS} = 20\text{ V}$ , $V_{GS} = 0\text{ V}$                                                                                   | -    | 28      | -         | ns                     |
| Turn-on delay time                                                                   | $t_{d(on)}$             | $f = 1\text{ MHz}$                                                                                                               | 0.2  | 1       | 2         |                        |
| Rise time                                                                            | $t_r$                   | $V_{DD} = 20\text{ V}$ , $R_L = 1\text{ }\Omega$<br>$I_D \cong 10\text{ A}$ , $V_{GEN} = 10\text{ V}$ , $R_g = 1\text{ }\Omega$  | -    | 15      | 30        |                        |
| Turn-off delay time                                                                  | $t_{d(off)}$            |                                                                                                                                  | -    | 27      | 60        |                        |
| Fall time                                                                            | $t_f$                   |                                                                                                                                  | -    | 28      | 60        |                        |
| Turn-on delay time                                                                   | $t_{d(on)}$             |                                                                                                                                  | -    | 10      | 20        |                        |
| Rise time                                                                            | $t_r$                   | $V_{DD} = 20\text{ V}$ , $R_L = 1\text{ }\Omega$<br>$I_D \cong 10\text{ A}$ , $V_{GEN} = 4.5\text{ V}$ , $R_g = 1\text{ }\Omega$ | -    | 28      | 60        |                        |
| Turn-off delay time                                                                  | $t_{d(off)}$            |                                                                                                                                  | -    | 66      | 140       |                        |
| Fall time                                                                            | $t_f$                   |                                                                                                                                  | -    | 34      | 70        |                        |
|                                                                                      |                         |                                                                                                                                  | -    | 22      | 45        |                        |
| <b>Drain-Source Body Diode Characteristics</b>                                       |                         |                                                                                                                                  |      |         |           |                        |
| Continuous source-drain diode current                                                | $I_S$                   | $T_C = 25\text{ }^{\circ}\text{C}$                                                                                               | -    | -       | 60        | A                      |
| Pulse diode forward current ( $t_p$                                                  |                         |                                                                                                                                  |      |         |           |                        |



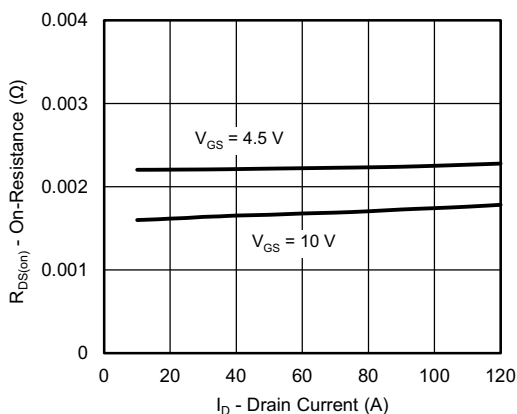
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



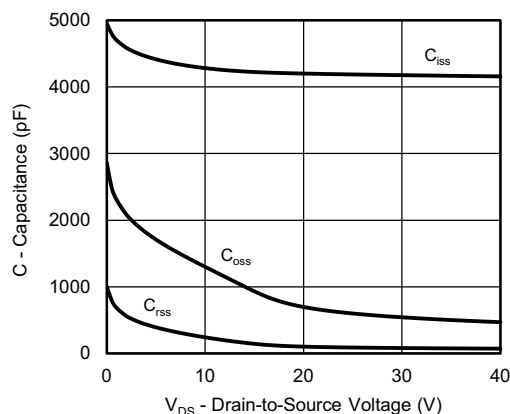
**Output Characteristics**



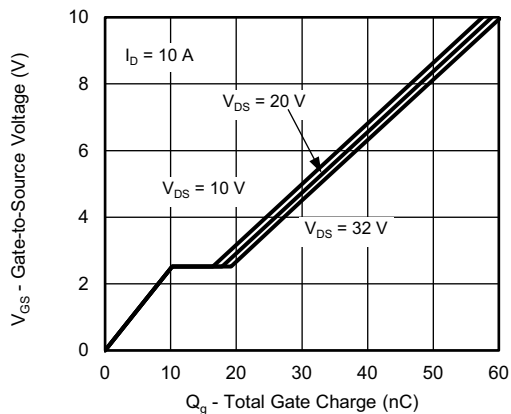
**Transfer Characteristics**



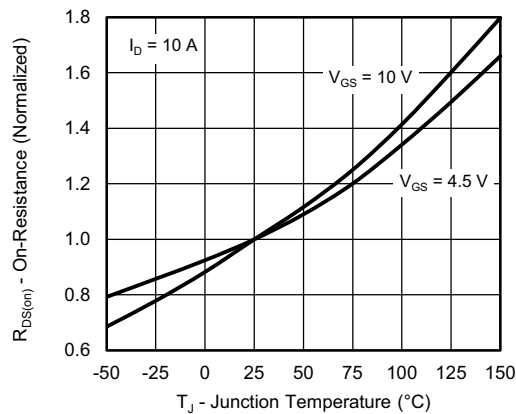
**On-Resistance vs. Drain Current**



**Capacitance**



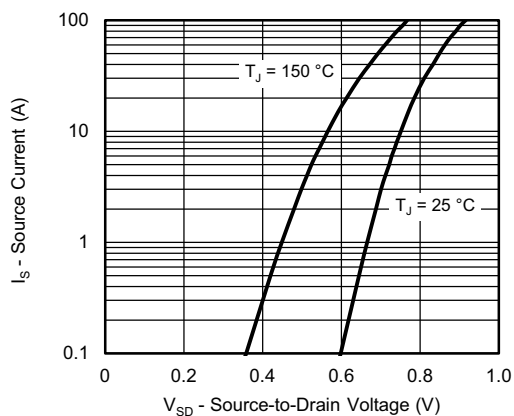
**Gate Charge**



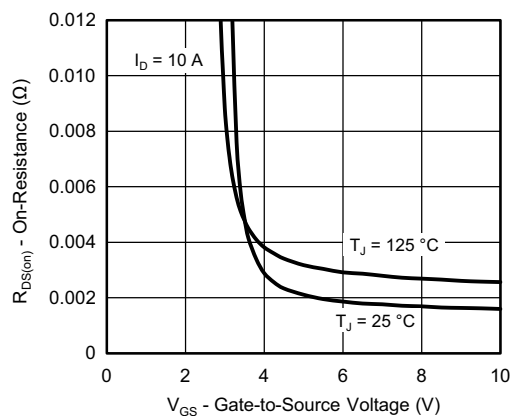
**On-Resistance vs. Junction Temperature**



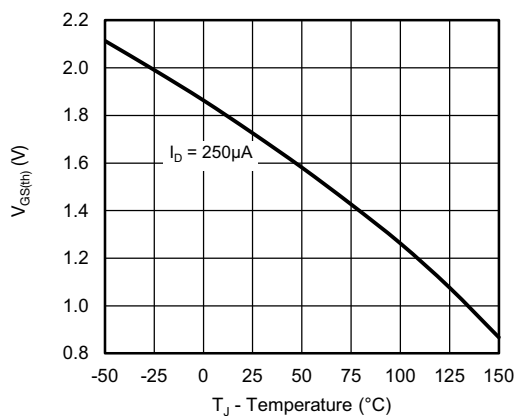
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



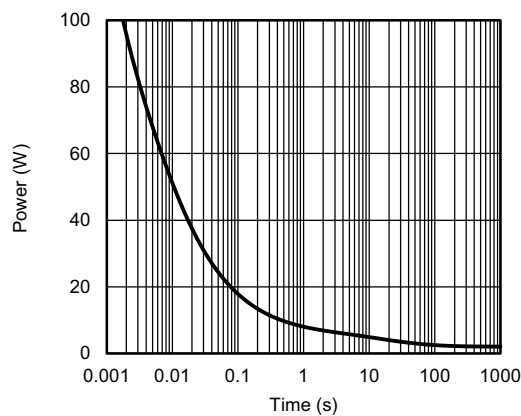
**Source-Drain Diode Forward Voltage**



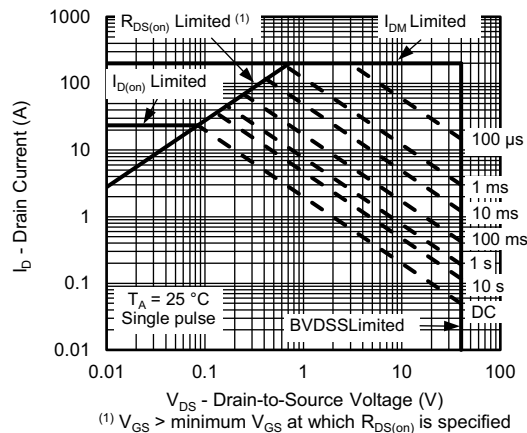
**On-Resistance vs. Gate-to-Source Voltage**



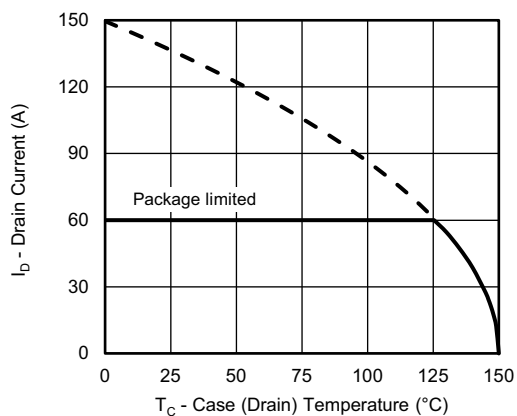
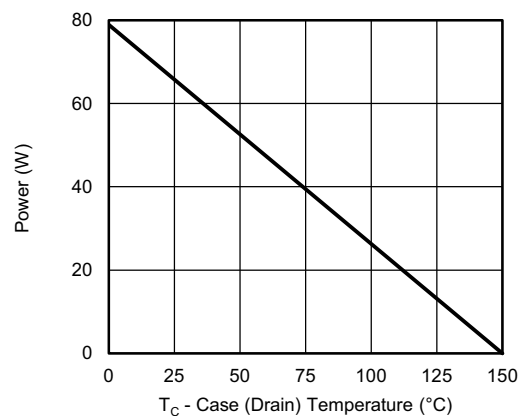
**Threshold Voltage**



**Single Pulse Power, Junction-to-Ambient**



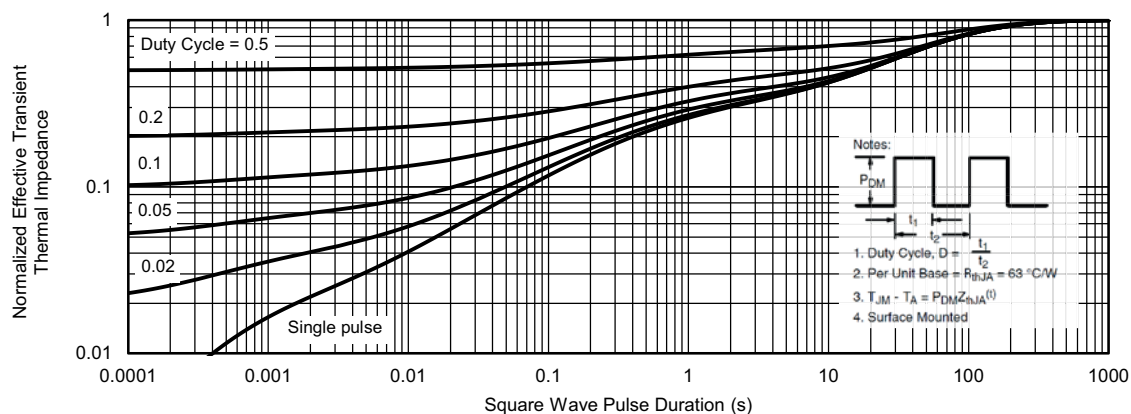
**Safe Operating Area**

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Current Derating <sup>a</sup>**

**Power, Junction-to-Case**
**Note**

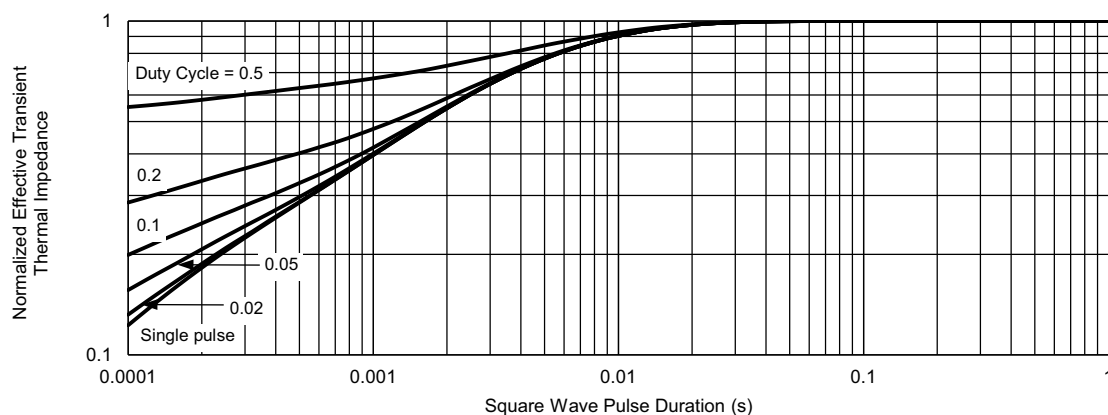
- a. The power dissipation  $P_D$  is based on  $T_J$  max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



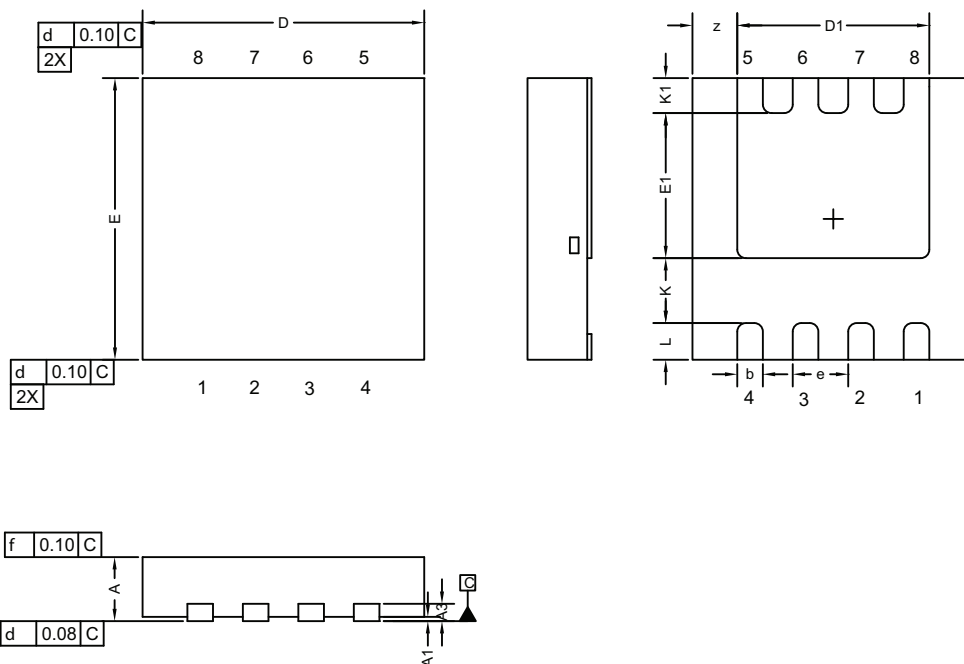
**Normalized Thermal Transient Impedance, Junction-to-Ambient**



**Normalized Thermal Transient Impedance, Junction-to-Case**

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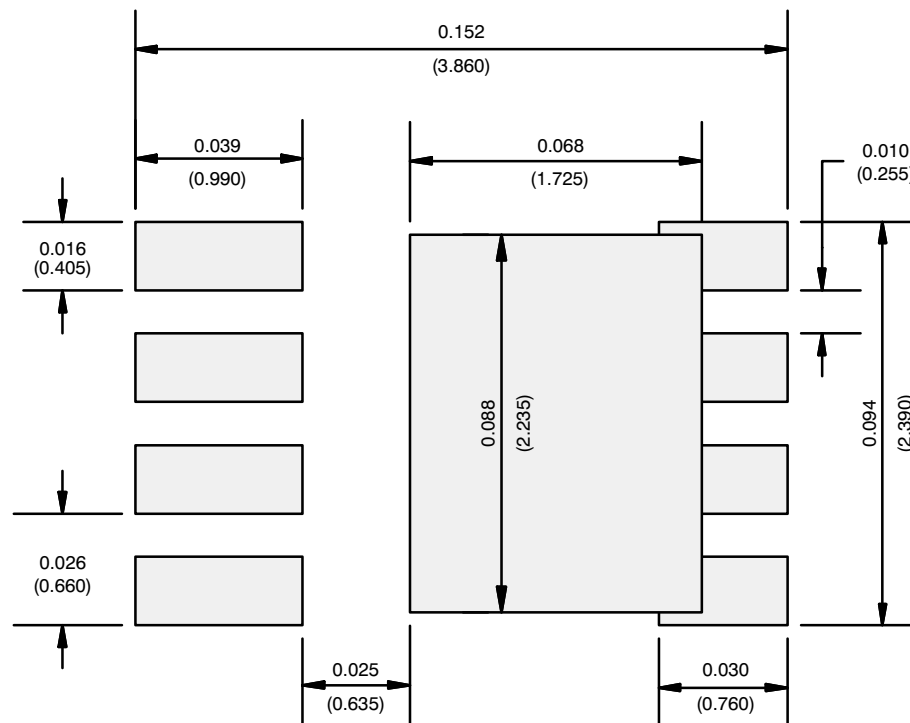
## Case Outline for PowerPAK® 1212-8S



### Note

- Millimeters will govern.

## RECOMMENDED MINIMUM PADS FOR PowerPAK® 1212-8 Single



Recommended Minimum Pads  
Dimensions in Inches/(mm)

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