PRELIMINARY DATA SHEET

SKY73420-11: 650-950 MHz Broadband, Application Configurable High Gain and Linearity Diversity Downconversion Mixer

Applications

- 2G/3G/4G base station transceivers:
  - GSM/EDGE, CDMA, UMTS/WCDMA, LTE
- Land mobile radio
- High performance radio links

Features

- Operating frequency range: 650 to 950 MHz
- IF frequency range: 150 to 400 MHz
- Conversion gain: 8.1 dB
- Input IP3: up to +25.6 dBm
- Noise Figure: 9.3 dB
- Power-down mode
- Integrated LO drivers
- Integrated low loss RF baluns
- High linearity IF amplifiers
- Application tuneability
- Small, QFN (36-pin, 6 x 6 mm) package (MSL3, 260 °C per JEDEC J-STD-020)

Skyworks Green™ products are compliant with all applicable legislation and are halogen-free. For additional information, refer to *Skyworks Definition of Green™*, document number SQ04-0074.

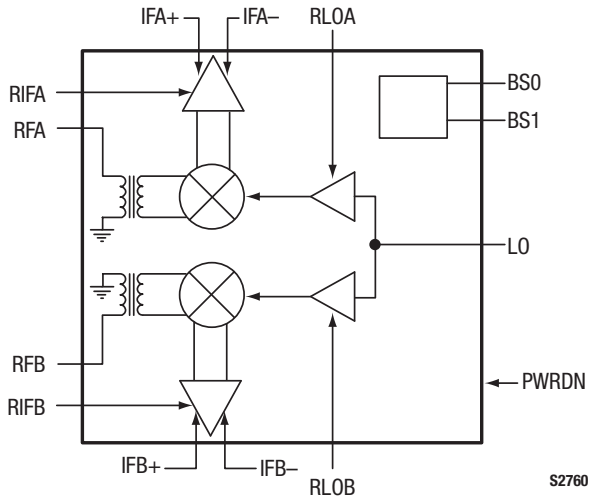


Figure 1. SKY73420-11 Block Diagram

Description

The SKY73420-11 is a fully integrated diversity mixer that includes Local Oscillator (LO) drivers, high linearity mixers, and large dynamic range Intermediate Frequency (IF) amplifiers. Low loss RF baluns have also been included to reduce design complications and lower system cost.

The SKY73420-11 features an IIP3 of up to +25.6 dBm and a Noise Figure (NF) of 9.3 dB, making the device an ideal solution for high dynamic range systems such as 2G/3G/4G base station receivers.

The SKY73420-11 has been designed for optimum long-term reliability. The SKY73420-11 diversity downconversion mixer is provided in a compact, 36-pin Quad Flat No-Lead (QFN) package. A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.

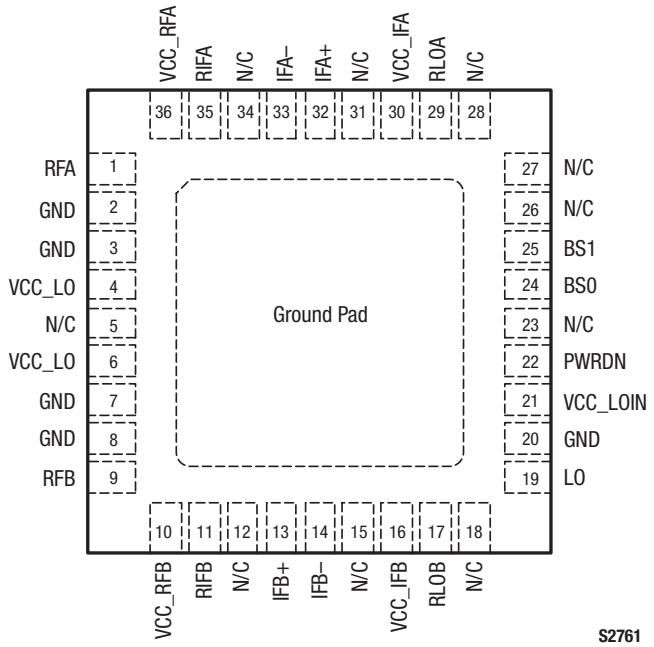


Figure 2. SKY73420-11 Pinout – 36-Pin QFN
(Top View)

Table 1. SKY73420-11 Signal Descriptions

Pin #	Name	Description	Pin #	Name	Description
1	RFA	RF channel A input	19	LO	Local oscillator input
2	GND	Ground	20	GND	Ground
3	GND	Ground	21	VCC_LOIN	DC supply, +5 V
4	VCC_LO	DC supply, +5 V	22	PWRDN	Power-down mode enable
5	N/C	No internal connection	23	N/C	No internal connection
6	VCC_LO	DC supply, +5 V	24	BS0	Band select 0 control
7	GND	Ground	25	BS1	Band select 1 control
8	GND	Ground	26	N/C	No internal connection
9	RFB	RF channel B input	27	N/C	No internal connection
10	VCC_RFB	DC supply, +5 V	28	N/C	No internal connection
11	RIFB	IF channel B bias control	29	RLOA	LO channel A bias control
12	N/C	No internal connection	30	VCC_IFA	DC supply, +5 V
13	IFB+	IF channel B positive output	31	N/C	No internal connection
14	IFB–	IF channel B negative output	32	IFA+	IF channel A positive output
15	N/C	No internal connection	33	IFA–	IF channel A negative output
16	VCC_IFB	DC supply, +5 V	34	N/C	No internal connection
17	RLOB	LO channel B bias control	35	RIFA	IF channel A bias control
18	N/C	No internal connection	36	VCC_RFA	DC supply, +5 V
			–	Ground pad	Bottom ground pad (Note 1)

Note 1: Bottom ground pad serves as a key electrical and thermal ground.

Functional Description

The SKY73420-11 is a high gain diversity mixer, optimized for base station receiver applications. The device consists of two diversity channels, each consisting of a low loss RF balun, high linearity passive mixer, and a low noise IF amplifier.

The SKY73420-11 also includes a power-down feature used to lower the supply current for standby operation (refer to Tables 4 and 5):

PWRDN Logic:	Mixer State:
High	Standby (low power mode)
Low	Full operation

LO amplifiers are also included that allow the SKY73420-11 to connect directly to the output of a Voltage Controlled Oscillator (VCO). This eliminates the extra gain stages needed by most discrete passive mixers.

RF Baluns and Passive Mixer

The RF baluns provide a single ended input, which can easily be matched to 50 Ω using a simple matching circuit. The RF baluns offer very low loss and excellent amplitude and phase balance over a wide frequency range of 650 to 950 MHz.

The high linearity mixer is a passive, double balanced mixer that provides a very low insertion loss, and excellent 3rd Order Input Insertion Point (IIP3) and linearity performance.

Additionally, the balanced nature of the mixer provides for excellent port-to-port isolation.

Bandselect Logic

The SKY73420-11 is designed to optimize performance in four sub-bands. The particular sub-band of interest is selected by setting pin 24 (BS0) and pin 25 (BS1). The bandselect logic is provided in Table 2.

Depending on the LO injection side, and the RF and IF frequencies involved, there could be multiple bandselect solutions. Table 5 identifies recommended bandselects for a number of frequency ranges.

LO Buffers

The LO buffers allow the input power of the SKY73420-11 to be driven in the range of –6 to +6 dBm. The LO section has been optimized for 800 to 1350 MHz. However, the LO can be driven over a wide frequency range with only slight degradation in performance.

Pins 17 (RLOB) and 29 (RLOA) allow for external biasing of the LO driver bias currents to trade off linearity for the core passive mixer current.

Power-Down Mode

The SKY73420-11 also includes a low current power-down mode controlled by pin 22 (PWRDN). When this pin is at a logic high level, the power-down function is enabled with the total mixer current under 45 mA.

IF Amplifier

The SKY73420-11 includes high dynamic range IF amplifiers that follow the passive mixers in the signal path. The outputs require a supply voltage connection using inductive chokes. These choke inductors should be high-Q and have the ability to handle 200 mA or greater.

A simple matching network allows the output ports to be matched to a balanced 200 Ω impedance. The IF amplifiers are optimized for IF frequencies between 100 and 400 MHz. The IF amplifiers can be operated outside of this range, but with a slight degradation in performance.

Pins 11 (RIFB) and 35 (RIFA) allow for external biasing of the IF amplifier bias currents to trade off linearity for the IF amplifier current.

Electrical and Mechanical Specifications

The absolute maximum ratings of the SKY73420-11 are provided in Table 3. The recommended operating conditions are specified in Table 4 and electrical specifications are provided in Tables 5 and 6.

Table 2. SKY73420-11 Bandselect Logic

BS1 (Pin 25)	BS0 (Pin 24)	LO Center Frequency (MHz)
0	0	*** TBD ***
0	1	*** TBD ***
1	0	*** TBD ***
1	1	*** TBD ***

Table 3. SKY73420-11 Absolute Maximum Ratings

Parameter	Symbol	Minimum	Maximum	Units
Supply voltage, +5 V (VCC_LO, VCC_RFA, VCC_RFB, VCC_IFA, VCC_IFB, VCC_LOIN)	V_{CC}	4.5	5.5	V
Total supply current	I_{CC}		360	mA
RF input power	P_{RF}		+20	dBm
LO input power	P_{LO}		+20	dBm
Operating case temperature	T_C	–40	+100	°C
Junction temperature	T_J		+125	°C
Storage case temperature	T_{STG}	–40	+150	°C
Thermal resistance	Θ_{JC}		10.4	°C/W

Notes: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

***CAUTION**: Although this device is designed to be as robust as possible, Electrostatic Discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.*

Table 4. SKY73420-11 Recommended Operating Conditions

Parameter	Symbol	Minimum	Typical	Maximum	Units
RF frequency range	f_{RF}	650		950	MHz
LO frequency range	f_{LO}	800		1350	MHz
IF frequency range	f_{IF}	100		400	MHz
Supply voltage, +5 V	V_{CC}	4.75	5.00	5.25	V
Supply current (Note 1), PWRDN = 0 V	I_{CC}		310		mA
LO input power	P_{LO}	–6	0	+6	dBm
Logic levels (Note 2): high low	 V_{IN_H} V_{IN_L}	 1.09 0		 V_{CC} 0.74	 V V
Logic input current	I_L	***** TBD *****	0	***** TBD *****	mA
Operating case temperature (Note 3)	T_C	–40		+100	°C

Note 1: See Table 7 for external biasing and matching components.

Note 2: Logic signals are: PWRDN, BS0, and BS1. Valid logic levels must be applied. There are no internal pull-ups or pull-downs.

Note 3: Case temperature measured at bottom of package where device is soldered to board.

Table 5. SKY73420-11 Electrical Specifications: General (1 of 5) (Note 1)
(V_{CC} = 4.75 to 5.25 V, T_C = –40 to +100 °C, P_{LO} = –3 to +3 dBm, PWRDN = Logic "0," Unless Otherwise Noted)

Parameter	Symbol	Test Condition				Min	Typical	Max	Units
		RF Freq Range (MHz)	IF Freq (MHz)	BS1	BS0				
Conversion gain	G	824 to 849	200	0	1		8.1		dB
		824 to 849	350	0	1		8.0		dB
		830 to 862	200	1	1				dB
		830 to 862	350	1	1				dB
		880 to 915	200	0	1				dB
		880 to 915	350	0	1				dB
		698 to 716	200	1	1				dB
		698 to 716	350	1	1				dB
		777 to 798	200	1	1				dB
		777 to 798	350	1	1				dB
Noise Figure	NF	824 to 849	200	0	1		9.3	11.0	dB
		824 to 849	350	0	1		9.3	11.0	dB
		830 to 862	200	1	1				dB
		830 to 862	350	1	1				dB
		880 to 915	200	0	1				dB
		880 to 915	350	0	1				dB
		698 to 716	200	1	1				dB
		698 to 716	350	1	1				dB
		777 to 798	200	1	1				dB
		777 to 798	350	1	1				dB
3rd Order Input Intercept Point	IIP3	824 to 849	200	0	1	+23.5	+25.6		dBm
		824 to 849	350	0	1	+23.5	+25.6		dBm
		830 to 862	200	1	1	+23.5			dBm
		830 to 862	350	1	1	+23.5			dBm
		880 to 915	200	0	1	+23.5			dBm
		880 to 915	350	0	1	+23.5			dBm
		698 to 716	200	1	1	+23.5			dBm
		698 to 716	350	1	1	+23.5			dBm
		777 to 798	200	1	1	+23.5			dBm
		777 to 798	350	1	1	+23.5			dBm

Table 5. SKY73420-11 Electrical Specifications: General (2 of 5) (Note 1)
(V_{CC} = 4.75 to 5.25 V, T_C = –40 to +100 °C, P_{LO} = –3 to +3 dBm, PWRDN = Logic "0," Unless Otherwise Noted)

Parameter	Symbol	Test Condition				Min	Typical	Max	Units
		RF Freq Range (MHz)	IF Freq (MHz)	BS1	BS0				
Operating current (on)	I_{CC_ON}	824 to 849	200	0	1		317		mA
		824 to 849	350	0	1		329		mA
		830 to 862	200	1	1				mA
		830 to 862	350	1	1				mA
		880 to 915	200	0	1				mA
		880 to 915	350	0	1				mA
		698 to 716	200	1	1				mA
		698 to 716	350	1	1				mA
		777 to 798	200	1	1				mA
		777 to 798	350	1	1				mA
Operating current (off) (Note 2)	I_{CC_OFF}	824 to 849	200	0	1		34		mA
		824 to 849	350	0	1		34		mA
		830 to 862	200	1	1				mA
		830 to 862	350	1	1				mA
		880 to 915	200	0	1				mA
		880 to 915	350	0	1				mA
		698 to 716	200	1	1				mA
		698 to 716	350	1	1				mA
		777 to 798	200	1	1				mA
		777 to 798	350	1	1				mA
Input 1 dB compression point	IP1dB	824 to 849	200	0	1	+10.0	+12.9		dBm
		824 to 849	350	0	1	+10.0	+13.7		dBm
		830 to 862	200	1	1	+10.0			dBm
		830 to 862	350	1	1	+10.0			dBm
		880 to 915	200	0	1	+10.0			dBm
		880 to 915	350	0	1	+10.0			dBm
		698 to 716	200	1	1	+10.0			dBm
		698 to 716	350	1	1	+10.0			dBm
		777 to 798	200	1	1	+10.0			dBm
		777 to 798	350	1	1	+10.0			dBm

Table 5. SKY73420-11 Electrical Specifications: General (3 of 5) (Note 1)
(V_{CC} = 4.75 to 5.25 V, T_C = –40 to +100 °C, P_{LO} = –3 to +3 dBm, PWRDN = Logic "0," Unless Otherwise Noted)

Parameter	Symbol	Test Condition				Min	Typical	Max	Units
		RF Freq Range (MHz)	IF Freq (MHz)	BS1	BS0				
1xLO to RF leakage (measured @ LO)	1xLO-RF	824 to 849	200	0	1		–48	–25	dBm
		824 to 849	350	0	1		–48	–25	dBm
		830 to 862	200	1	1			–25	dBm
		830 to 862	350	1	1			–25	dBm
		880 to 915	200	0	1			–25	dBm
		880 to 915	350	0	1			–25	dBm
		698 to 716	200	1	1			–25	dBm
		698 to 716	350	1	1			–25	dBm
		777 to 798	200	1	1			–25	dBm
		777 to 798	350	1	1			–25	dBm
2xLO to RF leakage (measured @ 2xLO)	2xLO-RF	824 to 849	200	0	1		–48	–25	dBm
		824 to 849	350	0	1		–41	–25	dBm
		830 to 862	200	1	1			–25	dBm
		830 to 862	350	1	1			–25	dBm
		880 to 915	200	0	1			–25	dBm
		880 to 915	350	0	1			–25	dBm
		698 to 716	200	1	1			–25	dBm
		698 to 716	350	1	1			–25	dBm
		777 to 798	200	1	1			–25	dBm
		777 to 798	350	1	1			–25	dBm
3xLO to RF leakage (measured @ 3xLO)	3xLO-RF	824 to 849	200	0	1		–65	–28	dBm
		824 to 849	350	0	1		–65	–28	dBm
		830 to 862	200	1	1			–28	dBm
		830 to 862	350	1	1			–28	dBm
		880 to 915	200	0	1			–28	dBm
		880 to 915	350	0	1			–28	dBm
		698 to 716	200	1	1			–28	dBm
		698 to 716	350	1	1			–28	dBm
		777 to 798	200	1	1			–28	dBm
		777 to 798	350	1	1			–28	dBm

Table 5. SKY73420-11 Electrical Specifications: General (4 of 5) (Note 1)
(V_{CC} = 4.75 to 5.25 V, T_C = –40 to +100 °C, P_{LO} = –3 to +3 dBm, PWRDN = Logic "0," Unless Otherwise Noted)

Parameter	Symbol	Test Condition				Min	Typical	Max	Units
		RF Freq Range (MHz)	IF Freq (MHz)	BS1	BS0				
4xLO to RF leakage (measured @ 4xLO)	4xLO-RF	824 to 849	200	0	1		–21		dBm
		824 to 849	350	0	1		–21		dBm
		830 to 862	200	1	1				dBm
		830 to 862	350	1	1				dBm
		880 to 915	200	0	1				dBm
		880 to 915	350	0	1				dBm
		698 to 716	200	1	1				dBm
		698 to 716	350	1	1				dBm
		777 to 798	200	1	1				dBm
		777 to 798	350	1	1				dBm
2RF – 2LO (Note 3)	2x2	824 to 849	200	0	1		–82	–63	dBc
		824 to 849	350	0	1		–78	–63	dBc
		830 to 862	200	1	1				dBc
		830 to 862	350	1	1				dBc
		880 to 915	200	0	1				dBc
		880 to 915	350	0	1				dBc
		698 to 716	200	1	1				dBc
		698 to 716	350	1	1				dBc
		777 to 798	200	1	1				dBc
		777 to 798	350	1	1				dBc
3RF – 3LO (Note 3)	3x3	824 to 849	200	0	1		–86	–70	dBc
		824 to 849	350	0	1		–85	–70	dBc
		830 to 862	200	1	1				dBc
		830 to 862	350	1	1				dBc
		880 to 915	200	0	1				dBc
		880 to 915	350	0	1				dBc
		698 to 716	200	1	1				dBc
		698 to 716	350	1	1				dBc
		777 to 798	200	1	1				dBc
		777 to 798	350	1	1				dBc

Table 5. SKY73420-11 Electrical Specifications: General (5 of 5) (Note 1)
(V_{CC} = 4.75 to 5.25 V, T_C = –40 to +100 °C, P_{LO} = –3 to +3 dBm, PWRDN = Logic "0," Unless Otherwise Noted)

Parameter	Symbol	Test Condition				Min	Typical	Max	Units
		RF Freq Range (MHz)	IF Freq (MHz)	BS1	BS0				
LO to IF (leakage measured @ LO)	LO-IF	824 to 849	200	0	1		–58	–23	dBm
		824 to 849	350	0	1		–53	–23	dBm
		830 to 862	200	1	1			–23	dBm
		830 to 862	350	1	1			–23	dBm
		880 to 915	200	0	1			–23	dBm
		880 to 915	350	0	1			–23	dBm
		698 to 716	200	1	1			–23	dBm
		698 to 716	350	1	1			–23	dBm
		777 to 798	200	1	1			–23	dBm
		777 to 798	350	1	1			–23	dBm
RF to IF (isolation measured @ RF)	RF-IF	824 to 849	200	0	1		44	30	dB
		824 to 849	350	0	1		45	30	dB
		830 to 862	200	1	1			30	dB
		830 to 862	350	1	1			30	dB
		880 to 915	200	0	1			30	dB
		880 to 915	350	0	1			30	dB
		698 to 716	200	1	1			30	dB
		698 to 716	350	1	1			30	dB
		777 to 798	200	1	1			30	
		777 to 798	350	1	1			30	
Channel to channel (isolation measured @ IF)	Ch-Ch	824 to 849	200	0	1		70	40	dB
		824 to 849	350	0	1		72	40	dB
		830 to 862	200	1	1			40	dB
		830 to 862	350	1	1			40	dB
		880 to 915	200	0	1			40	dB
		880 to 915	350	0	1			40	dB
		698 to 716	200	1	1			40	dB
		698 to 716	350	1	1			40	dB
		777 to 798	200	1	1			40	
		777 to 798	350	1	1			40	

Note 1: Performance is guaranteed only under the conditions listed in this Table. Production tested: RF frequency = ***** TBD ***** MHz, LO frequency= ***** TBD ***** MHz. All others guaranteed by design and characterization. Testing conducted with balun on IF output (see Figure 5). Table 1 performance is of the DUT, with balun loss de-embedded.
Input matching can be unique for each bandselect setting.
Conditions for typical values: V_{CC} = 5 V, T_C = +35 C, P_{LO} = 0 dBm, middle of RF range.
See Table 7 for external biasing and matching components.

Note 2: PWRDN pin = logic "1."

Note 3: Interferer tone is –10 dBm.

Table 6. SKY73420-11 Electrical Specifications (Note 1)
(V_{CC} = 4.75 to 5.25 V, T_C = –40 to +100 °C, P_{LO} = –3 to +3 dBm, PWRDN = Logic "0," BS0/BS1 = Logic "1," P_{RF} = –10 dBm, f_{IF} = 200 MHz, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
Noise Figure with a blocker signal (Note 2)	NF_{BLK}	Blocking signal input power = +8 dBm, 2.5 MHz offset		*** TBD ***		dB
Power-up time (Note 2) (Note 3)	t_{ON}			*** TBD ***	1	µs
Power-down time (Note 2) (Note 4)	t_{OFF}			*** TBD ***	1	µs
RF port input return loss (Note 2)	Z_{IN_RF}	With external matching components	14			dB
LO port input return loss (Note 2)	Z_{IN_LO}	With external matching components	14			dB
IF port input return loss (Note 2)	Z_{OUT_IF}	With external matching components	14			dB

Note 1: Performance is guaranteed only under the conditions listed in this Table. Production tested: RF frequency = *** **TBD** *** MHz, LO frequency= *** **TBD** *** MHz. All others guaranteed by design and characterization. Testing conducted with balun on IF output (see Figure 5). Table 1 performance is of the DUT, with balun loss de-embedded.
Input matching can be unique for each bandselect setting.
Conditions for typical values: V_{CC} = 5 V, T_C = +35 C, P_{LO} = 0 dBm, middle of RF range.
See Table 7 for external biasing and matching components.

Note 2: Not production tested. Guaranteed by design and characterization.

Note 3: Time required for IF envelope to reach 97.5% of final value following a PWRDN transition to logic "0" with t_{FALL} < 1 ns.

Note 4: Time required for total supply current to be less than 53 mA following a PWRDN transition to logic "1" with t_{RISE} < 1 ns.

Evaluation Board Description

The SKY73420-11 Evaluation Board is used to test the performance of the SKY73420-11 downconversion mixer. An assembly drawing for the Evaluation Board is shown in Figure 3 and the layer detail is provided in Figure 4. A schematic diagram of the SKY73420-11 Evaluation Board is shown in Figure 5.

RF and IF matching components are listed in Table 7.

Circuit Design Considerations

The following design considerations are general in nature and must be followed regardless of final use or configuration:

1. Paths to ground should be made as short as possible.
2. The ground pad of the SKY73420-11 has special electrical and thermal grounding requirements. This pad is the main thermal conduit for heat dissipation. Since the circuit board acts as the heat sink, it must shunt as much heat as possible from the device. Therefore, design the connection to the ground pad to dissipate the maximum wattage produced by the circuit board.
3. Skyworks recommends including external bypass capacitors on the VCC voltage inputs of the device.
4. Components L12, L13, L14, and L15 (see Figure 5) are high-Q low loss inductors. These inductors must be able to pass currents in excess of 200 mA DC.
5. Components R8, R9, R31, and R35 (see Figure 5) allow for external adjustment of the IF amplifier bias currents. Skyworks recommends that these resistors have a tolerance of ±1% to optimize performance consistency of the SKY73420-11. These resistors are optional for device operation, but the performance in Tables 5 and 6 is specified for the values of the R_{IF} resistors (R31 and R35) as indicated in the Tables.
6. Components R29, R33, R34, and R37 (see Figure 5) allow for external adjustment of the LO driver bias currents. Skyworks recommends that these resistors have a tolerance of ±1 percent to optimize performance consistency of the SKY73420-11. These resistors are optional for device operation, but the performance in Tables 5 and 6 specified for the values of the R_{LO} resistors (R29 and R37) as indicated in the Tables.
7. It is recommended to apply solder paste with stencil, as noted in Figure 6.

Package Dimensions

The PCB layout footprint for the SKY73420-11 is provided in Figure 6. Figure 7 shows the package dimensions for the 36-pin QFN and Figure 8 provides the tape and reel dimensions.

Package and Handling Information

Since the device package is sensitive to moisture absorption, it is baked and vacuum packed before shipping. Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SKY73420-11 is rated to Moisture Sensitivity Level 3 (MSL3) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *PCB Design & SMT Assembly/Rework Guidelines for MCM-L Packages*, document number 101752.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

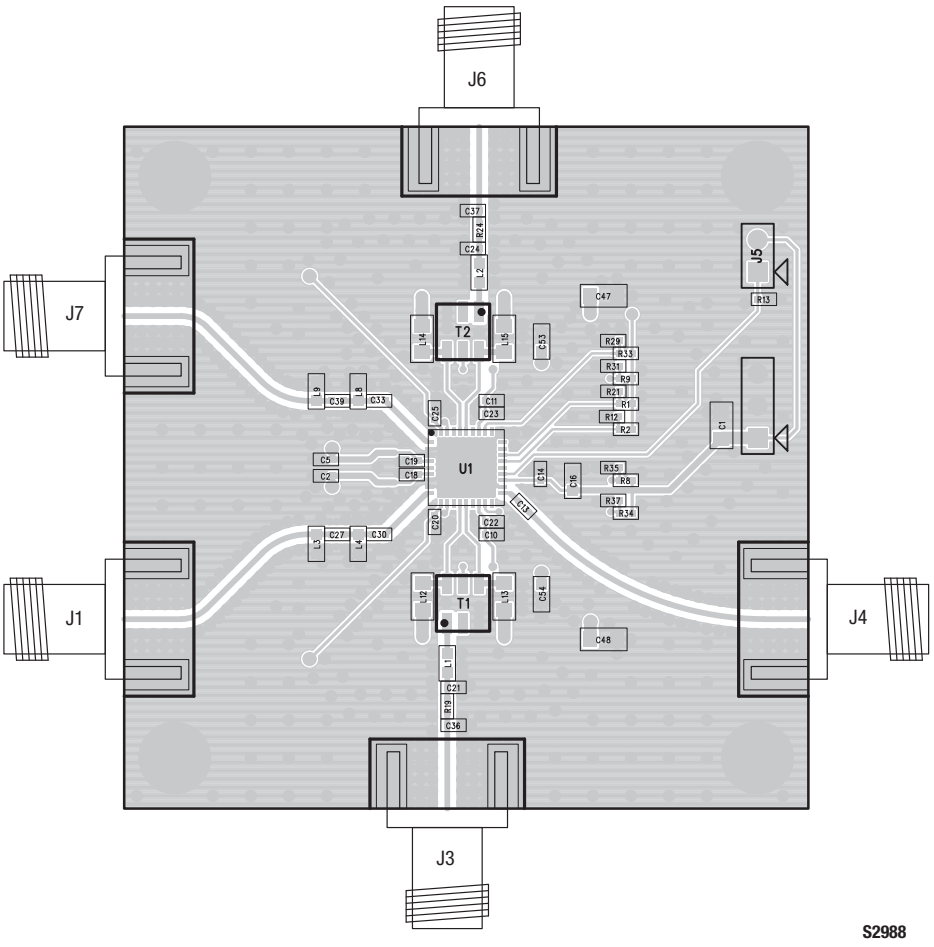
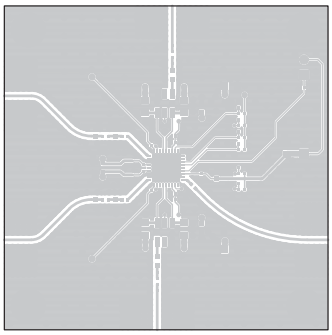
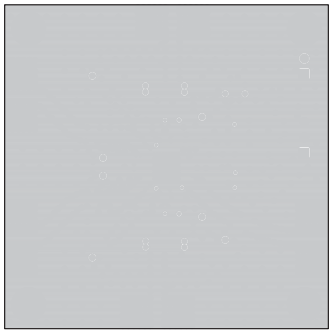


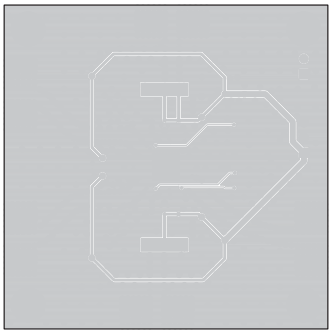
Figure 3. SKY73420-11 Evaluation Board Assembly Diagram



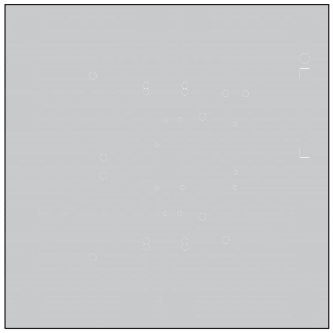
Top Layer



Ground Plane



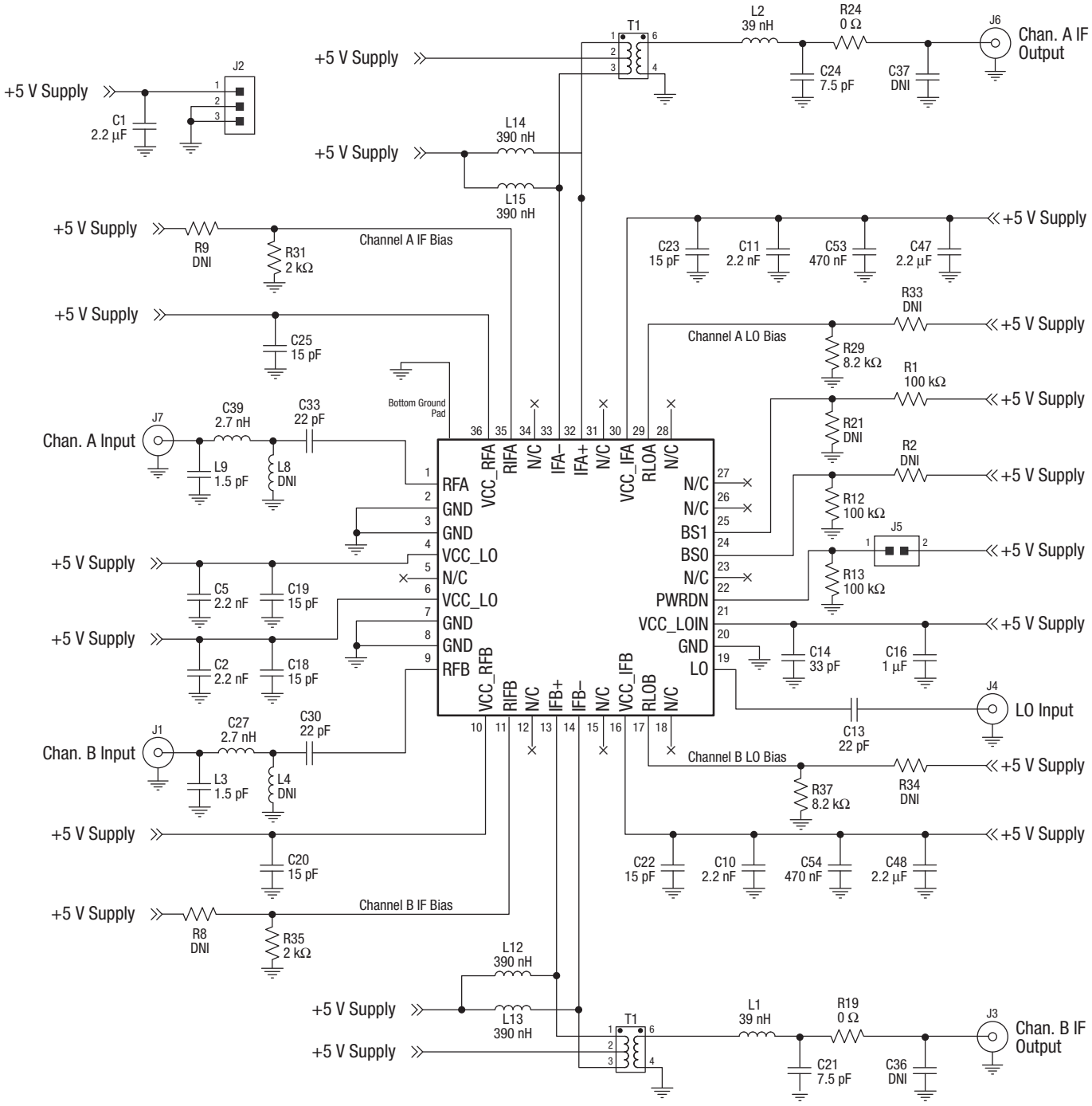
Power Plane



Bottom Layer

S2989

Figure 4. SKY73420-11 Evaluation Board Layer Detail



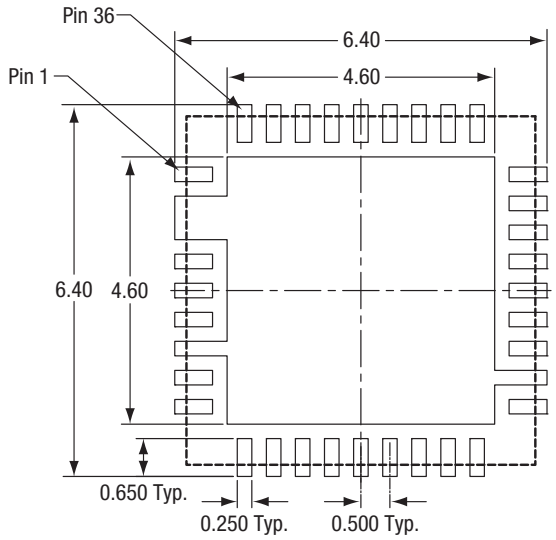
Notes: Some component labels may be different than the corresponding component symbol shown here. Component values, however, are accurate as of the date of this Data Sheet.

Y0067

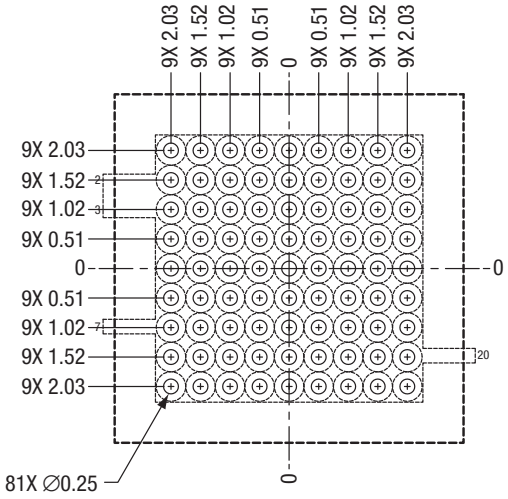
Figure 5. SKY73420-11 Evaluation Board Schematic

Table 7. Evaluation Board Schematic Band-Dependent Components

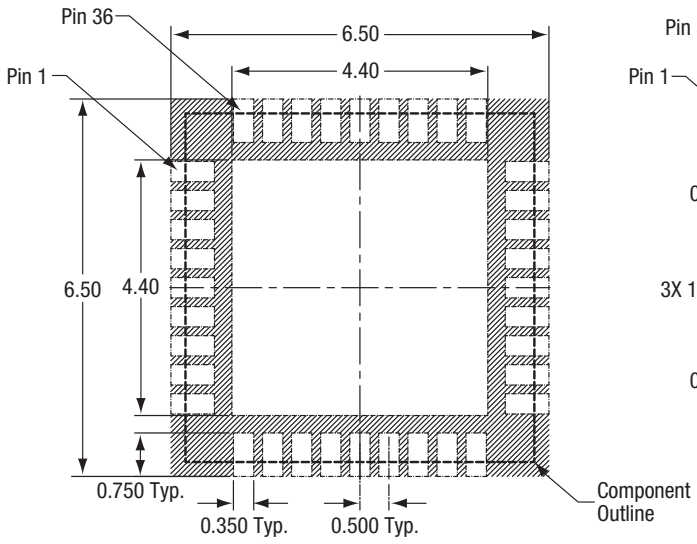
RF Frequency Range (MHz)	3GPP Band	f_{IF} (MHz)	Bandselects				RF Matching Comp's	IF Matching Comp's		IF Bias	LO Bias
			BS1	BS0	R1 (Ω)	R21 (kΩ)	C33, C30 (pF)	L2, L1 (nH)	C24, C21 (pF)	R31, R35 (kΩ)	R29, R37 (kΩ)
824 to 849	5	200	1	1							
824 to 849	5	350	1	1							
830 to 862	6, 19, 20	200	1	1							
830 to 862	6, 19, 20	350	1	1							
880 to 915	8	200	0	1							
880 to 915	8	350	0	1							
698 to 716	12, 17	200	1	1							
698 to 716	12, 17	350	1	1							
777 to 798	13, 14	200	1	1							
777 to 798	13,14	350	1	1							



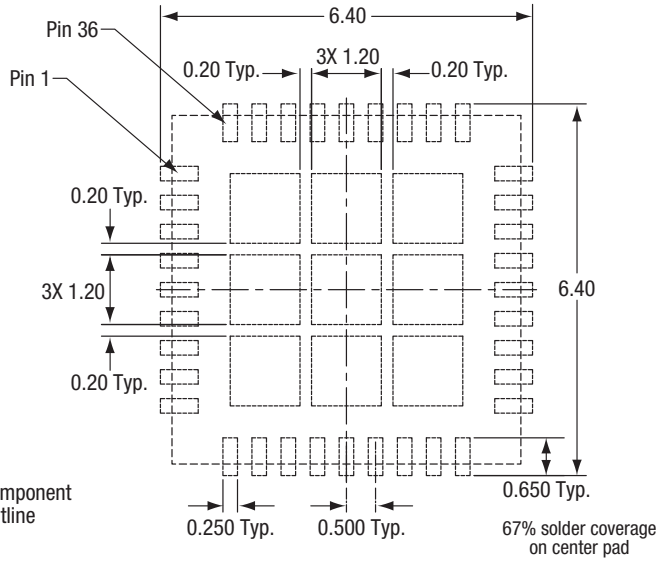
Board Metal



Via Pattern
(Note 1)



Solder Mask Pattern
(Note 2)



Stencil Pattern
(Note 3)

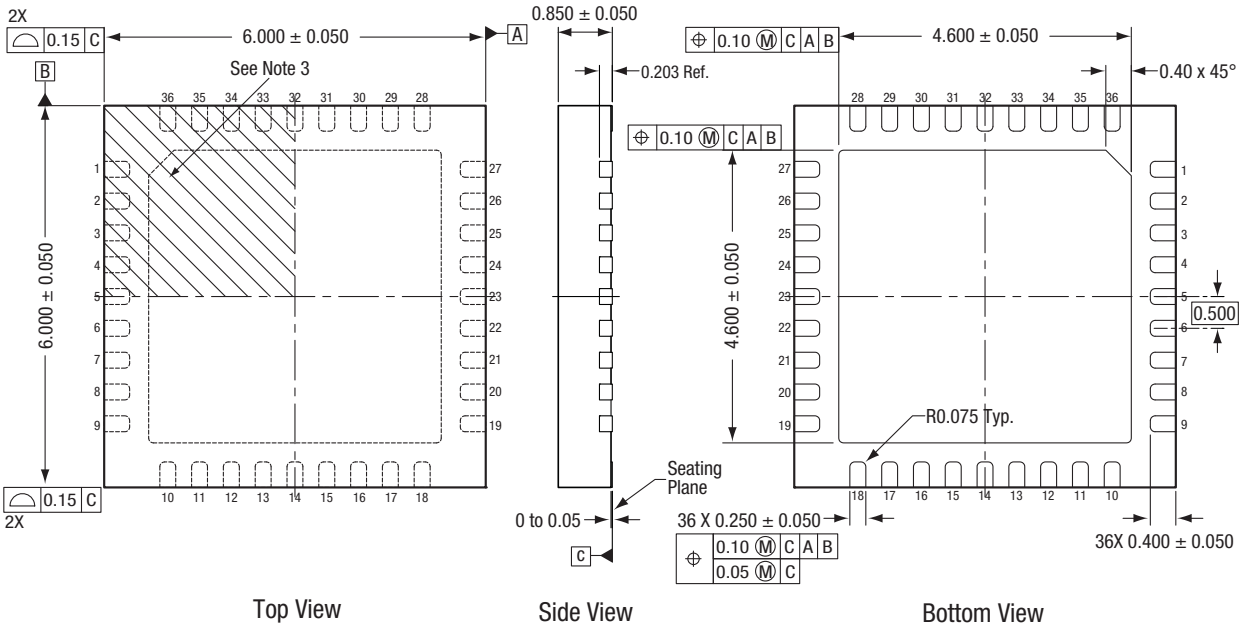
NOTES:

1. *Via hole recommendations:*
   0.025 mm Cu via wall plating (minimum), soldermask on the far side should tent or plug via holes.
2. *Soldermask recommendations:*
   Contact board fabricator for recommended soldermask offset and tolerance.
3. *Stencil recommendations:*
   0.125 mm stencil thickness, laser cut apertures, trapezoidal walls and rounded corners offer better paste release.

Dimension and tolerancing according to ASME Y14.5M-1994.
Unless specified, dimensions are symmetrical about center lines.
All dimensions are in millimeters.

S3158

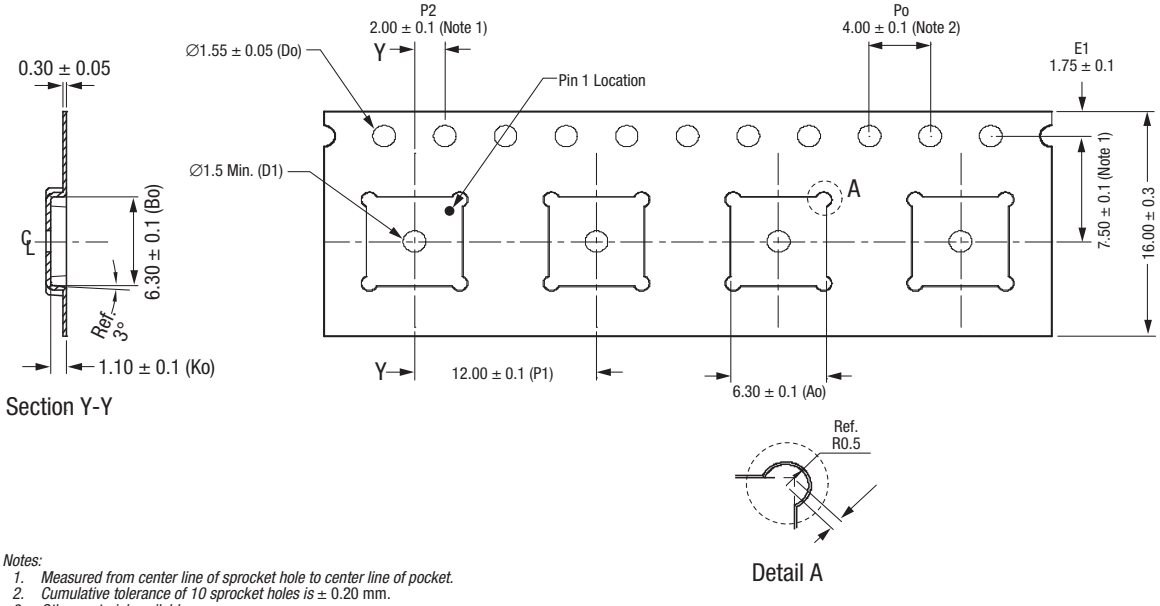
Figure 6. PCB Layout Footprint for the SKY73420-11 6 x 6 mm QFN



NOTES:
1. *All measurements are in millimeters.*
2. *Dimensioning and tolerancing according to ASME Y14.5M-1994.*
   Unless otherwise specified the following values apply:
   Decimal Tolerance: *Angular Tolerance:*
   X.X (1 place) ± 0.1 mm ±1°
   X.XX (2 places) ± 0.05 mm
   X.XXX (3 places) ± 0.025 mm
3. *Terminal #1 identification mark located within marked area.*
4. *Unless specified, dimensions are symmetrical about center lines.*

S2944

Figure 7. SKY73420-11 36-Pin QFN Package Dimensions



Notes:
1. *Measured from center line of sprocket hole to center line of pocket.*
2. *Cumulative tolerance of 10 sprocket holes is* ± 0.20 mm.
3. *Other material available.*

All measurements are in millimeters.

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Figure 8. SKY73420-11 Tape and Reel Dimensions

Ordering Information

Model Name	Manufacturing Part Number	Evaluation Board Part Number
SKY73420-11 650-950 MHz Downconversion Mixer	SKY73420-11	***** TBD *****

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- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
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- Поставка специализированных компонентов военного и аэрокосмического уровня качества (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Actel, Aeroflex, Peregrine, VPT, Syfer, Eurofarad, Texas Instruments, MS Kennedy, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

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