

LQA40T200C, LQA40B200C

Qspeed™ Family

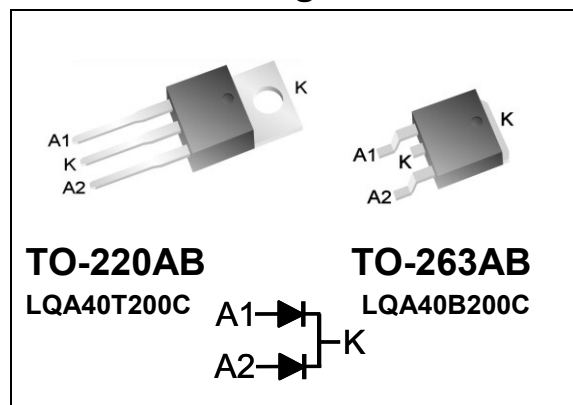


200 V, 40 A Common-Cathode Diode

Product Summary

| | | |
|------------------------------------|------|----|
| $I_{F(AVG)}$ per diode | 20 | A |
| V_{RRM} | 200 | V |
| Q_{RR} (Typ at 125 °C) | 61 | nC |
| I_{RRM} (Typ at 125 °C) | 3.7 | A |
| Softness t_b/t_a (Typ at 125 °C) | 0.32 | |

Pin Assignment



RoHS Compliant

Package uses Lead-free plating and "Green" mold compound Halogen free per IEC 61249-2-21.

Absolute Maximum Ratings

Absolute maximum ratings are the values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

| Symbol | Parameter | Conditions | Rating | Units |
|--------------|--------------------------------------|---|------------|-------|
| V_{RRM} | Peak repetitive reverse voltage | $T_J = 25\text{ °C}$ | 200 | V |
| $I_{F(AVG)}$ | Average forward current | Per Diode, $T_J = 150\text{ °C}$, $T_C = 116\text{ °C}$ | 20 | A |
| | | Per Device, $T_J = 150\text{ °C}$, $T_C = 116\text{ °C}$ | 40 | A |
| I_{FSM} | Non-repetitive peak surge current | Per Diode, 60 Hz, 1/2 cycle | 170 | A |
| I_{FSM} | Non-repetitive peak surge current | Per Diode, 1/2 cycle of $t = 28\text{ }\mu\text{s}$ Sinusoid, $T_C = 25\text{ °C}$ | 350 | A |
| T_J | Operating junction temperature range | | -55 to 150 | °C |
| T_{STG} | Storage temperature | | -55 to 150 | °C |
| | Lead soldering temperature | Leads at 1.6mm from case, 10 sec | 300 | °C |
| P_D | Power dissipation | $T_C = 25\text{ °C}$ | 65.8 | W |

Thermal Resistance

| Symbol | Resistance from: | Conditions | Rating | Units |
|-----------------|---------------------|-----------------|--------|-------|
| $R_{\theta JA}$ | Junction to ambient | TO-220AB (only) | 62 | °C/W |
| $R_{\theta JC}$ | Junction to case | Per Diode | 1.9 | °C/W |
| | | Per Device | 1.0 | °C/W |

General Description

This device has the lowest Q_{RR} of any 200 V Silicon diode. Its recovery characteristics increase efficiency, reduce EMI and eliminate snubbers.

Applications

- AC/DC and DC/DC output rectification
 - Output and freewheeling diodes
- Motor drive circuits
- DC-AC inverters

Features

- Low Q_{RR} , Low I_{RRM} , Low t_{RR}
- High di_F/dt capable (1000A/ μs)
- Soft recovery

Benefits

- Increases efficiency
 - Eliminates need for snubber circuits
 - Reduces EMI filter component size and count
- Enables extremely fast switching

Electrical Specifications at $T_J = 25\text{ }^\circ\text{C}$ (unless otherwise specified)

| Symbol | Parameter | Conditions | Min | Typ | Max | Units | |
|--|---|--|-----------------------------------|-------|------|---------------|----|
| DC Characteristics per diode | | | | | | | |
| I_R | Reverse current per diode | $V_R = 200\text{ V}, T_J = 25\text{ }^\circ\text{C}$ | - | - | 500 | μA | |
| | | $V_R = 200\text{ V}, T_J = 125\text{ }^\circ\text{C}$ | - | 1.0 | - | mA | |
| V_F | Forward voltage per diode | $I_F = 20\text{ A}, T_J = 25\text{ }^\circ\text{C}$ | - | 1.0 | 1.2 | V | |
| | | $I_F = 20\text{ A}, T_J = 150\text{ }^\circ\text{C}$ | - | 0.875 | - | V | |
| C_J | Junction capacitance per diode | $V_R = 10\text{ V}, 1\text{ MHz}$ | - | 74 | - | pF | |
| Dynamic Characteristics per diode | | | | | | | |
| t_{RR} | Reverse recovery time, per diode | $di_F/dt = 200\text{ A}/\mu\text{s}$ $V_R = 130\text{ V},$ $I_F = 20\text{ A}$ | $T_J = 25\text{ }^\circ\text{C}$ | - | 17 | - | ns |
| | | | $T_J = 125\text{ }^\circ\text{C}$ | - | 27 | - | ns |
| Q_{RR} | Reverse recovery charge, per diode | $di_F/dt = 200\text{ A}/\mu\text{s}$ $V_R = 130\text{ V},$ $I_F = 20\text{ A}$ | $T_J = 25\text{ }^\circ\text{C}$ | - | 22 | 34 | nC |
| | | | $T_J = 125\text{ }^\circ\text{C}$ | - | 61 | - | nC |
| I_{RRM} | Maximum reverse recovery current, per diode | $di_F/dt = 200\text{ A}/\mu\text{s}$ $V_R = 130\text{ V},$ $I_F = 20\text{ A}$ | $T_J = 25\text{ }^\circ\text{C}$ | - | 2.14 | 3.15 | A |
| | | | $T_J = 125\text{ }^\circ\text{C}$ | - | 3.7 | - | A |
| S | Softness per diode = $\frac{t_b}{t_a}$ | $di_F/dt = 200\text{ A}/\mu\text{s}$ $V_R = 130\text{ V},$ $I_F = 20\text{ A}$ | $T_J = 25\text{ }^\circ\text{C}$ | - | 0.45 | - | |
| | | | $T_J = 125\text{ }^\circ\text{C}$ | - | 0.32 | - | |

Note to component engineers: Q-Series diodes employ Schottky technologies in their design and construction. Therefore, component engineers should plan their test setups to be similar to traditional Schottky test setups. (For further details, see application note AN-300.)

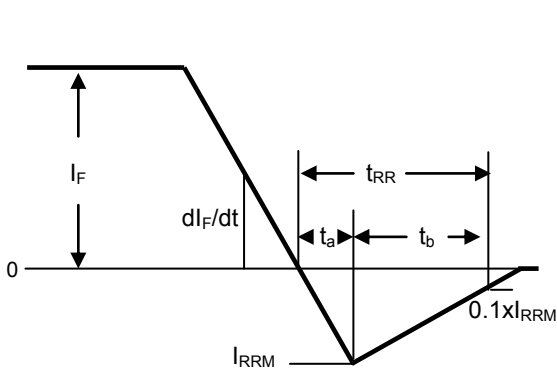


Figure 1. Reverse Recovery Definitions

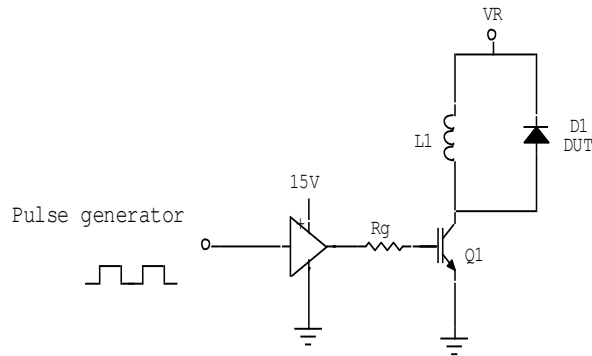


Figure 2. Reverse Recovery Test Circuit

Electrical Specifications at $T_J = 25\text{ }^\circ\text{C}$ (unless otherwise specified)

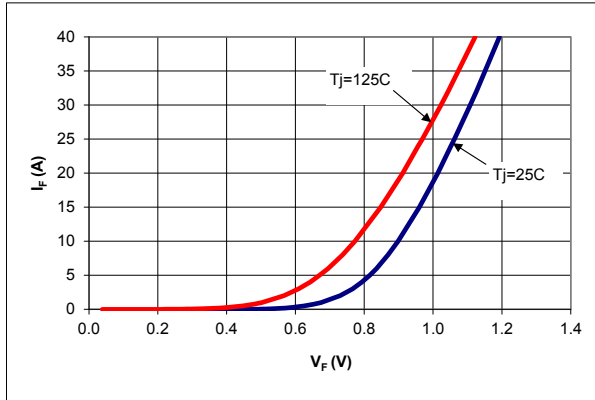


Figure 3. Typical I_F vs V_F

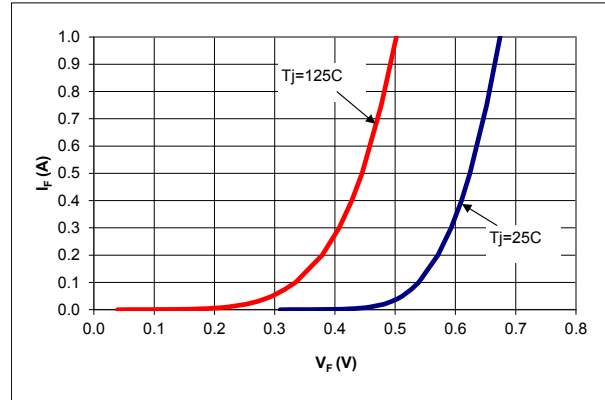


Figure 4. Typical I_F vs V_F

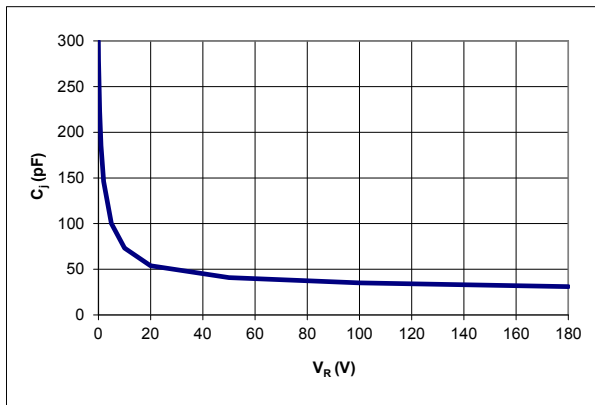


Figure 5. Typical C_i vs V_R

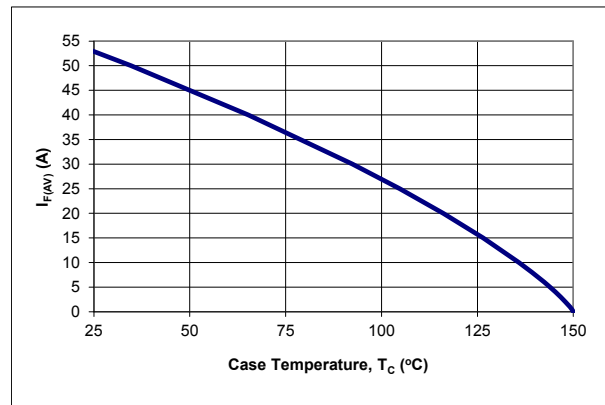


Figure 6. DC Current Derating Curve

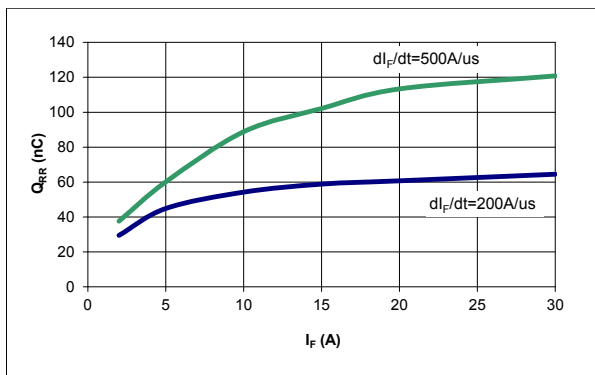


Figure 7. Typical Q_{RR} vs I_F at $T_J=125\text{ }^\circ\text{C}$

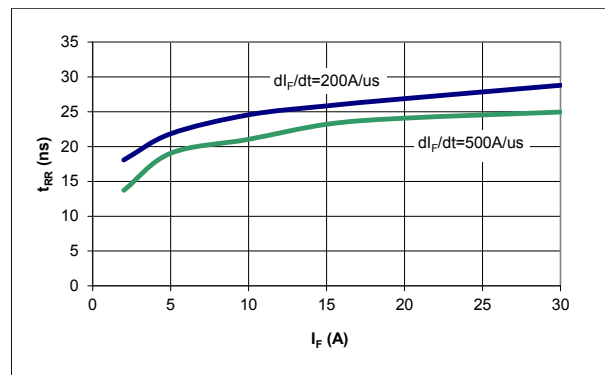


Figure 8. Typical t_{RR} vs I_F at $T_J=125\text{ }^\circ\text{C}$

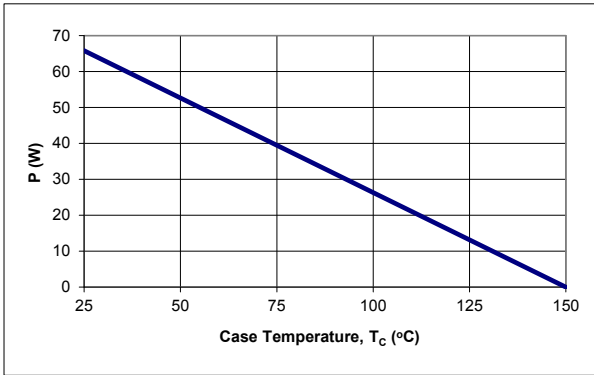


Figure 9. Power Derating Curve

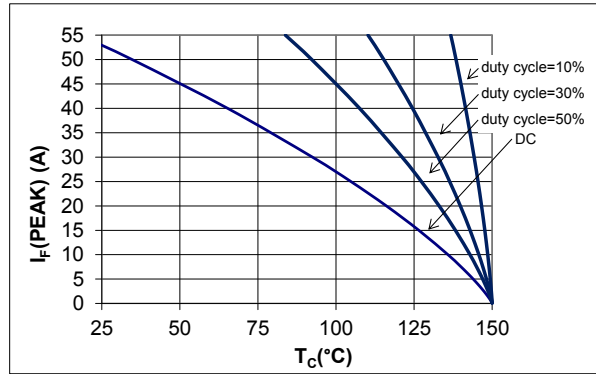


Figure 10. IF (Peak) vs TC, f = 70 kHz

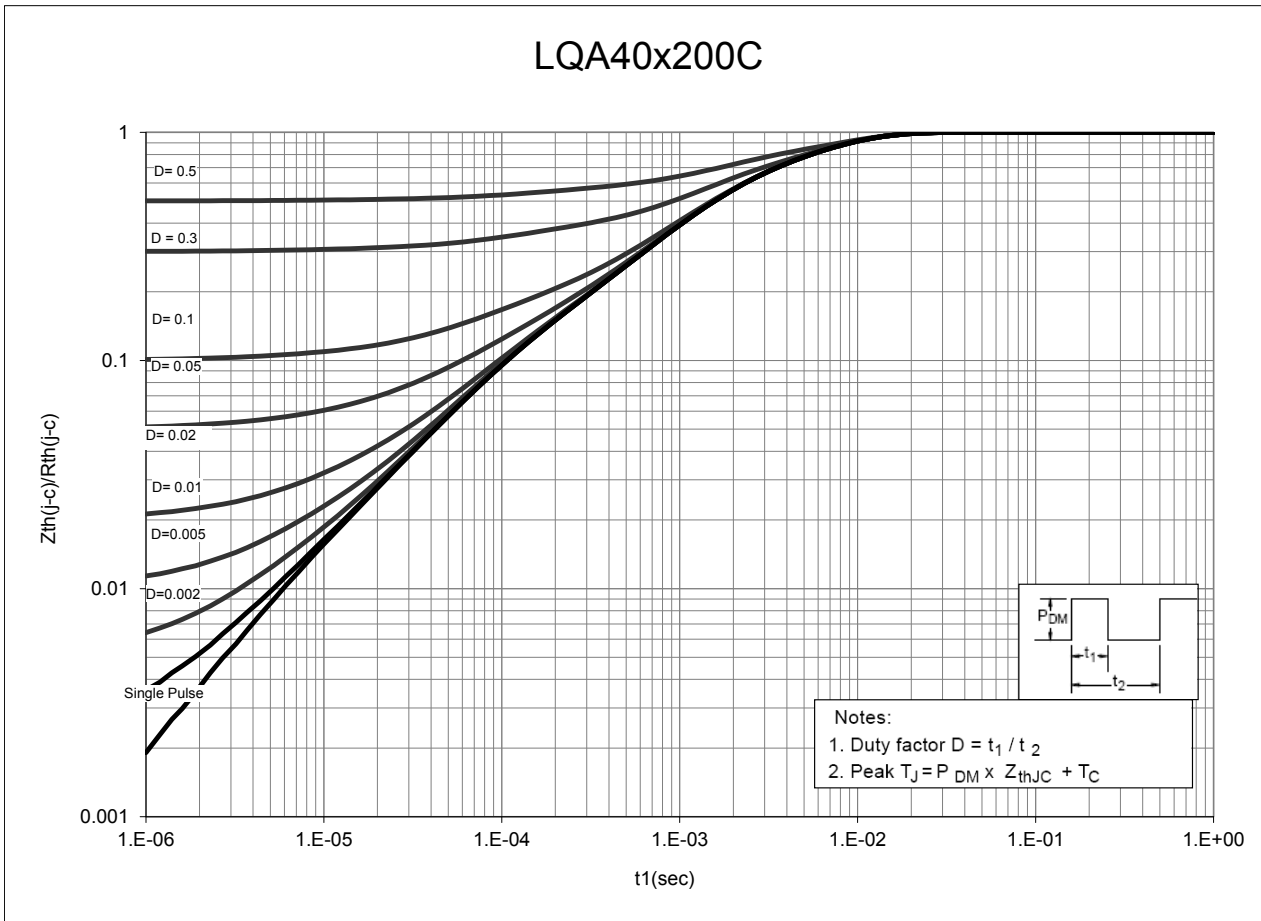
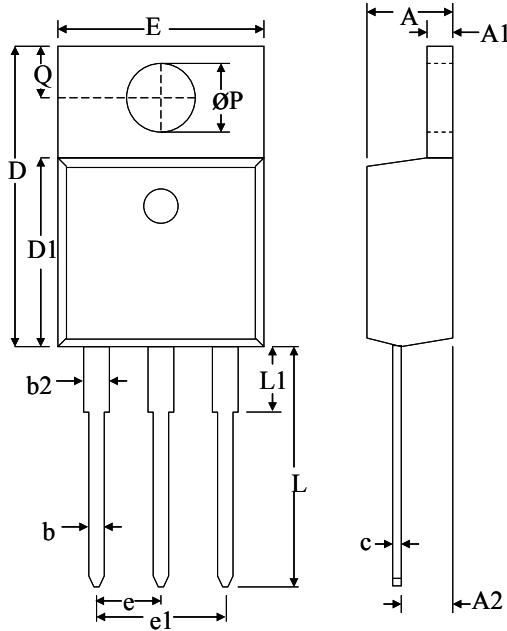


Figure 11. Normalized Maximum Transient Thermal Impedance

Dimensional Outline Drawings

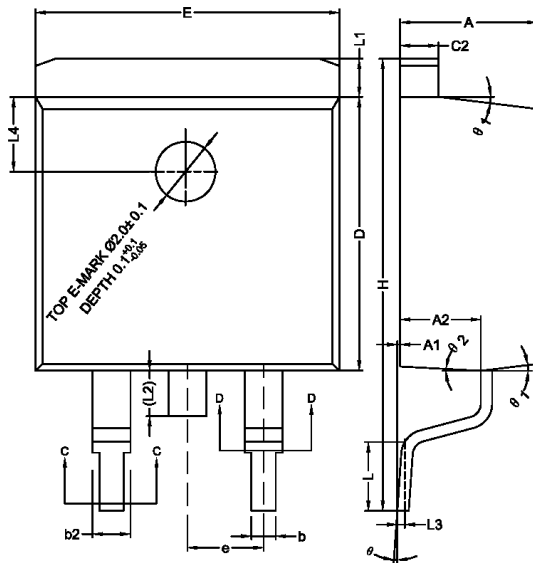
TO-220AB



| Dim | Millimeters | |
|-----|-------------|-------|
| | MIN | MAX |
| A | 4.32 | 4.70 |
| A1 | 1.11 | 1.38 |
| A2 | 2.59 | 2.79 |
| b | 0.77 | 1.00 |
| b2 | 1.23 | 1.36 |
| C | 0.34 | 0.47 |
| D | 14.71 | 15.75 |
| D1 | 9.05 | 9.25 |
| E | 9.96 | 10.36 |
| e | 2.44 | 2.64 |
| e1 | 4.98 | 5.18 |
| L | 12.70 | 14.22 |
| L1 | – | 3.90 |
| ØP | 3.71 | 3.96 |
| Q | 2.54 | 2.90 |

| Mechanical Mounting Method | Maximum Torque / Pressure specification |
|-----------------------------------|---|
| Screw through hole in package tab | 1 Newton Meter (nm) or 8.8 inch-pounds (lb-in) |
| Clamp against package body | 12.3 kilogram-force per square centimeter (kgf/cm ²) or 175 lbf/in ² |

TO-263AB



| Dim | Millimeters | |
|-----|-------------|----------|
| | MIN | MAX |
| A | 4.40 | 4.70 |
| A1 | 0.00 | 0.25 |
| A2 | 2.59 | 2.79 |
| b | 0.77 | 0.90 |
| b2 | 1.23 | 1.36 |
| c2 | 1.22 | 1.32 |
| D | 9.05 | 9.25 |
| E | 10.06 | 10.26 |
| e | 2.54 BSC | 2.54 BSC |
| H | 14.70 | 15.50 |
| L | 2.00 | 2.60 |
| L1 | 1.17 | 1.40 |
| L2 | – | 1.75 |
| L3 | 0.25 BSC | 0.25 BSC |
| L4 | 2.00 BSC | 2.00 BSC |
| Ø | 0° | 8° |
| Ø1 | 5° | 9° |
| Ø2 | 1° | 5° |

Soldering time and temperature: This product has been designed for use with high-temperature, lead-free solder. The component leads can be subjected to a maximum temperature of 300 °C, for up to 10 seconds. See Application Note AN-303, for more details.

Ordering Information

| Part Number | Package | Packing |
|-------------|----------|----------------|
| LQA40T300C | TO-220AB | 50 units/tube |
| LQA40B300C | TO-263AB | 800 units/reel |

The information contained in this document is subject to change without notice.

| Revision | Notes | Date |
|----------|-----------------|-------|
| 1.0 | Initial Release | 04/13 |

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