

Insulated Hyperfast Rectifier Module, 280 A



SOT-227

FEATURES

- Two fully independent diodes
- Fully insulated package
- Hyperfast, soft reverse recovery, with high operation junction temperature (T_J max. = 175 °C)
- Low forward voltage drop
- Optimized for power conversion: welding and industrial SMPS applications
- Easy to use and parallel
- Industry standard outline
- UL approved file E78996 
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT

PRIMARY CHARACTERISTICS

V_R	300 V
$I_{F(AV)}$ per module at $T_C = 81\text{ °C}$	280 A
t_{rr}	58 ns
Type	Modules - diode FRED Pt®
Package	SOT-227
Circuit configuration	Two separate diodes, parallel pin-out

DESCRIPTION / APPLICATIONS

The VS-UFH280FA30 insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The diodes structure, and its life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Cathode to anode voltage	V_R		300	V
Continuous forward current per diode	I_F	$T_C = 95\text{ °C}$	160	A
Single pulse forward current per diode	I_{FSM}	$T_C = 25\text{ °C}$	1539	
Maximum power dissipation per module	P_D	$T_C = 95\text{ °C}$	410	W
RMS isolation voltage	V_{ISOL}	Any terminal to case, $t = 1\text{ min}$	2500	V
Operating junction and storage temperatures	T_J, T_{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS PER DIODE ($T_J = 25\text{ °C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	$I_R = 200\text{ }\mu\text{A}$	300	-	-	V
Forward voltage	V_{FM}	$I_F = 100\text{ A}$	-	1.07	1.27	
		$I_F = 100\text{ A}, T_J = 175\text{ °C}$	-	0.82	-	
Reverse leakage current	I_{RM}	$V_R = V_R$ rated	-	0.5	100	μA
		$T_J = 175\text{ °C}, V_R = V_R$ rated	-	0.74	-	mA
Junction capacitance	C_T	$V_R = 300\text{ V}$	-	216	-	pF


DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	58	-	ns
		$T_J = 125\text{ }^{\circ}\text{C}$	-	85	-	
Peak recovery current	I_{RRM}	$T_J = 25\text{ }^{\circ}\text{C}$	-	4.5	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	10	-	
Reverse recovery charge	Q_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	130	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	429	-	

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction-to-case, single leg conducting	R_{thJC}		-	-	0.39	$^{\circ}\text{C/W}$
Junction-to-case, both leg conducting			-	-	0.195	
Case-to-heatsink	R_{thCS}	Flat, greased surface	-	0.1	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style			SOT-227			

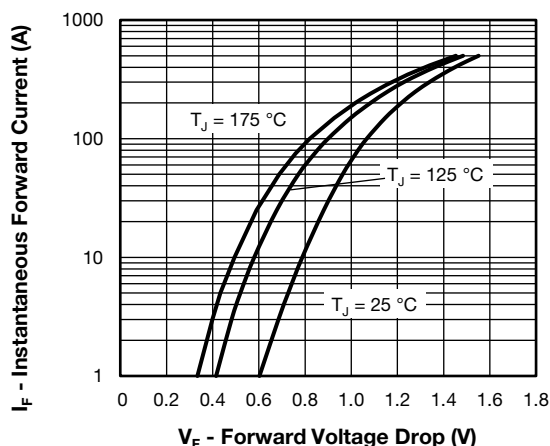


Fig. 1 - Typical Forward Voltage Drop vs. Instantaneous Forward Current (Per Diode)

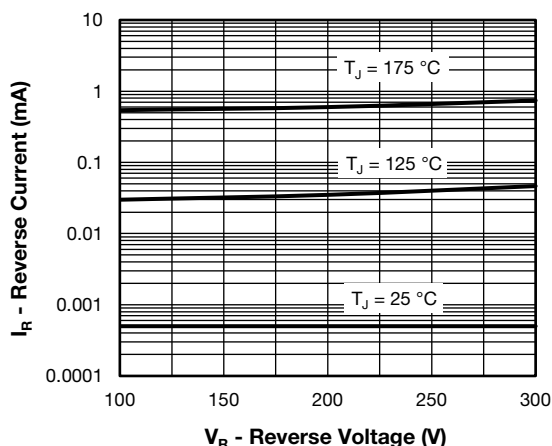


Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Diode)

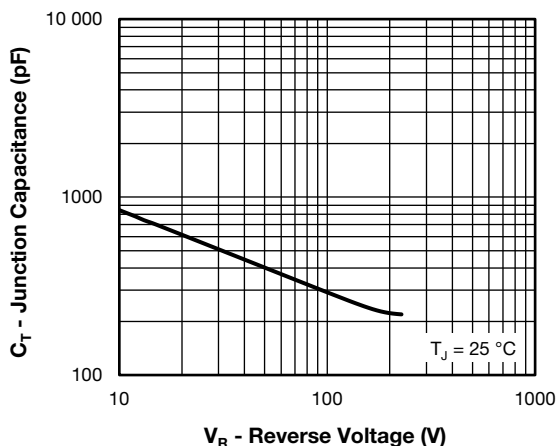


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Diode)

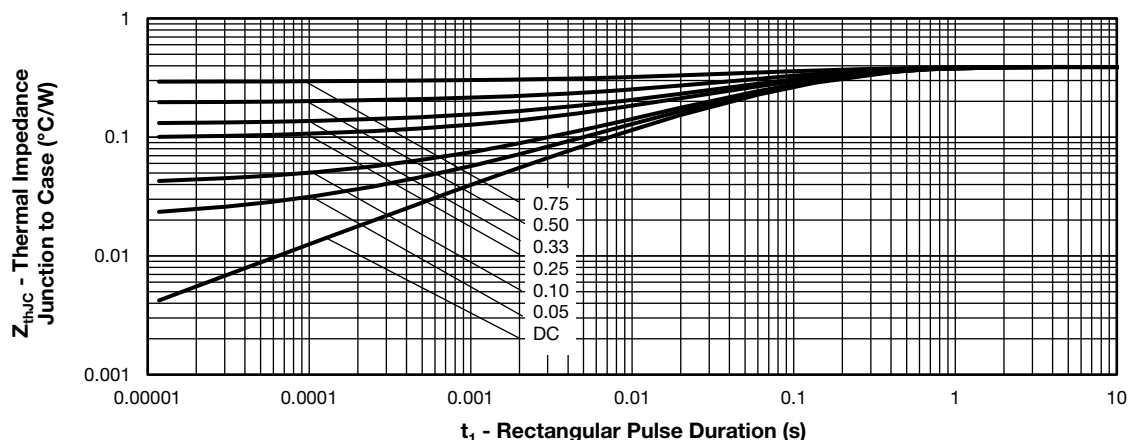


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Diode)

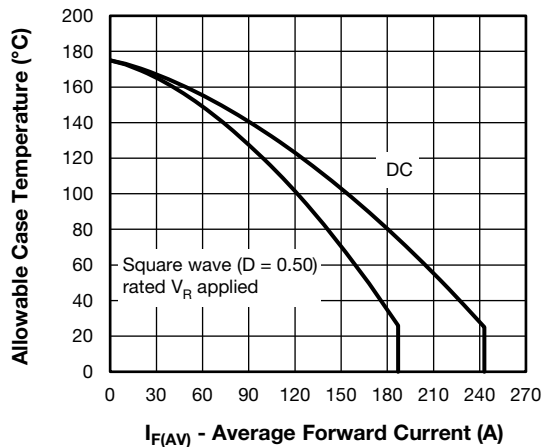


Fig. 5 - Maximum Current Rating Capability (Per Diode)

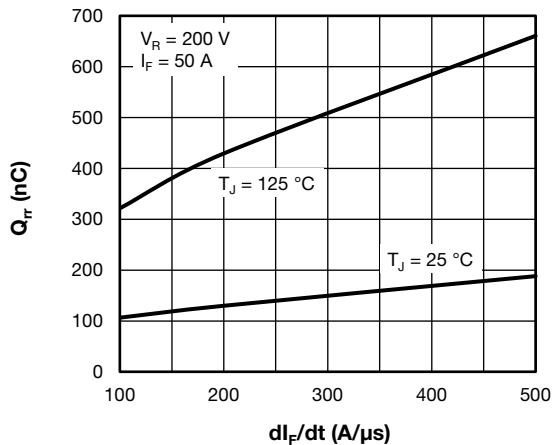


Fig. 7 - Typical Reverse Recovery Charge vs. dI_F/dt (Per Diode)

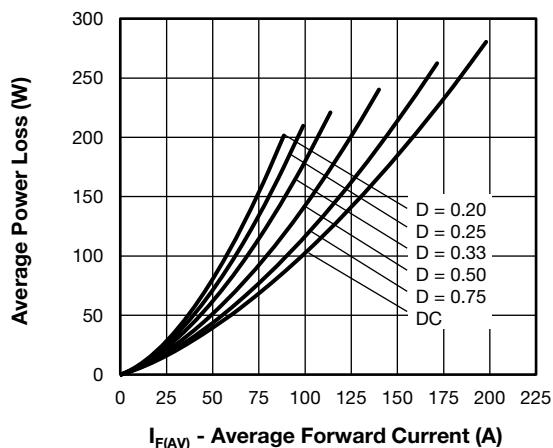


Fig. 6 - Forward Power Loss Characteristics (Per Diode)

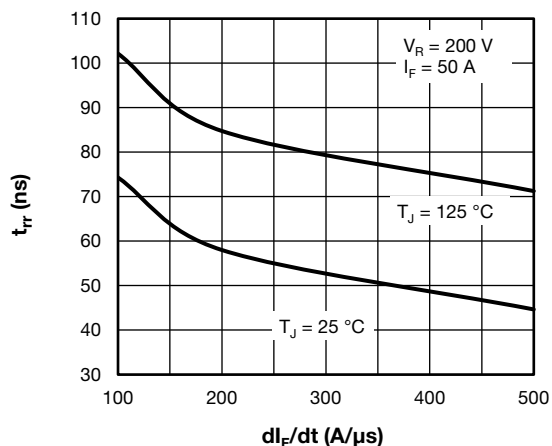


Fig. 8 - Typical Reverse Recovery Time vs. dI_F/dt (Per Diode)

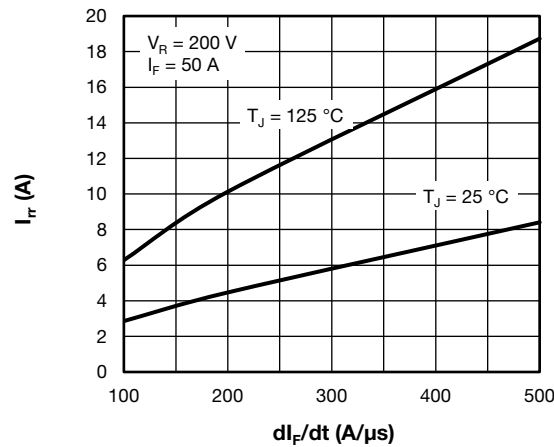


Fig. 9 - Typical Reverse Recovery Current vs. di_F/dt (Per Diode)

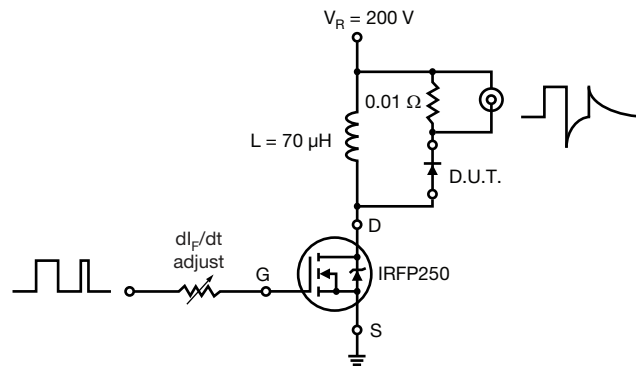


Fig. 10 - Reverse Recovery Parameter Test Circuit

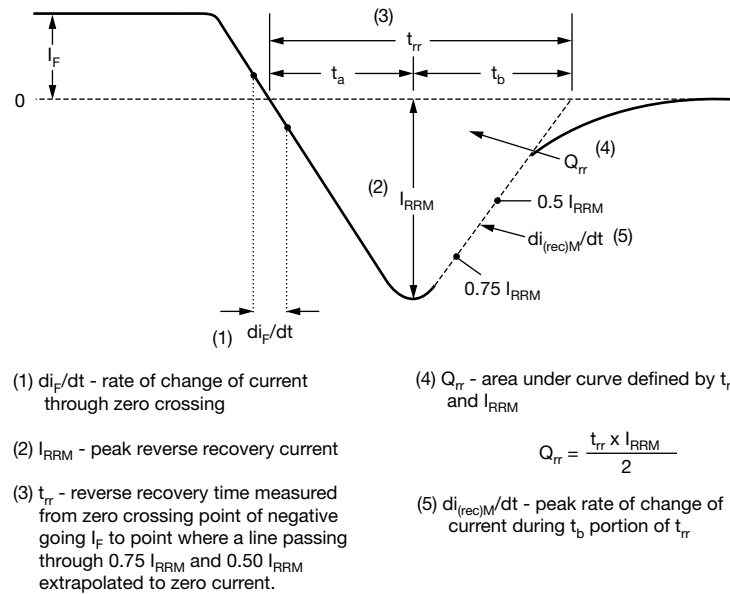


Fig. 11 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code	VS-	UF	H	280	F	A	30
	1	2	3	4	5	6	7

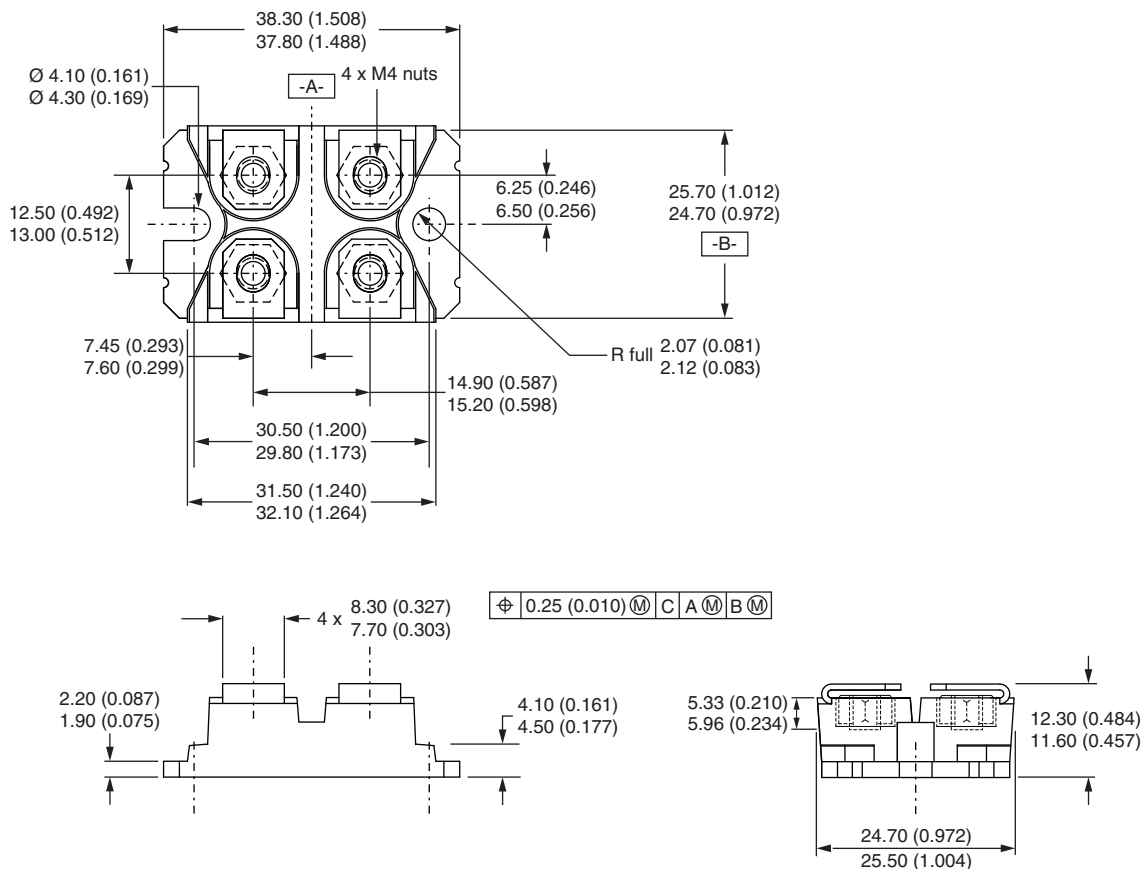
- 1 - Vishay Semiconductors product
- 2 - Ultra fast rectifier
- 3 - Hyper fast FRED Pt[®] diffused
- 4 - Current rating (280 = 280 A)
- 5 - Circuit configuration (two separate diodes, parallel pin-out)
- 6 - Package indicator (SOT-227 standard insulated base)
- 7 - Voltage rating (30 = 300 V)

Circuit Configuration		
Circuit	Circuit Configuration Code	Circuit Drawing
Two separate diodes, parallel pin-out	F	The circuit drawing for configuration F consists of two parts. On the left is a schematic diagram showing two diodes connected in parallel. The anodes of both diodes are connected to a common input terminal labeled '1'. The cathodes of both diodes are connected to a common output terminal labeled '2'. There are also two additional terminals, '4' and '3', shown on the left and right respectively, which are not connected in this schematic. On the right is a physical package layout diagram showing a rectangular package with four circular leads. The leads are numbered 1, 2, 3, and 4. Lead 1 is at the bottom left, lead 2 is at the bottom right, lead 3 is at the top right, and lead 4 is at the top left. The package has a central vertical slot and a circular feature on the left side.

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95423
Packaging information	www.vishay.com/doc?95425

SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

- Controlling dimension: millimeter



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