



MIC5281

120V_{IN}, 25mA, Ultra-Low I_Q, High-PSRR
Linear Regulator

General Description

The MIC5281 high-performance linear regulator offers a very-wide input operating voltage range, up to 120V DC, and supplies an output current of up to 25mA.

Ideal for high input voltage applications such as industrial and telecom, the MIC5281 offers $\pm 3\%$ initial accuracy, extremely high-power supply rejection ratio (>90db) and ultra-low quiescent current of 6 μ A. The MIC5281 is optimized for high-voltage line transients, making it ideal for harsh environment applications.

The MIC5281 is offered in both fixed output voltage (3.3V/5.0V) and adjustable output voltage (1.27V to 5.5V) options.

The MIC5281 operates over a -40°C to $+125^{\circ}\text{C}$ temperature range and is available in lead-free, RoHS-compliant, 8-pin ePad MSOP and 8-pin MSOP packages.

Data sheet and support documentation are found on the Micrel website: www.micrel.com.

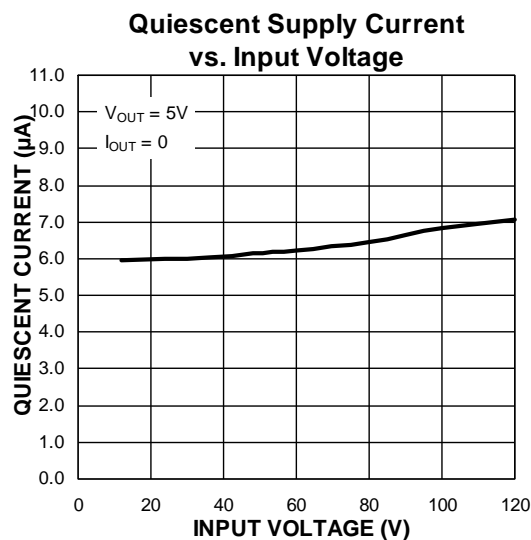
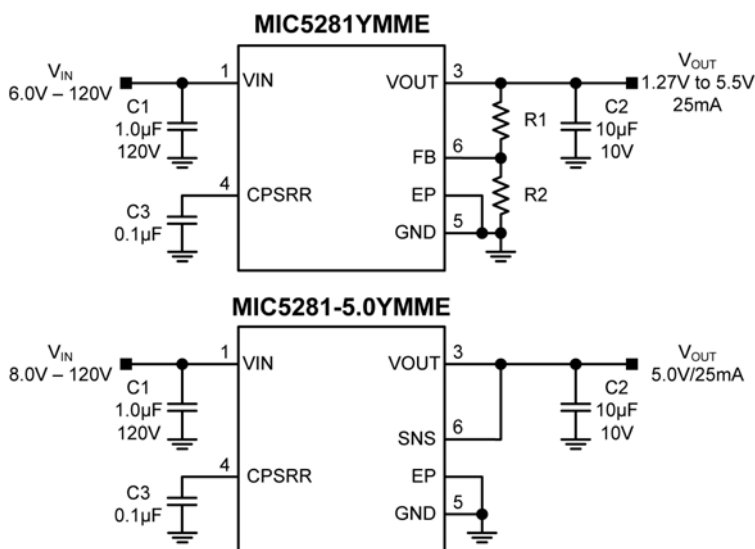
Features

- Wide input voltage range: 6V to 120V DC
- Ultra-low quiescent current: 6 μ A
- 25mA guaranteed output current
- Adjustable output from 1.27V to 5.5V
- Withstands up to +120V DC at the input
- Stable with ceramic capacitors
- Ultra-high PSRR >90dB
- Ultra-high line rejection (load dump)
- High output accuracy:
 - $\pm 3\%$ initial accuracy
- Thermal-shutdown and current-limit protection
- Thermally-efficient, 8-pin MSOP and 8-pin ePad MSOP packages

Applications

- Industrial applications
- Remote keyless entry power supply
- Telecom applications
- Off-line power supplies

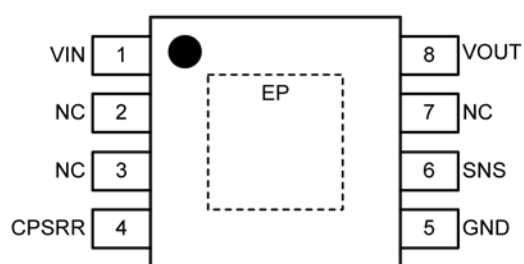
Typical Applications



Ordering Information

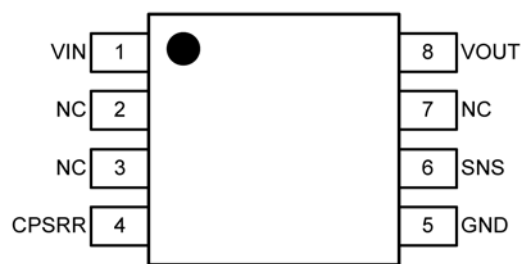
Part Number	Output Voltage	Top Mark	Temperature Range	Package	Lead Finish
MIC5281YMME	Adjustable	781A	-40°C to +125°C	8-Pin ePad MSOP	Pb-Free
MIC5281-3.3YMME	3.3V	781S	-40°C to +125°C	8-Pin ePad MSOP	Pb-Free
MIC5281-5.0YMME	5.0V	7815	-40°C to +125°C	8-Pin ePad MSOP	Pb-Free
MIC5281YMM	Adjustable	-	-40°C to +125°C	8-Pin MSOP	Pb-Free
MIC5281-3.3YMM	3.3V	-	-40°C to +125°C	8-Pin MSOP	Pb-Free
MIC5281-5.0YMM	5.0V	-	-40°C to +125°C	8-Pin MSOP	Pb-Free

Pin Configuration



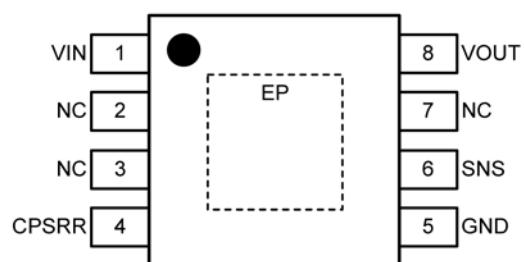
**8-Pin ePAD MSOP
MIC5281-x.xYMME**

(TOP VIEW)



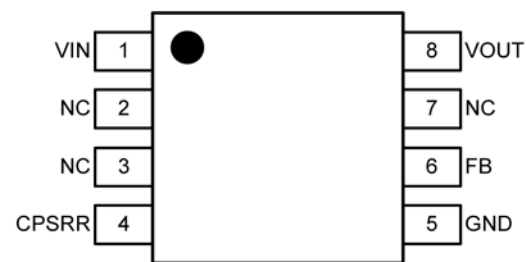
**8-Pin MSOP
MIC5281-x.xYMM**

(TOP VIEW)



**8-Pin ePAD MSOP
MIC5281YMME**

(TOP VIEW)



**8-Pin MSOP
MIC5281YMM**

(TOP VIEW)

Pin Description

Pin				Name	Function
MIC5281YMME	MIC5281-x.xYMME	MIC5281YMM	MIC5281-x.xYMM		
1	1	1	1	VIN	Supply Voltage Input. Connect 1 μ F capacitor from VIN to GND.
2, 3, 7	2, 3, 7	2, 3, 7	2, 3, 7	NC	Not internally connected. Connect NC to GND or leave unconnected.
4	4	4	4	CPSRR	Bypass Capacitor Connection. Connect 0.1 μ F capacitor from CPSRR to GND.
5	5	5	5	GND	Ground.
6	–	6	–	FB	Feedback Connection. For external resistor divider to set V _{OUT} .
–	6	–	6	SNS	Sense input. Connect SNS to V _{OUT} .
8	8	8	8	VOUT	Regulator Output. Connect 10 μ F capacitor from V _{OUT} to GND.
EP	EP	–	–	EP	Exposed Pad (ePad) for Thermal Relief. Connect EP to GND.

Absolute Maximum Ratings⁽¹⁾

V_{IN} to GND.....	-0.3V to +125V
V_{CPSRR} to GND.....	-0.3 to +14V
V_{FB} , V_{SNS} , V_{OUT} to GND	-0.3V to +6V
Lead Temperature (soldering, 10s).....	+260°C
Junction Temperature	$-40^{\circ}\text{C} \leq T_J \leq +125^{\circ}\text{C}$
Storage Temperature	$-65^{\circ}\text{C} \leq T_A \leq +150^{\circ}\text{C}$
ESD Ratings⁽⁴⁾	
HBM.....	2kV
MM.....	200V

Operating Ratings⁽²⁾

V_{IN}	+6V to +120V
V_{OUT} Adjust Range	+1.27V to +5.5V
Junction Temperature	$-40^{\circ}\text{C} \leq T_J \leq +125^{\circ}\text{C}$
Power Dissipation (P_D).....	Internally Limited ⁽³⁾
Junction Thermal Resistance (θ_{JA})	
8-pin ePad MSOP.....	64°C/W
8-pin MSOP.....	160°C/W

Electrical Characteristics⁽⁵⁾

$V_{IN} = 12\text{V}$, $C_{IN} = 1.0\mu\text{F}$, $C_{PSRR} = 0.1\mu\text{F}$, $C_{OUT} = 10\mu\text{F}$, $V_{OUT} = 5.0\text{V}$ or 3.3V , $I_{OUT} = 100\mu\text{A}$, $T_A = 25^{\circ}\text{C}$, **bold** values indicate $-40^{\circ}\text{C} \leq T_J \leq +125^{\circ}\text{C}$, unless noted.

Parameter	Condition	Min.	Typ.	Max.	Units
Power Supply Input					
Input Voltage Range ⁽⁶⁾		6		120	V
Quiescent Supply Current	$I_{OUT} = 0$		6	11	μA
Output Voltage					
Output Voltage	Adjustable	1.27		5.5	V
	Fixed 3.3V	3.2	3.3	3.4	
		3.13	3.3	3.47	
	Fixed 5.0V	4.85	5.0	5.15	
4.75		5.0	5.25		
Output Voltage Accuracy	Variation from nominal V_{OUT}	-3		+3	%
		-5		+5	%
Load Regulation	$I_{OUT} = 100\mu\text{A}$ to 25mA	-0.5	0.2	+0.5	%
Line Regulation ⁽⁷⁾	$V_{IN} = 10\text{V}$ to 120V	-0.5	0.04	+0.5	%/V
Feedback Input (Adjustable)					
FB Voltage		1.232	1.270	1.308	V
		1.206	1.270	1.333	
FB Current	$V_{FB} = 1.27\text{V}$		3.2		nA
Current Limit					
Current Limit	$V_{OUT} = 0\text{V}$	30	65	130	mA
Ripple Rejection					
Power Supply Rejection Ratio	$f = 20\text{kHz}$ to 2MHz		>90		dB

Notes:

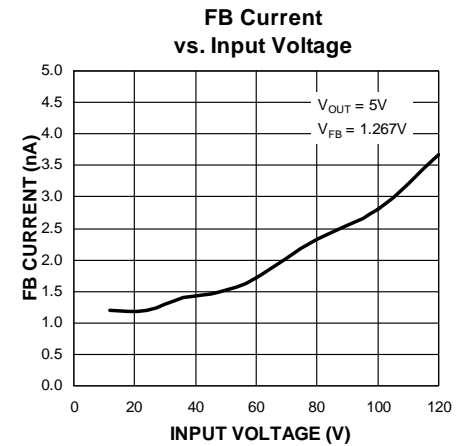
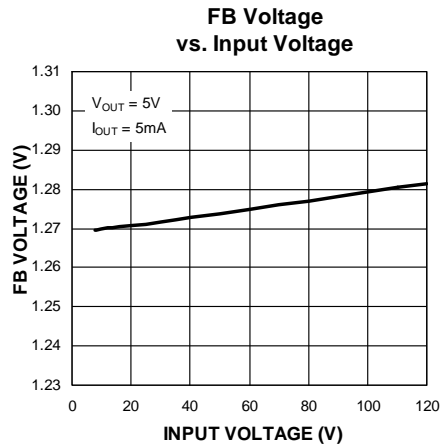
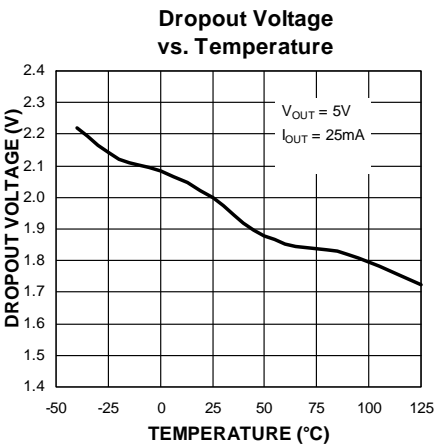
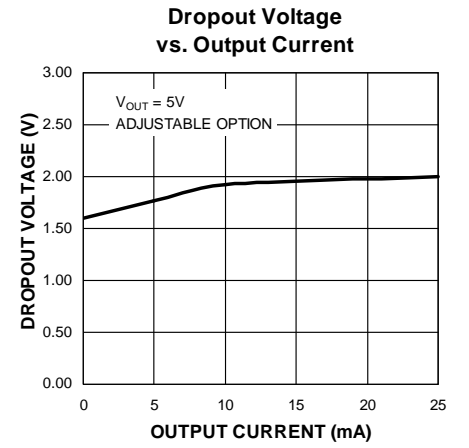
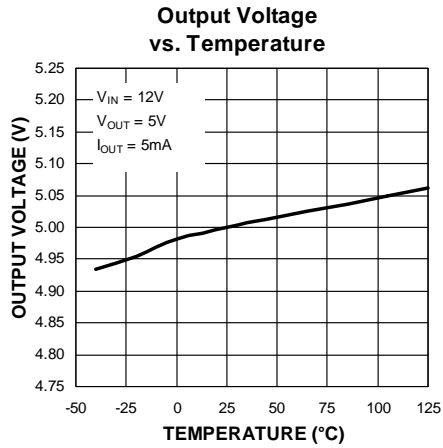
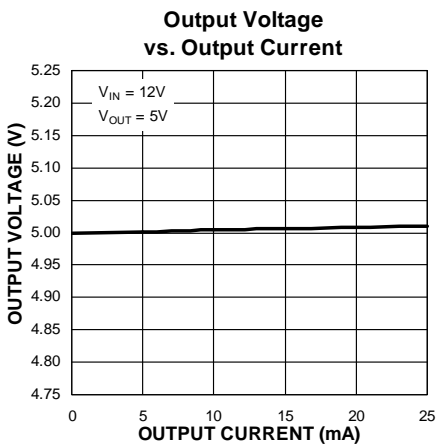
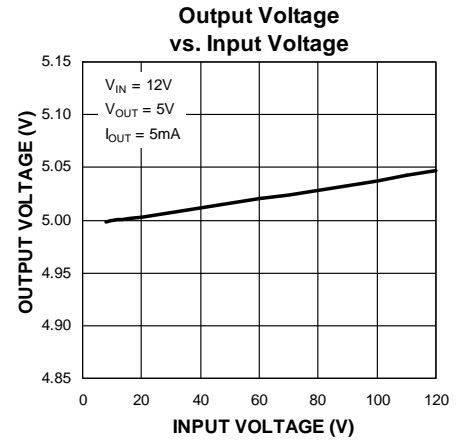
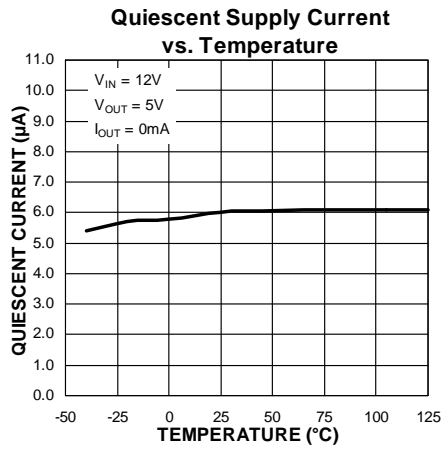
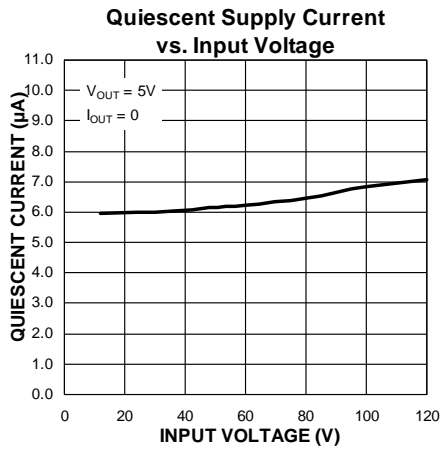
- Exceeding an absolute maximum rating may damage the device.
- The device is not guaranteed to function outside its operating rating.
- The maximum allowable power dissipation at any T_A (ambient temperature) is $P_{D(max)} = (T_{J(max)} - T_A) / \theta_{JA}$. Exceeding the maximum allowable power dissipation results in excessive die temperature, and causes the regulator to enter thermal shutdown.
- Devices are ESD sensitive; use proper handling precautions.
- Specifications are for packaged products only.
- Assure that $V_{IN} \geq (V_{OUT} + 3\text{V})$ and $V_{IN} \geq 6\text{V}$.
- Line regulation is a percentage of V_{OUT} .

Electrical Characteristics⁽⁵⁾ (Continued)

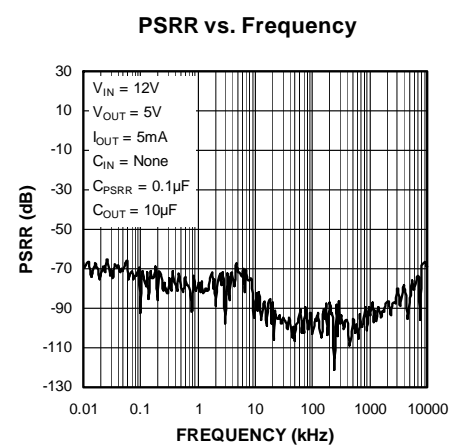
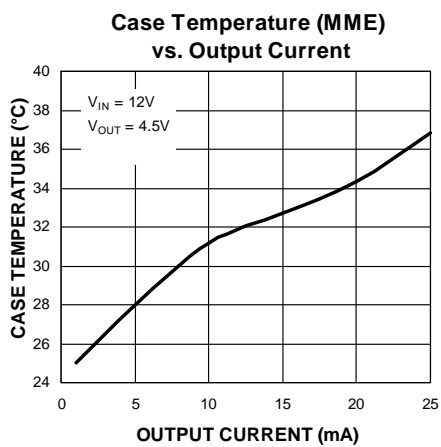
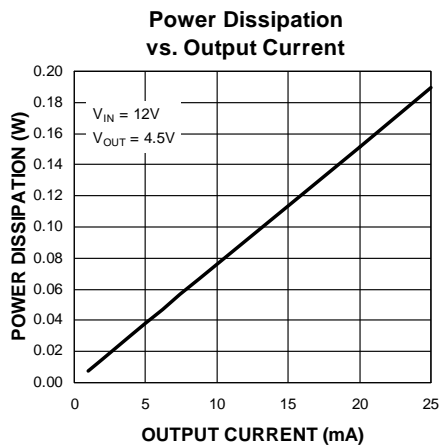
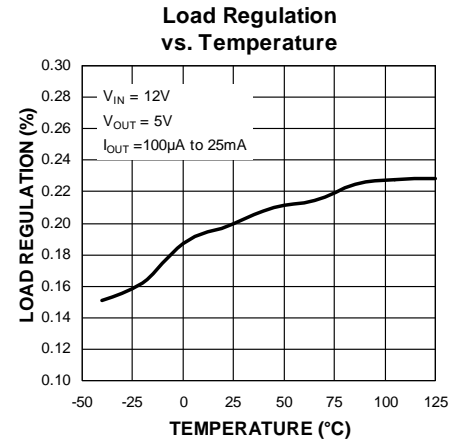
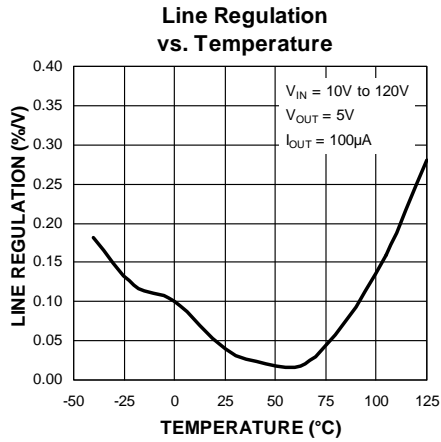
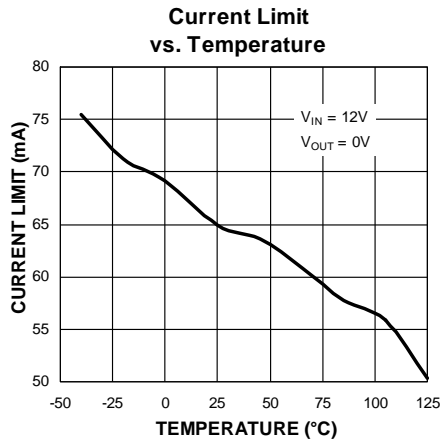
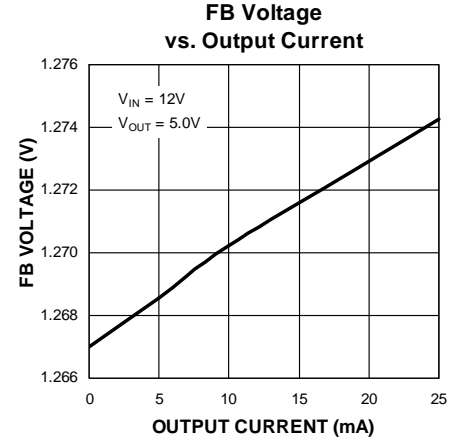
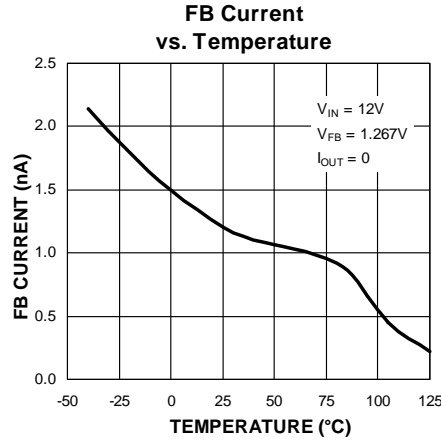
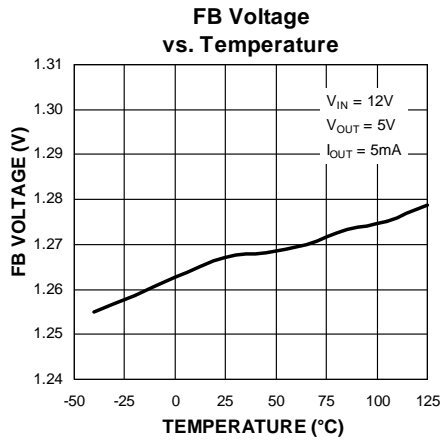
$V_{IN} = 12V$, $C_{IN} = 1.0\mu F$, $C_{PSRR} = 0.1\mu F$, $C_{OUT} = 10\mu F$, $V_{OUT} = 5.0V$ or $3.3V$, $I_{OUT} = 100\mu A$, $T_A = 25^\circ C$, **bold** values indicate $-40^\circ C \leq T_J \leq +125^\circ C$, unless noted.

Parameter	Condition	Min.	Typ.	Max.	Units
Power Dropout Voltage					
Dropout Voltage	$I_{OUT} = 25mA$		2	3	V
Thermal Protection					
Thermal-Shutdown Temperature	T_J rising		157		$^\circ C$
Thermal-Shutdown Hysteresis			15		$^\circ C$

Typical Characteristics

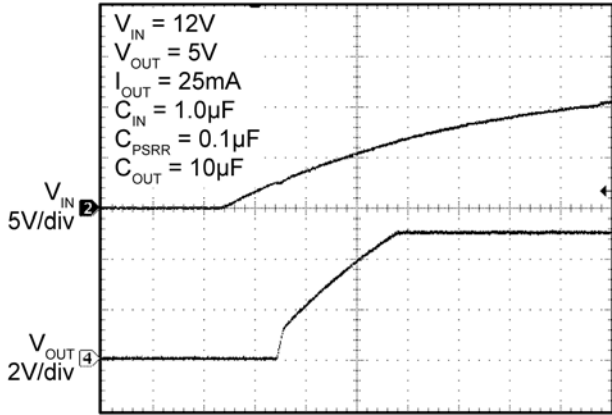


Typical Characteristics (Continued)



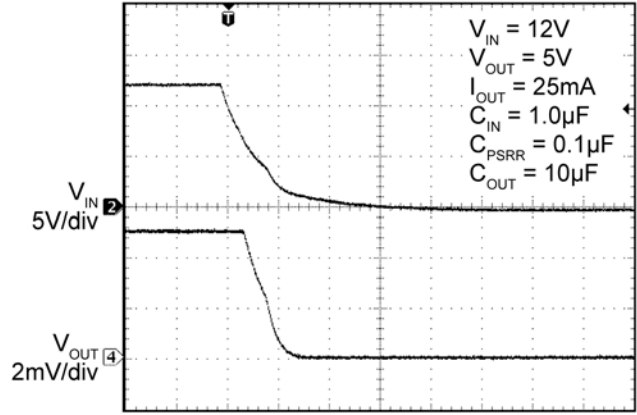
Functional Characteristics

Soft Turn-On into Full Load



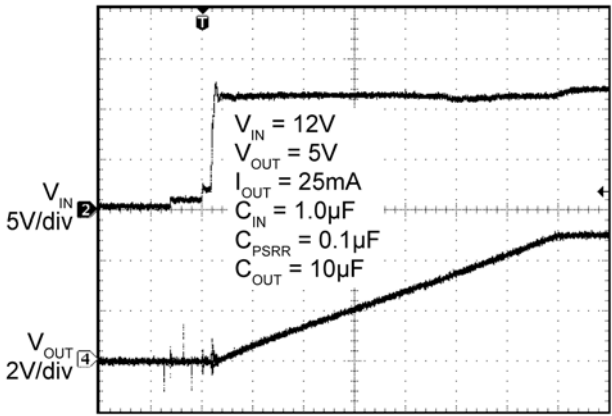
Time (4ms/div)

Soft Turn-Off



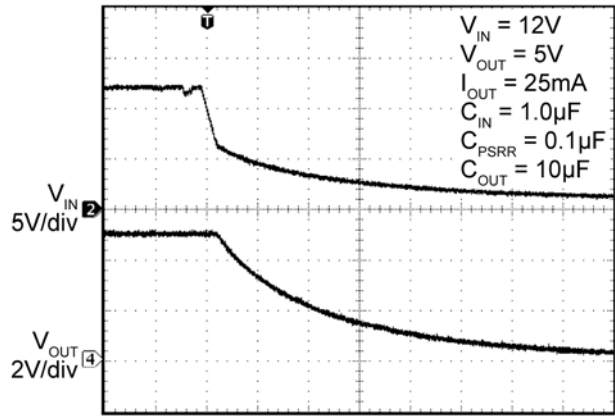
Time (200ms/div)

Hot Plug



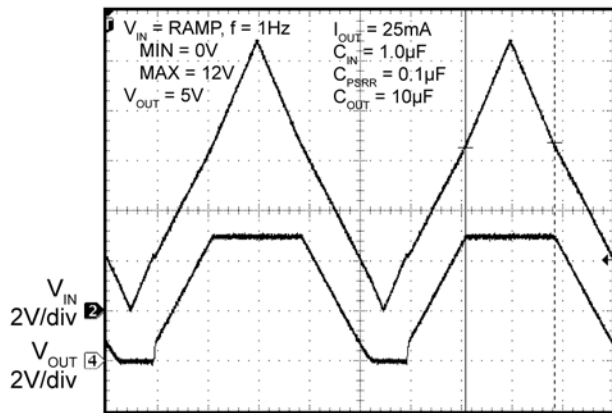
Time (100µs/div)

Hot Un-Plug



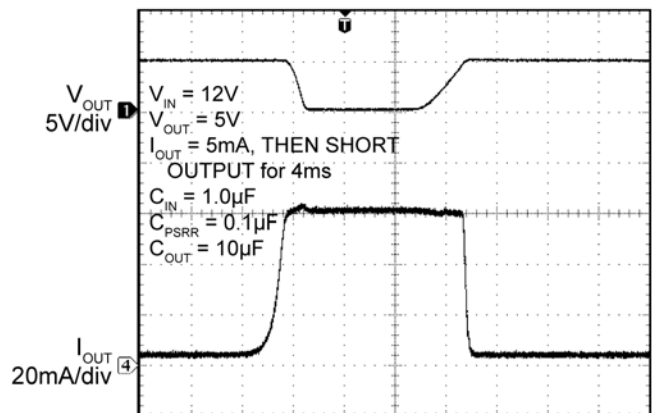
Time (4.0ms/div)

Turn-On and Turn-Off



Time (200ms/div)

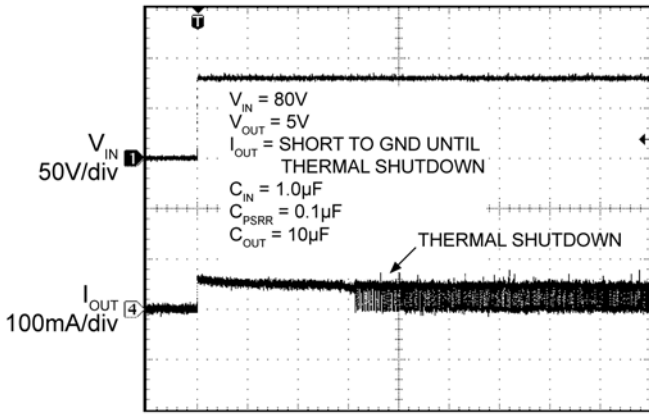
Current Limit



Time (1.0ms/div)

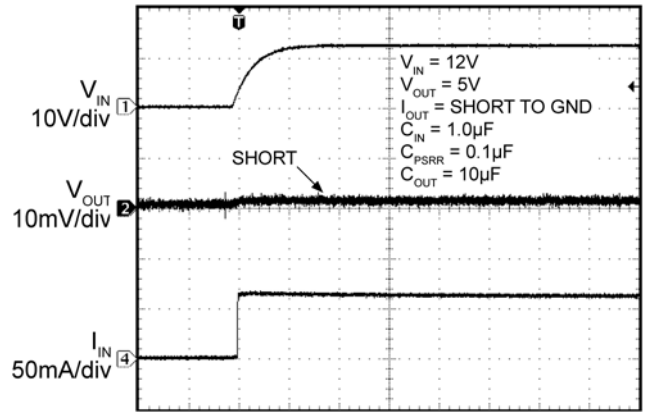
Functional Characteristics (Continued)

Thermal Shutdown, Short Circuit



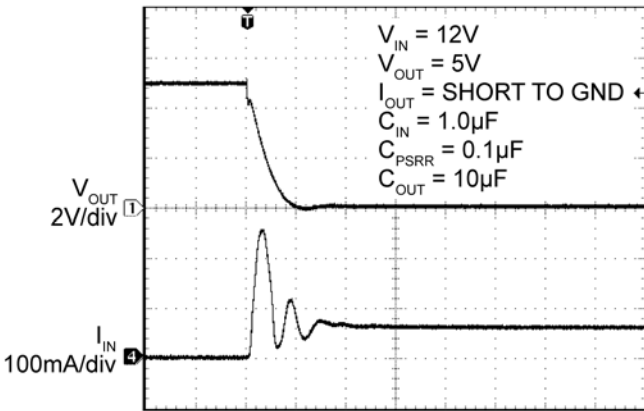
Time (100ms/div)

Turn-On into Short Circuit



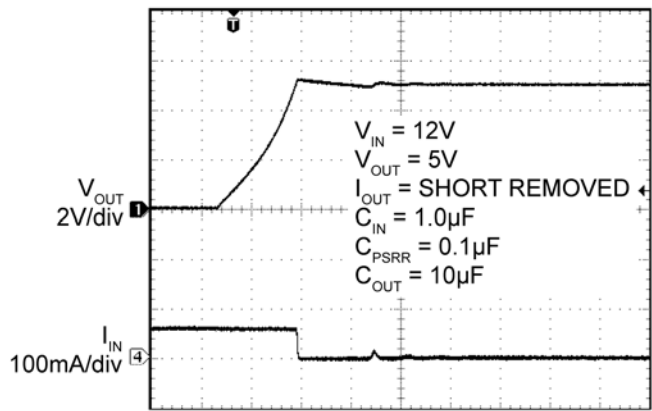
Time (40ms/div)

Short-Circuit Response



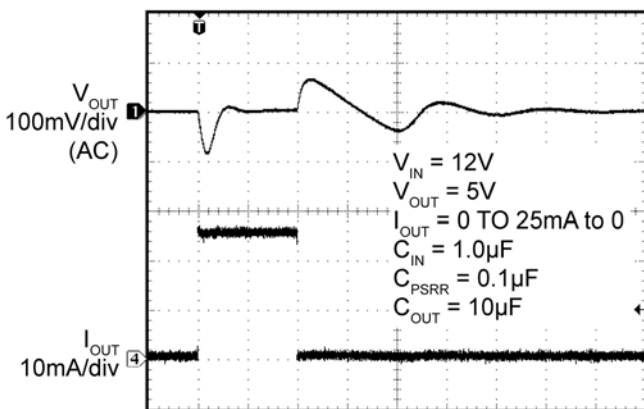
Time (4.0μs/div)

Short-Circuit Output Voltage Recovery



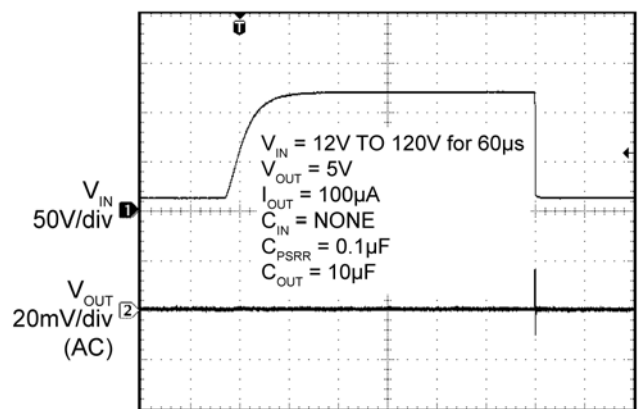
Time (400μs/div)

Load Transient Response



Time (100μs/div)

Line Transient Response



Time (10μs/div)

Detailed Description

The MIC5281 voltage regulator accepts a 6V to 120V input and has an ultra-low 6 μ A typical quiescent current while offering an excellent line transient response and PSRR. These features make it ideal for harsh, noisy environments. All options of the device offer 25mA of output current. The MIC5281YMM and MIC5281YMME offer an adjustable output voltage from 1.27V to 5.5V. The MIC5281-3.3YMME and MIC5281-3.3YMM offer fixed 3.3V outputs and the MIC5281-5.0YMME and MIC5281-5.0YMM offer fixed 5.0V outputs. The YMME packaged devices feature a heat slug to more effectively remove heat from the die.

Applications Information

Thermal Protection

MIC5281 has internal thermal shutdown to protect it from excessive heating of the die. When the junction temperature exceeds approximately +155°C, the output is disabled and the device begins to cool down. The device turns back on when the junction temperature cools by 15°C. This will result in a cycled output during continuous thermal-overload conditions.

Current Limit

MIC5281 features output current-limit protection. The output sustains a continuous short circuit to GND without damage to the device, but thermal shutdown often results.

Input Capacitor

Connect a 1.0 μ F capacitor from VIN to GND. Micrel recommends the C5750X7R2E105M, 1.0 μ F, 250V capacitor made by TDK. When using a different capacitor, assure that the voltage rating of the capacitor exceeds any potential transient.

CPSRR Capacitor

Connect a 0.1 μ F capacitor from CPSRR to GND to maintain high power supply rejection. The voltage rating of the capacitor must be at least 14V.

Output Capacitor

Connect a 10 μ F capacitor from VOUT to GND. Assure that the voltage rating of the capacitor exceeds the designed output voltage of the MIC5281.

Output Voltage Setting

For the MIC5281YMME and MIC5281YMM, V_{OUT} is programmed from 1.27V to 5.5V using:

$$V_{OUT} = V_{REF} \times \left(\frac{R1}{R2} + 1 \right)$$

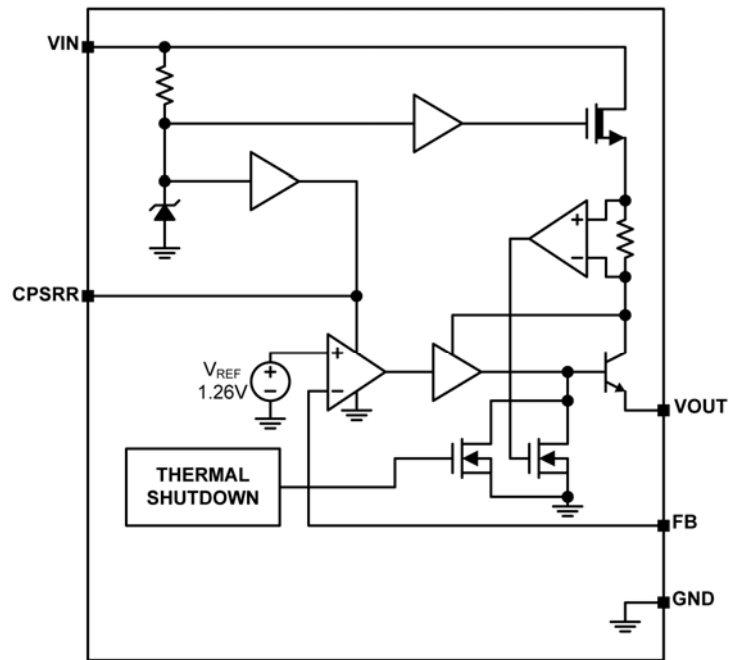
where V_{REF} = 1.267V, and R1 and R2 are shown in the Typical Applications circuit.

Thermal Measurements

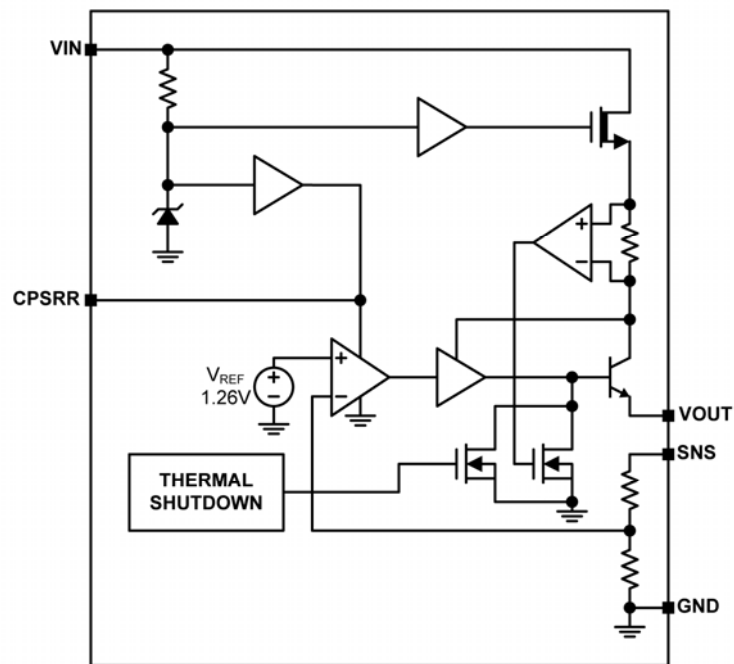
It is always wise to measure an IC's case temperature to make sure that it is within operating limits, but it is easy to get erroneous results. The standard thermal couple that comes with many voltage meters uses a large wire gauge that behaves like a heat-sink, resulting in artificially low case temperature measurements. Use a thermocouple of 36-gauge wire or smaller, such as the Omega (5SC-TT-K-36-36), to minimize the heat-sinking effect. Also, apply thermal compound to maximize heat transfer between the IC and the thermocouple.

An infrared thermometer is a recommended alternative. The IR thermometer from Optris has a 1mm spot size, ideal for monitoring small surface mount packages. Also, the optional stand makes it easy to keep the beam on the IC for long periods of time.

Functional Diagram

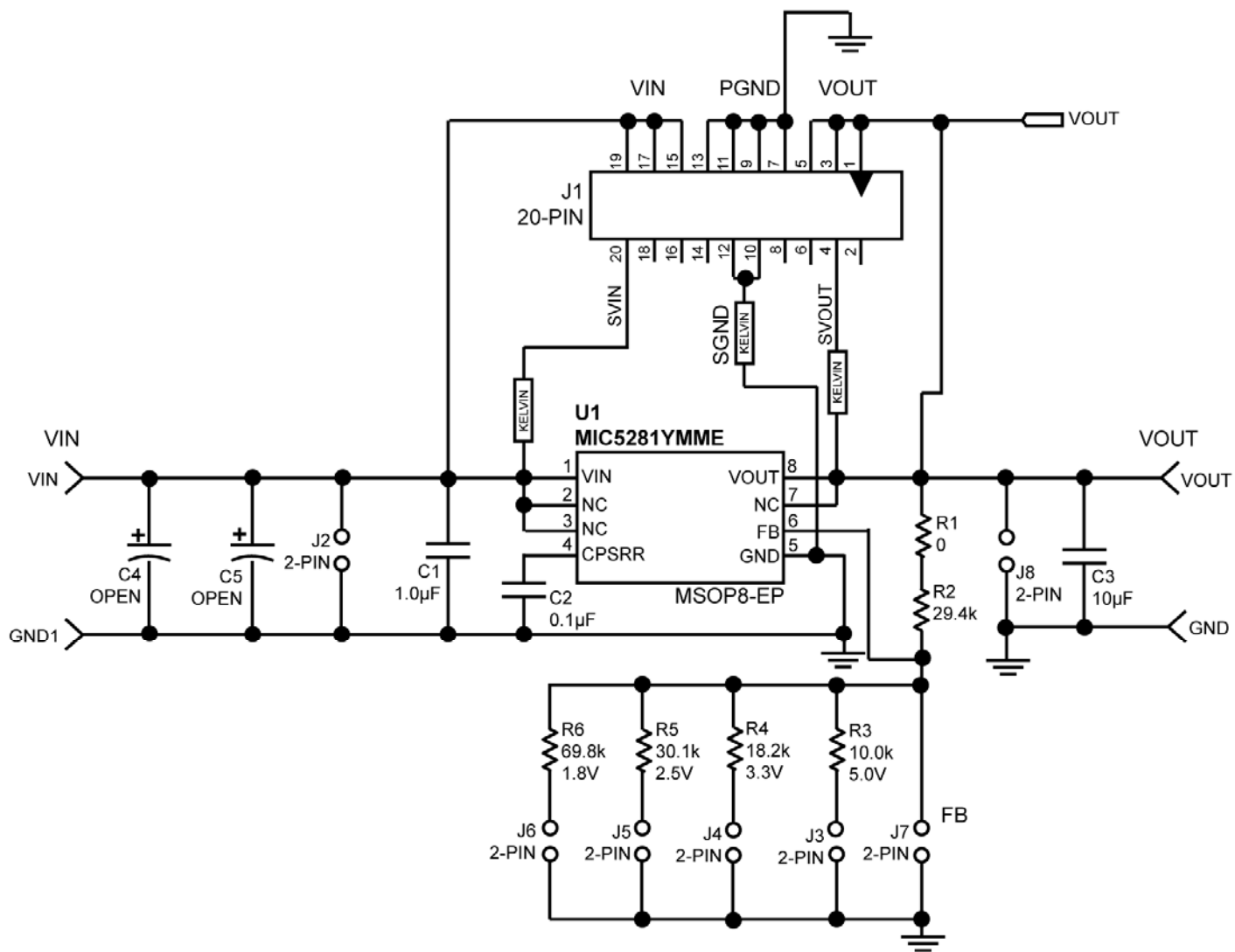


MIC5281 Adjustable Version



MIC5281 Fixed Version

MIC5281 Evaluation Board Schematic



MIC5281 Evaluation Board Schematic

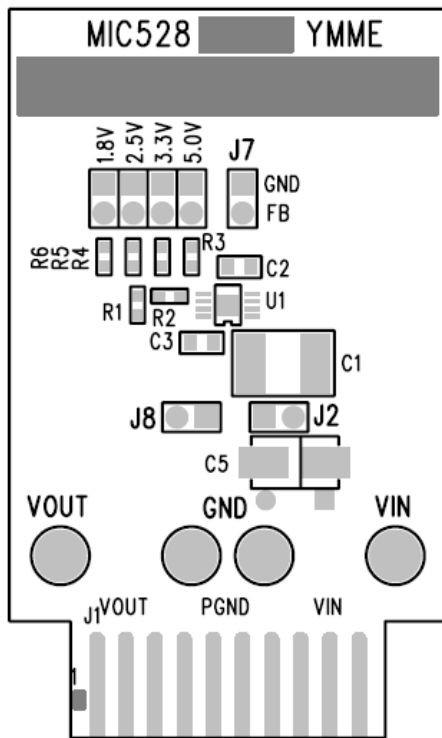
Bill of Materials

Item	Part Number	Manufacturer	Description	Qty.
C1	C5750X7R2E105M	TDK ⁽¹⁾	1.0μF, 250V, 20%, X7R capacitor (2220)	1
C2	08053C104KAT2A	AVX ⁽²⁾	0.1μF 25V 20%, X5R capacitor (0805)	1
C3	0805ZD106KAT2A	AVX ⁽²⁾	10μF, 10V, 20%, X5R, capacitor (0805)	1
C4	OPEN	Panasonic ⁽³⁾	100μF, 160V, 20% capacitor (ALU, 0.492" diameter can 0.197" lead spacing - Part # ECA-2CM101)	0
C5	OPEN	ANY	Capacitor	0
R1	CRCW06030000F	Vishay/Dale ⁽⁴⁾	0Ω, 1% resistor, 0603	1
R2	CRCW06032942F	Vishay/Dale ⁽⁴⁾	29.4kΩ, 1% resistor, 0603	1
R3	CRCW06031002F	Vishay/Dale ⁽⁴⁾	10.0kΩ, 1% resistor, 0603	1
R4	CRCW06031822F	Vishay/Dale ⁽⁴⁾	18.2kΩ, 1%, resistor, 0603	1
R5	CRCW06033012F	Vishay/Dale ⁽⁴⁾	30.1kΩ, 1% resistor chip, 0603	1
R6	CRCW06036982F	Vishay/Dale ⁽⁴⁾	69.8kΩ, 1%, resistor, 0603	1
U1	MIC5281YMME	Micrel⁽⁵⁾	120V_{IN}, 25mA, Ultra-Low I_Q, High-PSRR Linear Regulator	1

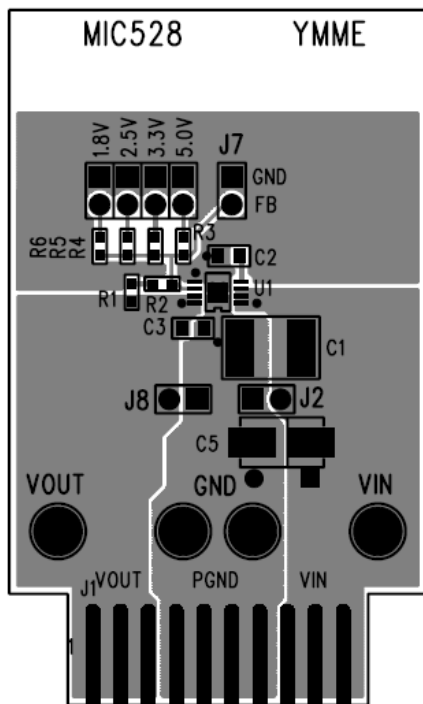
Notes:

1. TDK: www.tdk.com.
2. AVX.: www.avx.com.
3. Panasonic: www.panasonic.com.
4. Vishay Tel: www.vishay.com.
5. **Micrel, Inc.:** www.micrel.com.

PCB Evaluation Board Layout

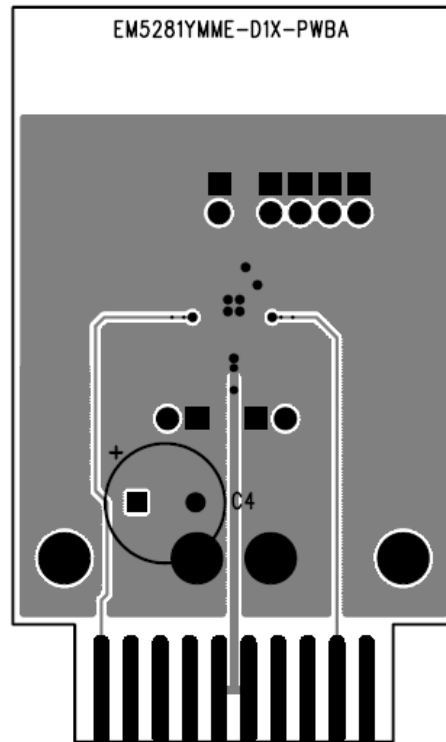


Top Layer Silk Screen

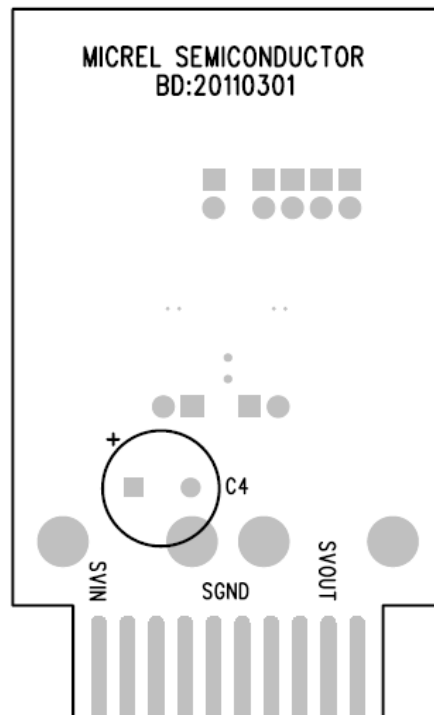


Top Layer Traces

PCB Evaluation Board Layout (Continued)

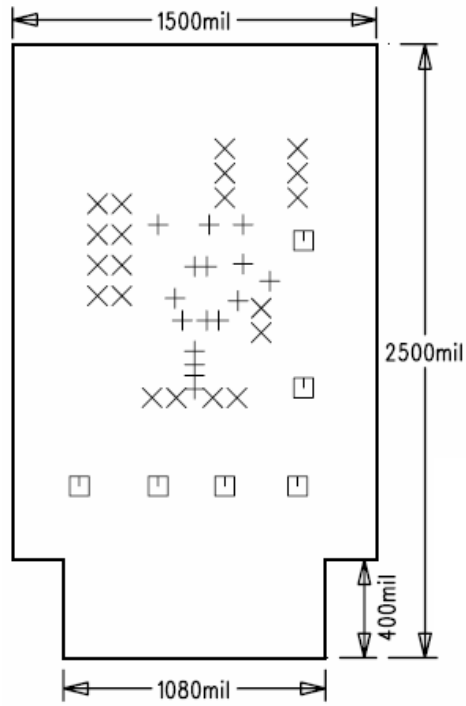


Bottom Layer Traces



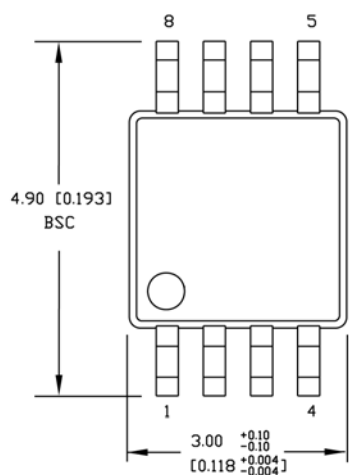
Bottom Layer Silk Screen

PCB Evaluation Board Layout (Continued)

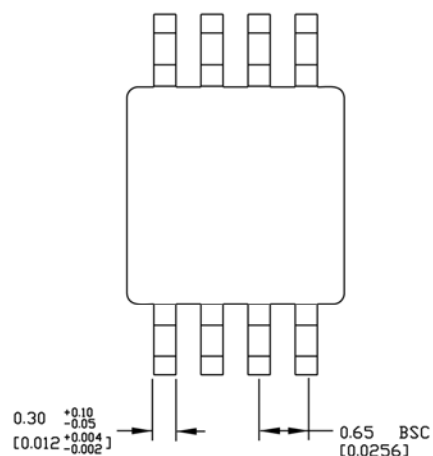


EV Board Dimensions

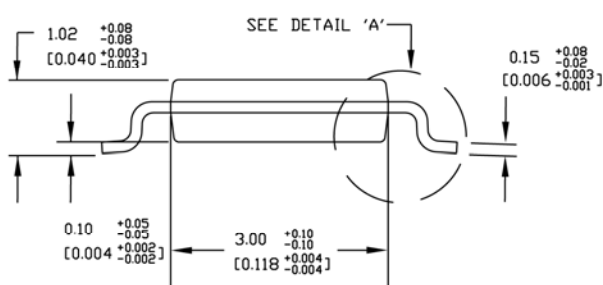
Package Information



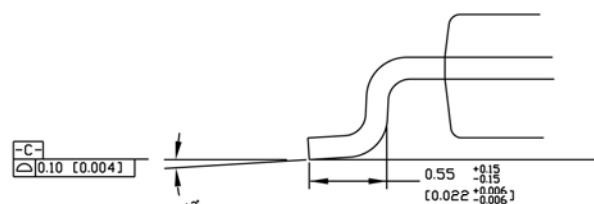
TOP VIEW



BOTTOM VIEW



SIDE VIEW



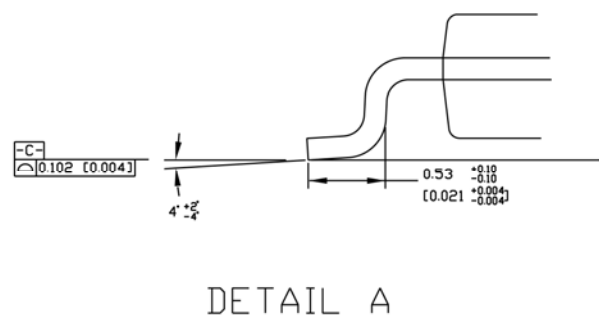
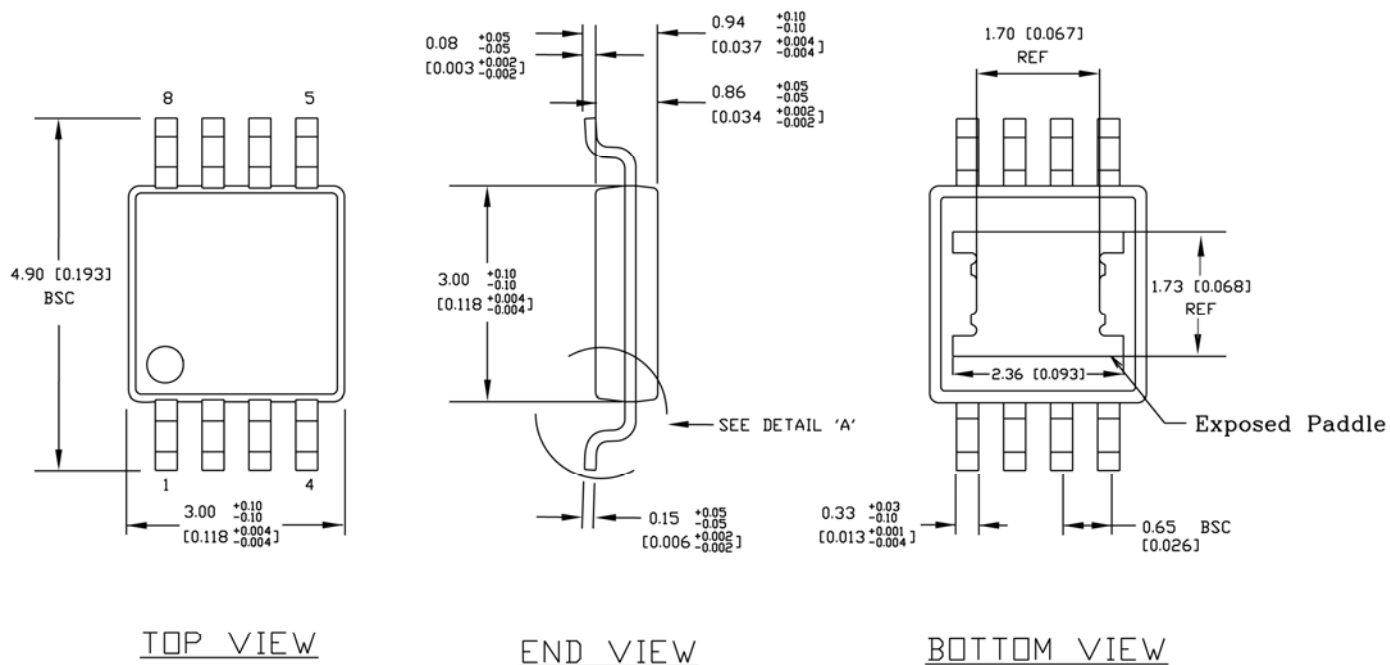
DETAIL A

NOTES:

1. DIMENSIONS ARE IN MM [INCHES].
2. CONTROLLING DIMENSION: MM
3. DIMENSION DOES NOT INCLUDE MOLD FLASH OR PROTRUSIONS, EITHER OF WHICH SHALL NOT EXCEED 0.20 [0.008] PER SIDE.

8-Pin MSOP

Package Information (Continued)

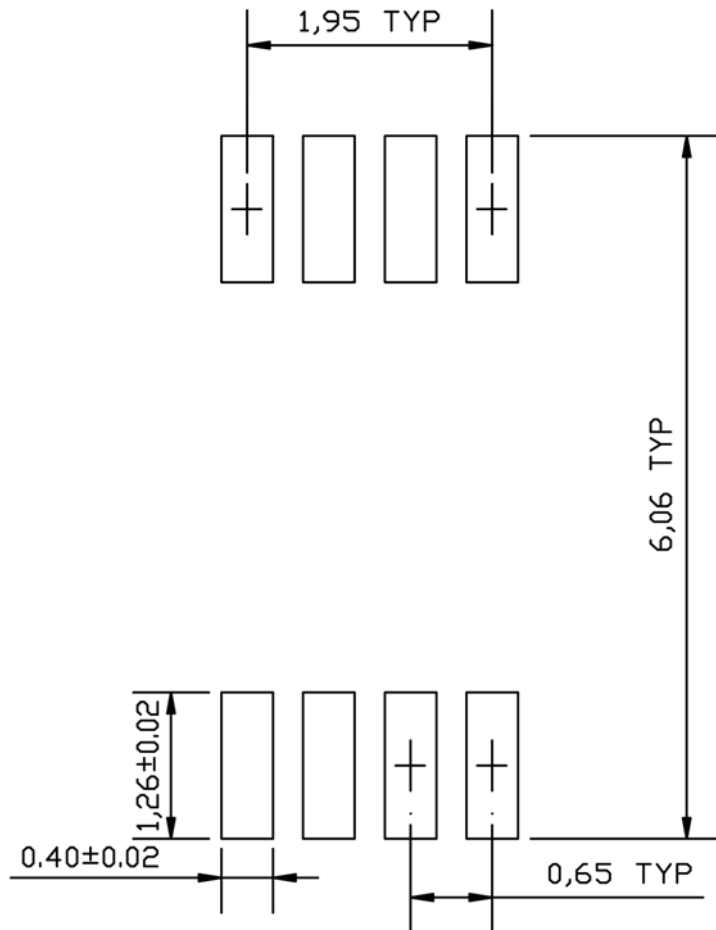


- NOTES:
1. DIMENSIONS ARE IN MM [INCHES].
 2. CONTROLLING DIMENSION: MM
 3. DIMENSION DOES NOT INCLUDE MOLD FLASH OR PROTRUSIONS, EITHER OF WHICH SHALL NOT EXCEED 0.20 [0.008] PER SIDE.

8-Pin Exposed Pad (ePad) MSOP

Recommended Landing Pattern

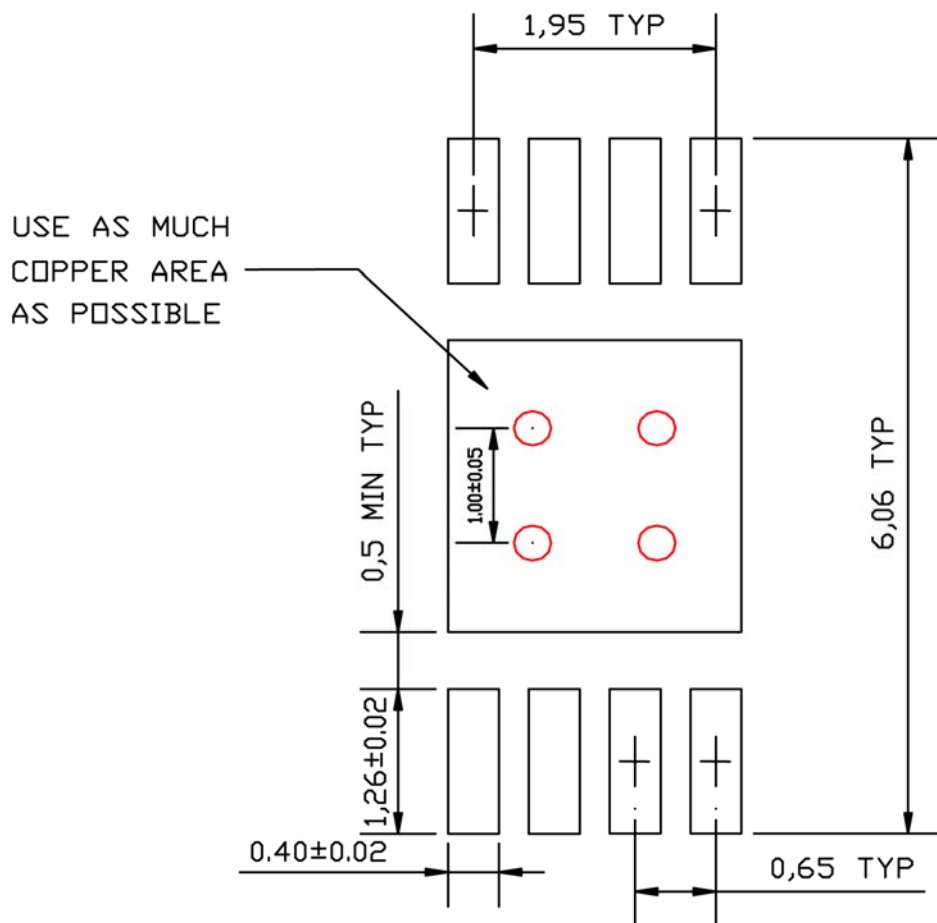
LP # **MSOP-8LD-LP-1**
 All units are in mm
 Tolerance ± 0.05 if not noted



8-Pin MSOP

Recommended Landing Pattern (Continued)

LP # **MSOPEP-8LD-LP-1**
 All units are in mm
 Tolerance ± 0.05 if not noted



8-Pin Exposed Pad (ePad) MSOP

Red circle indicates Thermal Via. Size should be .300mm – .350mm in diameter, 1.00mm pitch, and it should be connected to GND plane for maximum thermal performance.

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JONHON

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