

# MC14526B

## Presetable 4-Bit Down Counters

The MC14526B binary counter is constructed with MOS P-channel and N-channel enhancement mode devices in a monolithic structure.

This device is presetable, cascable, synchronous down counter with a decoded "0" state output for divide-by-N applications. In single stage applications the "0" output is applied to the Preset Enable input. The Cascade Feedback input allows cascade divide-by-N operation with no additional gates required. The Inhibit input allows disabling of the pulse counting function. Inhibit may also be used as a negative edge clock.

This complementary MOS counter can be used in frequency synthesizers, phase-locked loops, and other frequency division applications requiring low power dissipation and/or high noise immunity.

### Features

- Supply Voltage Range = 3.0 Vdc to 18 Vdc
- Logic Edge-Clocked Design: Incremented on Positive Transition of Clock or Negative Transition of Inhibit
- Asynchronous Preset Enable
- Capable of Driving Two Low-Power TTL Loads or One Low-Power Schottky TTL Load Over the Rated Temperature Range
- These Devices are Pb-Free and are RoHS Compliant

### MAXIMUM RATINGS

| Rating                                            | Symbol            | Value                  | Unit |
|---------------------------------------------------|-------------------|------------------------|------|
| DC Supply Voltage Range                           | $V_{DD}$          | -0.5 to +18.0          | V    |
| Input or Output Voltage Range (DC or Transient)   | $V_{in}, V_{out}$ | -0.5 to $V_{DD} + 0.5$ | V    |
| Input or Output Current (DC or Transient) per Pin | $I_{in}, I_{out}$ | $\pm 10$               | mA   |
| Power Dissipation per Package (Note 1)            | $P_D$             | 500                    | mW   |
| Operating Temperature Range                       | $T_A$             | -55 to +125            | °C   |
| Storage Temperature Range                         | $T_{stg}$         | -65 to +150            | °C   |
| Lead Temperature (8-Second Soldering)             | $T_L$             | 260                    | °C   |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### 1. Temperature Derating:

Plastic "P and D/DW" Packages: - 7.0 mW/°C From 65°C To 125°C

This device contains protection circuitry to guard against damage due to high static voltages or electric fields. However, precautions must be taken to avoid applications of any voltage higher than maximum rated voltages to this high-impedance circuit. For proper operation,  $V_{in}$  and  $V_{out}$  should be constrained to the range  $V_{SS} \leq (V_{in} \text{ or } V_{out}) \leq V_{DD}$ .

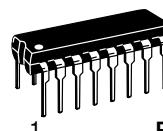
Unused inputs must always be tied to an appropriate logic voltage level (e.g., either  $V_{SS}$  or  $V_{DD}$ ). Unused outputs must be left open.



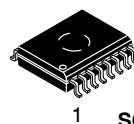
ON Semiconductor®

<http://onsemi.com>

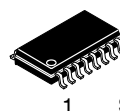
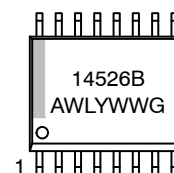
### MARKING DIAGRAMS



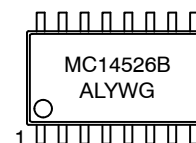
PDIP-16  
P SUFFIX  
CASE 648



SOIC-16 WB  
DW SUFFIX  
CASE 751G



SOEIAJ-16  
F SUFFIX  
CASE 966



A = Assembly Location  
WL, L = Wafer Lot  
YY, Y = Year  
WW, W = Work Week  
G = Pb-Free Package

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 8 of this data sheet.

FUNCTION TABLE

| Inputs |       |         |               |                  | Output | Resulting Function          |
|--------|-------|---------|---------------|------------------|--------|-----------------------------|
| Clock  | Reset | Inhibit | Preset Enable | Cascade Feedback | "0"    |                             |
| X      | H     | X       | L             | L                | L      | Asynchronous reset*         |
| X      | H     | X       | H             | L                | H      | Asynchronous reset          |
| X      | H     | X       | X             | H                | H      | Asynchronous reset          |
| X      | L     | X       | H             | X                | L      | Asynchronous preset         |
| L      | L     | H       | L             | X                | L      | Decrement inhibited         |
|        | L     |         | L             | X                | L      | Decrement inhibited         |
| H      | L     | L       | L             | L                | L      | No change** (inactive edge) |
|        | L     |         | L             | L                | L      | No change** (inactive edge) |
|        | L     | L       | L             | L                | L      | Decrement**                 |
|        | L     |         | L             | L                | L      | Decrement**                 |

X = Don't Care

NOTES:

\* Output "0" is low when reset goes high only if PE and CF are low.

\*\* Output "0" is high when reset is low, only if CF is high and count is 0000.

## PIN DESCRIPTIONS

**Preset Enable (Pin 3)** — If Reset is low, a high level on the Preset Enable input asynchronously loads the counter with the programmed values on P0, P1, P2, and P3.

**Inhibit (Pin 4)** — A high level on the Inhibit input prevents the Clock from decrementing the counter. With Clock (pin 6) held high, Inhibit may be used as a negative edge clock input.

**Clock (Pin 6)** — The counter decrements by one for each rising edge of Clock. See the Function Table for level requirements on the other inputs.

**Reset (Pin 10)** — A high level on Reset asynchronously forces Q0, Q1, Q2, and Q3 low and, if Cascade Feedback is high, causes the "0" output to go high.

**"0" (Pin 12)** — The "0" (Zero) output issues a pulse one clock period wide when the counter reaches terminal count (Q0 = Q1 = Q2 = Q3 = low) if Cascade Feedback is high and Preset Enable is low. When presetting the counter to a value

other than all zeroes, the "0" output is valid after the rising edge of Preset Enable (when Cascade Feedback is high). See the Function Table.

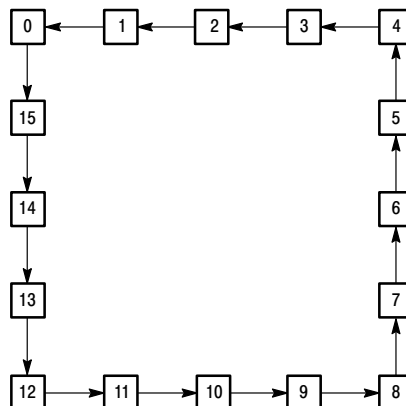
**Cascade Feedback (Pin 13)** — If the Cascade Feedback input is high, a high level is generated at the "0" output when the count is all zeroes. If Cascade Feedback is low, the "0" output depends on the Preset Enable input level. See the Function Table.

**P0, P1, P2, P3 (Pins 5, 11, 14, 2)** — These are the preset data inputs. P0 is the LSB.

**Q0, Q1, Q2, Q3 (Pins 7, 9, 15, 1)** — These are the synchronous counter outputs. Q0 is the LSB.

**V<sub>SS</sub> (Pin 8)** — The most negative power supply potential. This pin is usually ground.

**V<sub>DD</sub> (Pin 16)** — The most positive power supply potential. V<sub>DD</sub> may range from 3.0 to 18 V with respect to V<sub>SS</sub>.

STATE DIAGRAM  
MC14526B

# MC14526B

## ELECTRICAL CHARACTERISTICS (Voltages Referenced to $V_{SS}$ )

| Characteristic                                                                                                                | Symbol          | V <sub>DD</sub><br>Vdc | –55°C |      | 25°C  |                 |      | 125°C |      | Unit |
|-------------------------------------------------------------------------------------------------------------------------------|-----------------|------------------------|-------|------|-------|-----------------|------|-------|------|------|
|                                                                                                                               |                 |                        | Min   | Max  | Min   | Typ<br>(Note 2) | Max  | Min   | Max  |      |
| Output Voltage<br>V <sub>in</sub> = V <sub>DD</sub> or 0                                                                      | V <sub>OL</sub> | 5.0                    | –     | 0.05 | –     | 0               | 0.05 | –     | 0.05 | Vdc  |
| V <sub>in</sub> = 0 or V <sub>DD</sub>                                                                                        |                 | 10                     | –     | 0.05 | –     | 0               | 0.05 | –     | 0.05 |      |
|                                                                                                                               |                 | 15                     | –     | 0.05 | –     | 0               | 0.05 | –     | 0.05 |      |
| Output Voltage<br>V <sub>in</sub> = V <sub>DD</sub> or 0                                                                      | V <sub>OH</sub> | 5.0                    | 4.95  | –    | 4.95  | 5.0             | –    | 4.95  | –    | Vdc  |
| V <sub>in</sub> = 0 or V <sub>DD</sub>                                                                                        |                 | 10                     | 9.95  | –    | 9.95  | 10              | –    | 9.95  | –    |      |
|                                                                                                                               |                 | 15                     | 14.95 | –    | 14.95 | 15              | –    | 14.95 | –    |      |
| Input Voltage<br>(V <sub>O</sub> = 4.5 or 0.5 Vdc)<br>(V <sub>O</sub> = 9.0 or 1.0 Vdc)<br>(V <sub>O</sub> = 13.5 or 1.5 Vdc) | V <sub>IL</sub> | 5.0                    | –     | 1.5  | –     | 2.25            | 1.5  | –     | 1.5  | Vdc  |
|                                                                                                                               |                 | 10                     | –     | 3.0  | –     | 4.50            | 3.0  | –     | 3.0  |      |
|                                                                                                                               |                 | 15                     | –     | 4.0  | –     | 6.75            | 4.0  | –     | 4.0  |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |
|                                                                                                                               |                 |                        |       |      |       |                 |      |       |      |      |

2. Data labelled “Typ” is not to be used for design purposes but is intended as an indication of the IC’s potential performance.

3. The formulas given are for the typical characteristics only at 25°C.

4. To calculate total supply current at loads other than 50 pF:

$$I_T(C_L) = I_T(50 \text{ pF}) + (C_L - 50) Vfk$$

where:  $I_T$  is in μA (per package),  $C_L$  in pF,  $V = (V_{DD} - V_{SS})$  in volts,  $f$  in kHz is input frequency, and  $k = 0.001$ .

# MC14526B

## SWITCHING CHARACTERISTICS ( $C_L = 50 \text{ pF}$ , $T_A = 25^\circ\text{C}$ ) (Note 5)

| Characteristic                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | Symbol                                       | $V_{DD}$        | Min               | Typ<br>(Note 6)       | Max                | Unit          |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------|-----------------|-------------------|-----------------------|--------------------|---------------|
| Output Rise and Fall Time<br>$t_{TLH}, t_{THL} = (1.5 \text{ ns/pF}) C_L + 25 \text{ ns}$<br>$t_{TLH}, t_{THL} = (0.75 \text{ ns/pF}) C_L + 12.5 \text{ ns}$<br>$t_{TLH}, t_{THL} = (0.55 \text{ ns/pF}) C_L + 9.5 \text{ ns}$                                                                                                                                                                                                                                                                                         | $t_{TLH},$<br>$t_{THL}$<br>(Figures 4, 5)    | 5.0<br>10<br>15 | –<br>–<br>–       | 100<br>50<br>40       | 200<br>100<br>80   | ns            |
| Propagation Delay Time (Inhibit Used as Negative Edge Clock)<br>Clock or Inhibit to Q<br>$t_{PLH}, t_{PHL} = (1.7 \text{ ns/pF}) C_L + 465 \text{ ns}$<br>$t_{PLH}, t_{PHL} = (0.66 \text{ ns/pF}) C_L + 197 \text{ ns}$<br>$t_{PLH}, t_{PHL} = (0.5 \text{ ns/pF}) C_L + 135 \text{ ns}$<br>Clock or Inhibit to "0"<br>$t_{PLH}, t_{PHL} = (1.7 \text{ ns/pF}) C_L + 155 \text{ ns}$<br>$t_{PLH}, t_{PHL} = (0.66 \text{ ns/pF}) C_L + 87 \text{ ns}$<br>$t_{PLH}, t_{PHL} = (0.5 \text{ ns/pF}) C_L + 65 \text{ ns}$ | $t_{PLH},$<br>$t_{PHL}$<br>(Figures 4, 5, 6) | 5.0<br>10<br>15 | –<br>–<br>–       | 550<br>225<br>160     | 1100<br>450<br>320 | ns            |
|                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |                                              | 5.0<br>10<br>15 | –<br>–<br>–       | 240<br>130<br>100     | 480<br>260<br>200  |               |
| Propagation Delay Time<br>Pn to Q                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | $t_{PLH},$<br>$t_{PHL}$<br>(Figures 4, 7)    | 5.0<br>10<br>15 | –<br>–<br>–       | 260<br>120<br>100     | 520<br>240<br>200  | ns            |
| Propagation Delay Time<br>Reset to Q                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | $t_{PHL}$<br>(Figure 8)                      | 5.0<br>10<br>15 | –<br>–<br>–       | 250<br>110<br>80      | 500<br>220<br>160  | ns            |
| Propagation Delay Time<br>Preset Enable to "0"                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | $t_{PHL},$<br>$t_{PLH}$<br>(Figures 4, 9)    | 5.0<br>10<br>15 | –<br>–<br>–       | 220<br>100<br>80      | 440<br>200<br>160  | ns            |
| Clock or Inhibit Pulse Width                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           | $t_w$<br>(Figures 5, 6)                      | 5.0<br>10<br>15 | 250<br>100<br>80  | 125<br>50<br>40       | –<br>–<br>–        | ns            |
| Clock Pulse Frequency (with PE = low)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | $f_{max}$<br>(Figures 4, 5, 6)               | 5.0<br>10<br>15 | –<br>–<br>–       | 2.0<br>5.0<br>6.6     | 1.5<br>3.0<br>4.0  | MHz           |
| Clock or Inhibit Rise and Fall Time                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | $t_r,$<br>$t_f$<br>(Figures 5, 6)            | 5.0<br>10<br>15 | –<br>–<br>–       | –<br>–<br>–           | 15<br>5<br>4       | $\mu\text{s}$ |
| Setup Time<br>Pn to Preset Enable                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | $t_{su}$<br>(Figure 1)                       | 5.0<br>10<br>15 | 90<br>50<br>40    | 40<br>15<br>10        | –<br>–<br>–        | ns            |
| Hold Time<br>Preset Enable to Pn                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       | $t_h$<br>(Figure 2)                          | 5.0<br>10<br>15 | 30<br>30<br>30    | – 15<br>– 5<br>0      | –<br>–<br>–        | ns            |
| Preset Enable Pulse Width                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | $t_w$<br>(Figure 3)                          | 5.0<br>10<br>15 | 250<br>100<br>80  | 125<br>50<br>40       | –<br>–<br>–        | ns            |
| Reset Pulse Width                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | $t_w$<br>(Figure 8)                          | 5.0<br>10<br>15 | 350<br>250<br>200 | 175<br>125<br>100     | –<br>–<br>–        | ns            |
| Reset Removal Time                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     | $t_{rem}$<br>(Figure 8)                      | 5.0<br>10<br>15 | 10<br>20<br>30    | – 110<br>– 30<br>– 20 | –<br>–<br>–        | ns            |

5. The formulas given are for the typical characteristics only at  $25^\circ\text{C}$ .

6. Data labelled "Typ" is not to be used for design purposes but is intended as an indication of the IC's potential performance.

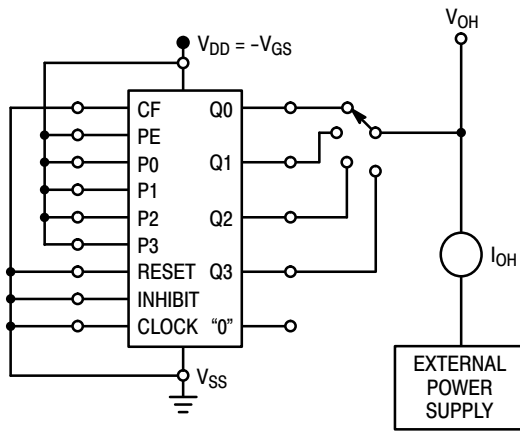


Figure 1. Typical Output Source Characteristics Test Circuit

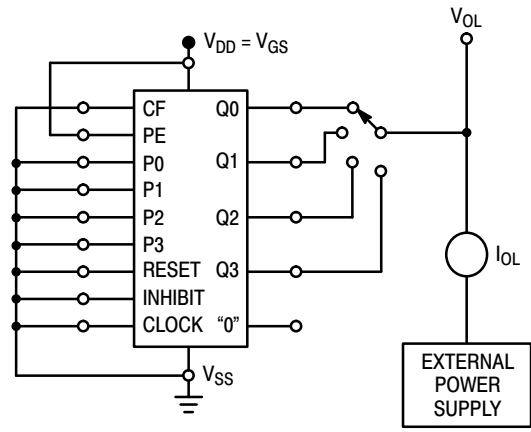


Figure 2. Typical Output Sink Characteristics Test Circuit

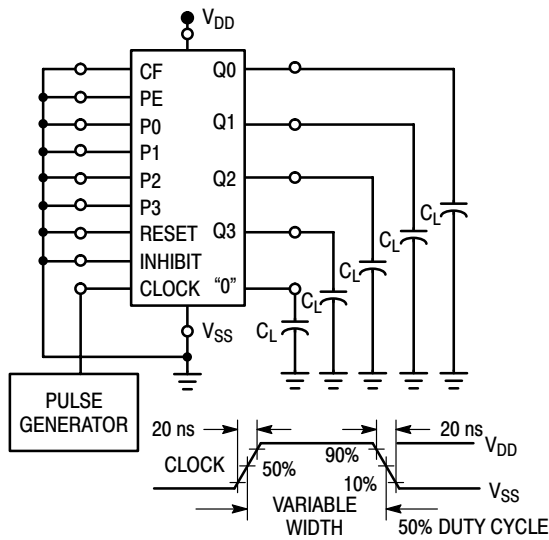
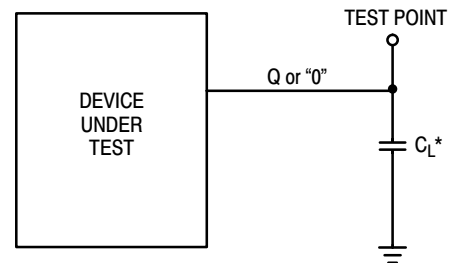


Figure 3. Power Dissipation



\*Includes all probe and jig capacitance.

Figure 4. Test Circuit

SWITCHING WAVEFORMS

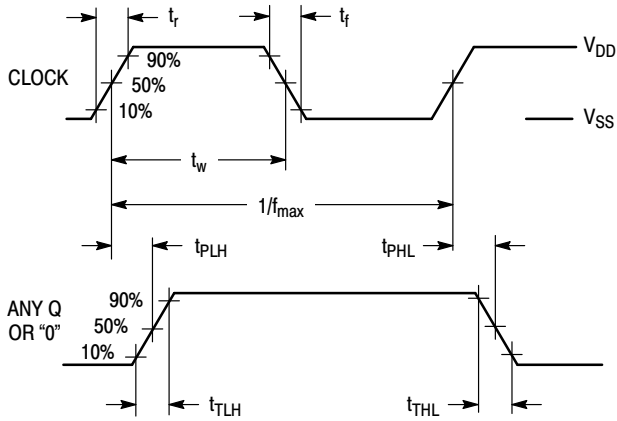


Figure 5.

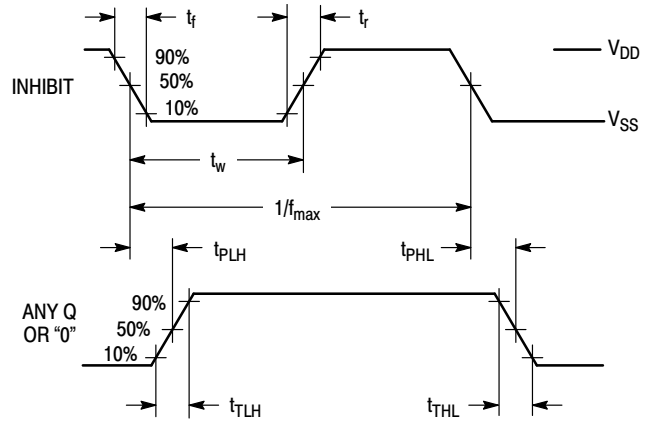


Figure 6.

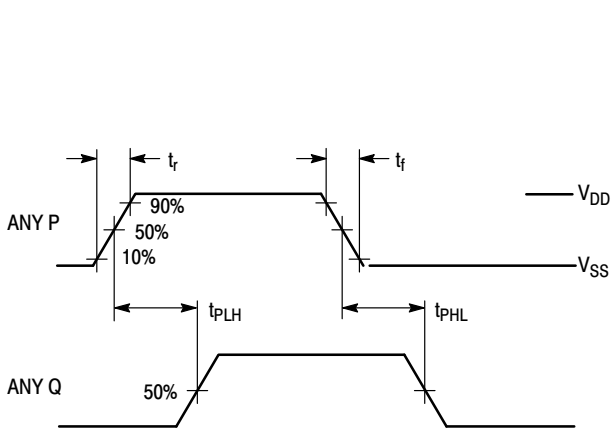


Figure 7.

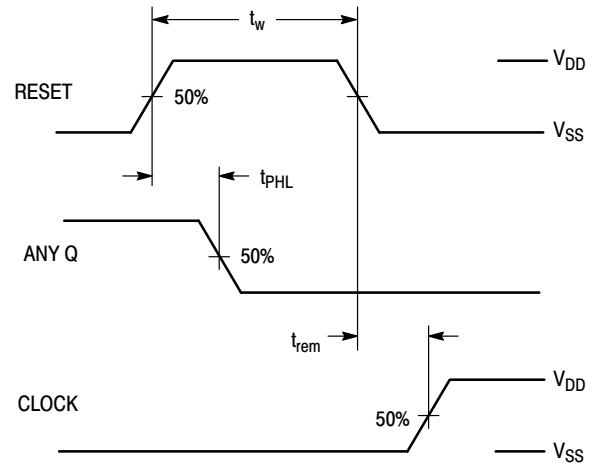


Figure 8.

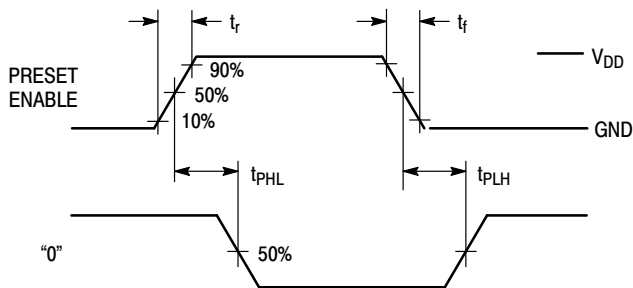


Figure 9.

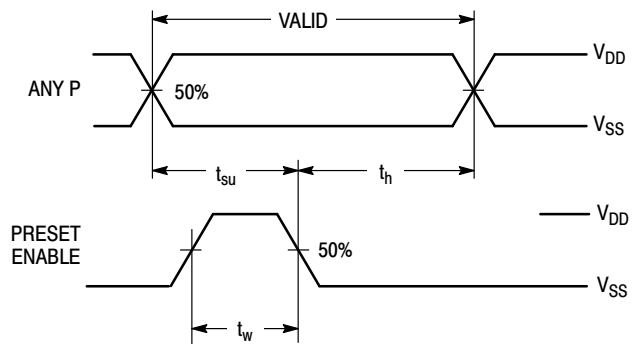
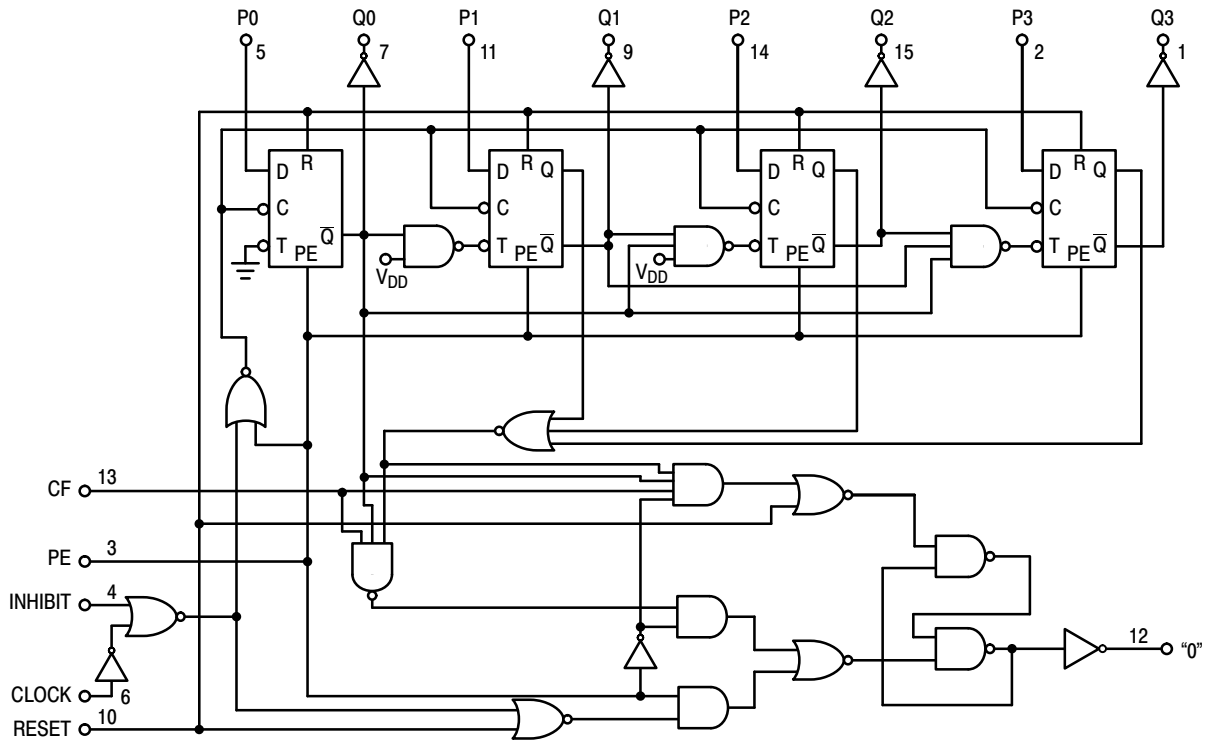


Figure 10.

# MC14526B

## MC14526B LOGIC DIAGRAM (Binary Down Counter)



## APPLICATIONS INFORMATION

### Divide-By-N, Single Stage

Figure 11 shows a single stage divide-by-N application.

To initialize counting a number, N is set on the parallel inputs (P0, P1, P2, and P3) and reset is taken high asynchronously. A zero is forced into the master and slave of each bit and, at the same time, the "0" output goes high. Because Preset Enable is tied to the "0" output, preset is enabled. Reset must be released while the Clock is high so the slaves of each bit may receive N before the Clock goes low. When the Clock goes low and Reset is low, the "0" output goes low (if P0 through P3 are unequal to zero).

The counter downcounts with each rising edge of the Clock. When the counter reaches the zero state, an output pulse occurs on "0" which presets N. The propagation delays from the Clock's rising and falling edges to the "0" output's rising and falling edges are about equal, making the "0" output pulse approximately equal to that of the Clock pulse.

The Inhibit pin may be used to stop pulse counting. When this pin is taken high, decrementing is inhibited.

### Cascaded, Presetable Divide-By-N

Figure 12 shows a three stage cascade application. Taking Reset high loads N. Only the first stage's Reset pin (least significant counter) must be taken high to cause the preset for all stages, but all pins could be tied together, as shown.

When the first stage's Reset pin goes high, the "0" output is latched in a high state. Reset must be released while Clock is high and time allowed for Preset Enable to load N into all stages before Clock goes low.

When Preset Enable is high and Clock is low, time must be allowed for the zero digits to propagate a Cascade Feedback to the first non-zero stage. Worst case is from the most significant bit (M.S.B.) to the L.S.B., when the L.S.B. is equal to one (i.e. N = 1).

After N is loaded, each stage counts down to zero with each rising edge of Clock. When any stage reaches zero and the leading stages (more significant bits) are zero, the "0" output goes high and feeds back to the preceding stage. When all stages are zero, the Preset Enable automatically loads N while the Clock is high and the cycle is renewed.

## MC14526B

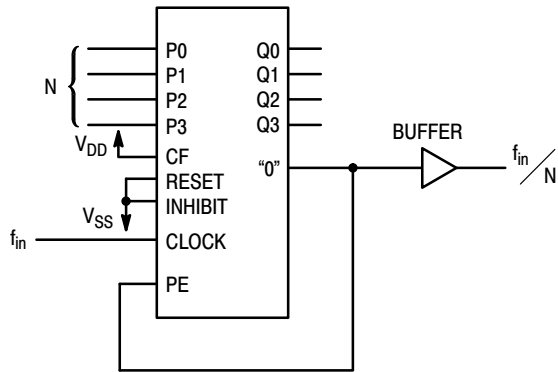


Figure 11.  $\div N$  Counter

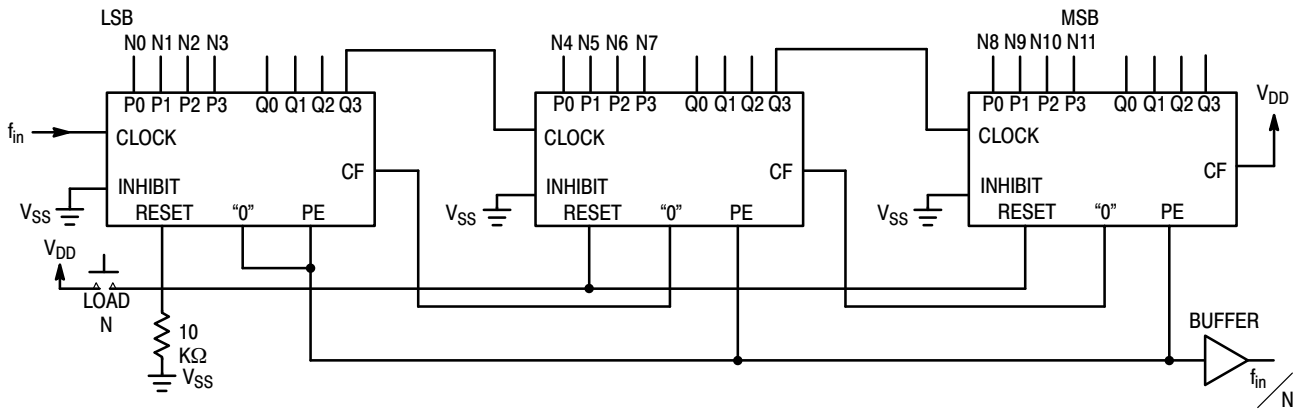


Figure 12. 3 Stages Cascaded

### ORDERING INFORMATION

| Device        | Package                | Shipping <sup>†</sup> |
|---------------|------------------------|-----------------------|
| MC14526BCPG   | PDIP-16<br>(Pb-Free)   | 500 Units / Rail      |
| MC14526BDWG   | SOIC-16<br>(Pb-Free)   | 47 Units / Rail       |
| MC14526BDWR2G | SOIC-16<br>(Pb-Free)   | 1000 / Tape & Reel    |
| MC14526BFG    | SOEIAJ-16<br>(Pb-Free) | 50 Units / Rail       |

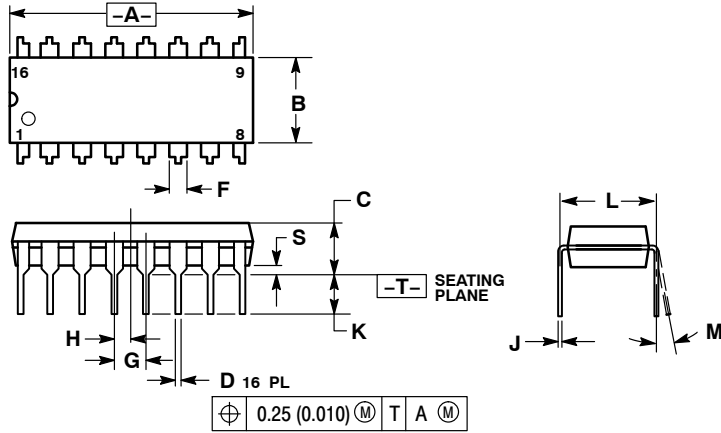
<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



# MC14526B

## PACKAGE DIMENSIONS

PDIP-16  
CASE 648-08  
ISSUE T

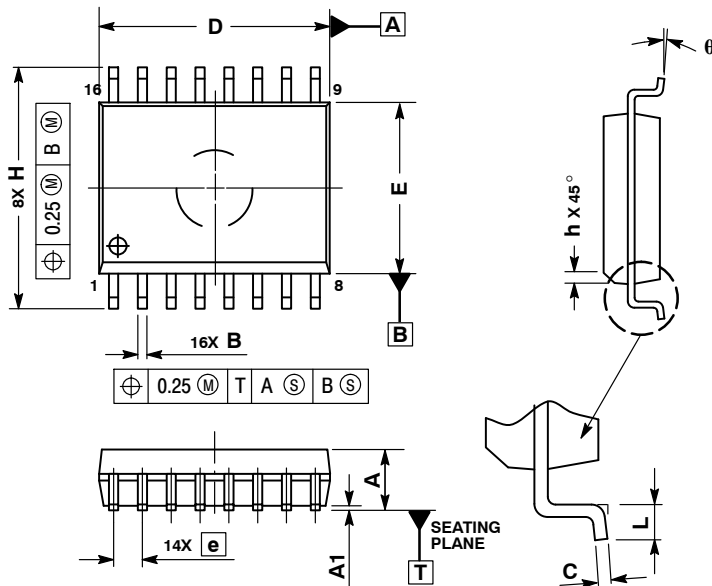


### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
4. DIMENSION B DOES NOT INCLUDE MOLD FLASH.
5. ROUNDED CORNERS OPTIONAL.

| DIM | INCHES    |       | MILLIMETERS |       |
|-----|-----------|-------|-------------|-------|
|     | MIN       | MAX   | MIN         | MAX   |
| A   | 0.740     | 0.770 | 18.80       | 19.55 |
| B   | 0.250     | 0.270 | 6.35        | 6.85  |
| C   | 0.145     | 0.175 | 3.69        | 4.44  |
| D   | 0.015     | 0.021 | 0.39        | 0.53  |
| F   | 0.040     | 0.70  | 1.02        | 1.77  |
| G   | 0.100 BSC |       | 2.54 BSC    |       |
| H   | 0.050 BSC |       | 1.27 BSC    |       |
| J   | 0.008     | 0.015 | 0.21        | 0.38  |
| K   | 0.110     | 0.130 | 2.80        | 3.30  |
| L   | 0.295     | 0.305 | 7.50        | 7.74  |
| M   | 0°        | 10°   | 0°          | 10°   |
| S   | 0.020     | 0.040 | 0.51        | 1.01  |

SOIC-16WB  
CASE 751G-03  
ISSUE C



### NOTES:

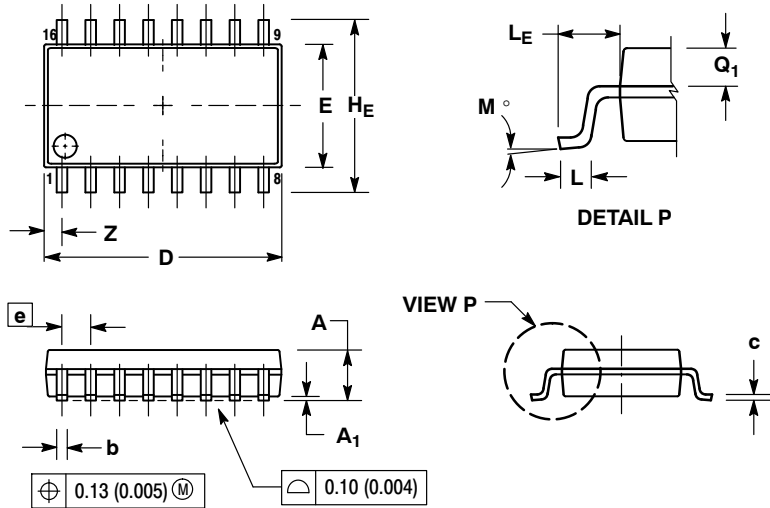
1. DIMENSIONS ARE IN MILLIMETERS.
2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.
3. DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.
5. DIMENSION B DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF THE B DIMENSION AT MAXIMUM MATERIAL CONDITION.

| DIM | MILLIMETERS |       |
|-----|-------------|-------|
|     | MIN         | MAX   |
| A   | 2.35        | 2.65  |
| A1  | 0.10        | 0.25  |
| B   | 0.35        | 0.49  |
| C   | 0.23        | 0.32  |
| D   | 10.15       | 10.45 |
| E   | 7.40        | 7.60  |
| e   | 1.27 BSC    |       |
| H   | 10.05       | 10.55 |
| h   | 0.25        | 0.75  |
| L   | 0.50        | 0.90  |
| q   | 0°          | 7°    |

# MC14526B

## PACKAGE DIMENSIONS

SOEIAJ-16  
CASE 966-01  
ISSUE A



### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS AND ARE MEASURED AT THE PARTING LINE. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
4. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
5. THE LEAD WIDTH DIMENSION (b) DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE LEAD WIDTH DIMENSION AT MAXIMUM MATERIAL CONDITION. DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OR THE FOOT. MINIMUM SPACE BETWEEN PROTRUSIONS AND ADJACENT LEAD TO BE 0.46 (0.018).

| DIM            | MILLIMETERS |       | INCHES    |       |
|----------------|-------------|-------|-----------|-------|
|                | MIN         | MAX   | MIN       | MAX   |
| A              | ---         | 2.05  | ---       | 0.081 |
| A <sub>1</sub> | 0.05        | 0.20  | 0.002     | 0.008 |
| b              | 0.35        | 0.50  | 0.014     | 0.020 |
| c              | 0.10        | 0.20  | 0.007     | 0.011 |
| D              | 9.90        | 10.50 | 0.390     | 0.413 |
| E              | 5.10        | 5.45  | 0.201     | 0.215 |
| e              | 1.27 BSC    |       | 0.050 BSC |       |
| H <sub>E</sub> | 7.40        | 8.20  | 0.291     | 0.323 |
| L              | 0.50        | 0.85  | 0.020     | 0.033 |
| L <sub>E</sub> | 1.10        | 1.50  | 0.043     | 0.059 |
| M              | 0 °         | 10 °  | 0 °       | 10 °  |
| Q <sub>1</sub> | 0.70        | 0.90  | 0.028     | 0.035 |
| Z              | ---         | 0.78  | ---       | 0.031 |

ECLinPS is a trademark of Semiconductor Components Industries, LLC (SCILLC).

ON Semiconductor and  are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## PUBLICATION ORDERING INFORMATION

### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor  
P.O. Box 5163, Denver, Colorado 80217 USA  
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada  
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada  
Email: [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

N. American Technical Support: 800-282-9855 Toll Free  
USA/Canada

Europe, Middle East and Africa Technical Support:  
Phone: 421 33 790 2910

Japan Customer Focus Center  
Phone: 81-3-5773-3850

ON Semiconductor Website: [www.onsemi.com](http://www.onsemi.com)

Order Literature: <http://www.onsemi.com/orderlit>

For additional information, please contact your local  
Sales Representative

Компания «Океан Электроники» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Поставка оригинальных импортных электронных компонентов напрямую с производств Америки, Европы и Азии, а так же с крупнейших складов мира;
- Широкая линейка поставок активных и пассивных импортных электронных компонентов (более 30 млн. наименований);
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Помощь Конструкторского Отдела и консультации квалифицированных инженеров;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Поставка электронных компонентов под контролем ВП;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- При необходимости вся продукция военного и аэрокосмического назначения проходит испытания и сертификацию в лаборатории (по согласованию с заказчиком);
- Поставка специализированных компонентов военного и аэрокосмического уровня качества (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Actel, Aeroflex, Peregrine, VPT, Syfer, Eurofarad, Texas Instruments, MS Kennedy, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Компания «Океан Электроники» является официальным дистрибьютором и эксклюзивным представителем в России одного из крупнейших производителей разъемов военного и аэрокосмического назначения «JONHON», а так же официальным дистрибьютором и эксклюзивным представителем в России производителя высокотехнологичных и надежных решений для передачи СВЧ сигналов «FORSTAR».



## JONHON

«JONHON» (основан в 1970 г.)

Разъемы специального, военного и аэрокосмического назначения:

(Применяются в военной, авиационной, аэрокосмической, морской, железнодорожной, горно- и нефтедобывающей отраслях промышленности)

«FORSTAR» (основан в 1998 г.)

ВЧ соединители, коаксиальные кабели,  
кабельные сборки и микроволновые компоненты:

(Применяются в телекоммуникациях гражданского и специального назначения, в средствах связи, РЛС, а так же военной, авиационной и аэрокосмической отраслях промышленности).



Телефон: 8 (812) 309-75-97 (многоканальный)

Факс: 8 (812) 320-03-32

Электронная почта: [ocean@oceanchips.ru](mailto:ocean@oceanchips.ru)

Web: <http://oceanchips.ru/>

Адрес: 198099, г. Санкт-Петербург, ул. Калинина, д. 2, корп. 4, лит. А