

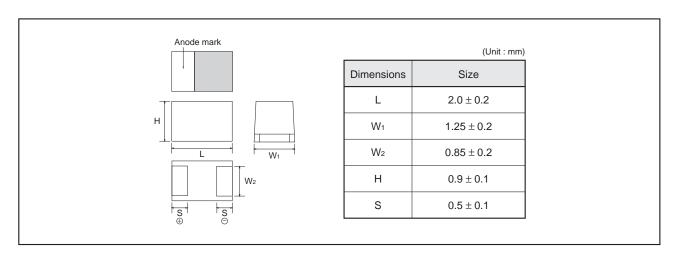
# Conductive polymer chip capacitors (New Bottom surface electrode type : Extra Large capacitance)

TCSO Series PL Case Data sheet

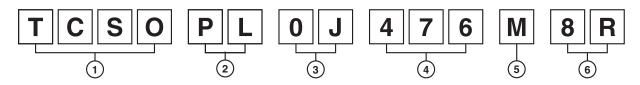
#### Features

- 1) Conductive polymer used at the cathode for ultra-low ESR.
- 2) New package structure results in the largest capacitance.
- 3) Compact, low profile, ultra-high capacitance contribute to smaller, thinner sets with greater functionality.
- 4) Conductive polymer has a self-healing function that prevents failure, resulting in safe, high reliability operation.

#### Dimensions



#### ●Part No. Explanation



1 Series name TCSO

(2) Case style

PL: 2012-10 (0805) Low profile size

3 Rated voltage

Rated voltage (V) 2.5 4 6.3 10 CODE 0E 0G 0J 1A

(4) Nominal capacitance

Nominal capacitance in pF in 3 digits: 2 significant figures followed by the figure representing the number of 0's.

(5) Capacitance tolerance

 $M:\pm 20\%$ 

(6) Taping

8: Tape width

R: Positive electrode on the side opposite to sprocket hole

<sup>\*</sup>This specification has possibility of charge, due to underdevelopment product. Please ask for latest specification to our sales.

#### ●Rated table

(ESR :  $m\Omega$ )

				(LOIX : 11152)			
Capacitance	Rated voltage (V.DC)						
(μF)	2.5	4	6.3	10			
22 (226)							
33 (336)				☆200			
47 (476)			150 / 200				
100 (107)	☆200						

☆ Under development

#### Marking

The indications listed below should be given on the surface of a capacitor.

(1) Polarity : The polarity should be shown by  $\square$  bar. (on the anode side)

(2) Rated DC voltage: A voltage code is shown as below table.

(3) Capacitance : A capacitance code is shown as below table.

Voltage Code	Rated DC Voltage (V)
е	2.5
g	4
j	6.3
А	10

Capacitance Code	Nominal Capacitance (μF)
j	22
n	33
S	47
ā	100

Visual typical example

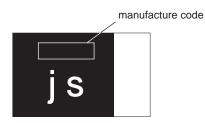
voltage code and capacitance code are variable with parts number.

[PL case]

EX.)

$$\frac{1}{(1)}$$
  $\frac{s}{(2)}$ 

(1) voltage code (2) capacitance code



#### Characteristics

Iter	n	Performance				Test conditions (based on JIS C 5101–1 and JIS C 5101–3)					
	Operating Temperature -55°C to +105°C			Voltage reduction when temperature exceeds +85°C							
Maximum operatemperature wit derating	ating h no voltage	+85	°C								
Rated voltage (	V.DC)	2.5	4 6	.3 1		at 85°C					
Category voltag	e (V.DC)	2 3	.2	5 8		at 105°C					
Surge voltage (	V.DC)	3.2	5 8	3 1	3	at 85°C					
DC Leakage cu	rrent		ll be anda		fied the value on t "	As per 4.	As per 4.9 JIS C 5101-1 As per 4.5.1 JIS C 5101-3 Voltage: Rated voltage for 5min				
Capacitance tol	erance	Sha ±20		satis	fied allowance range.	As per 4.7 JIS C 5101-1 As per 4.5.2 JIS C 5101-3 Measuring frequency: 120±12Hz Measuring voltage: 0.5Vrms +1.5V.DC Measuring circuit: DC equivalent series circuit					
Tangent of loss angle (Df, $\tan \delta$ ) Shall be satisfied the voltage on "Standard list"					As per 4.8 JIS C 5101-1 As per 4.5.3 JIS C 5101-3 Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5V.DC Measuring circuit : DC equivalent series circuit						
ESR Shall be satisfied the value on "Standard list "			As per 4.10 JIS C 5101-1 As per 4.5.4 JIS C 5101-3 Measuring frequency: 100±10kHz Measuring voltage: 0.5Vrms or less Measuring circuit: DC equivalent series circuit								
Resistance to Soldering heat	Appearance				be no significant abnormality.  as should be clear.	As per 4.14 JIS C 5101-1 As per 4.6 JIS C 5101-3					
	L.C.	Les	s tha	n 30	0% of initial limit		Dip in the solder bath Solder temp. : 240±5°C				
	⊿C / C	Witl	nin ±	20%	of initial value	Duration : 10±0.5s Repetition : 1					
	Df (tan $\delta$ )	Les	s tha	n 30	0% of initial limit	After the specimens, leave it at room temperature for over 24h and then measure the sample.					
Temperature cycle	Appearance	There should be no significant abnormality. The indications should be clear.			be no significant abnormality.	As per 4.16 JIS C 5101-1 As per 4.10 JIS C 5101-3					
	L.C.	Les	s tha	n 10	00% of initial limit		Repetition: 5 cycles (1 cycle: steps 1 to 4) without discontinuation.				
	⊿c / c	Witl	nin ±	20%	of initial value	(. 6)6.6	Temp.	Time			
	Df (tan δ)	Les	s tha	n 30	0% of initial limit	1	-55±3°C	30±3min.			
	2. (10.10)			50	-,,-	2	Room temp.	3min. or less			
						3	105±2°C	30±3min.			
						4	Room temp.	3min. or less			
			After the specimens, leave it at room temperature for over 24h and then measure the sample.								
Moisture resistance	Appearance	There should be no significant abnormality. The indications should be clear.				As per 4.	As per 4.22 JIS C 5101-1 As per 4.12 JIS C 5101-3				
	L.C.	Les	s tha	n 30	0% of initial limit			under such atmospheric			
	⊿C / C	Witl	nin +	30/–2	20% of initial value	of initial value condition that the temperature a 40±2°C and 90 to 95% RH, resp					
Df $(\tan \delta)$		Less than 300% of initial limit				leave it at room temperature for over 24h and then measure the sample.					

Iten	n	Performance	Test conditions (based on JIS C 5101-1 and JIS C 5101-3)				
Temperature Temp.		–55°C	As per 4.29 JIS C 5101-1 As per 4.13 JIS C 5101-3				
-	⊿C/C	Within 0/–20% of initial value	AS per 4.13 JIS C 5101-3				
	Df (tan δ)	Shall be satisfied the value on " Standard list "					
L.C. Temp.		-					
		+105°C					
	⊿C / C	Within +50/0% of initial value					
	Df (tan δ)	Shall be satisfied the value on " Standard list "					
	L.C.	Less than 1,000% of initial value					
Surge voltage	Appearance	There should be no significant abnormality. The indications should be clear.	As per 4.26JIS C 5101-1 As per 4.14JIS C 5101-3 Apply the specified surge voltage via the serial resistance of $1k\Omega$ ever $5\pm0.5$ min. for $30\pm5$ s. each time in the atmospheric condition of $85\pm2^{\circ}$ C.				
	L.C.	Less than 200% of initial limit					
	⊿C / C	Within ±20% of initial value	Repeat this procedure 1,000 times.				
	Df (tan δ)	Less than 200% of initial limit	After the specimens, leave it at room temperature for over 24h and then measure the sample.				
Loading at High temperature	Appearance	There should be no significant abnormality. The indications should be clear.	As per 4.23 JIS C 5101-1 As per 4.15 JIS C 5101-3				
	L.C.	Less than 400% of initial limit	After applying the rated voltage for 1000+72/0 h without discontinuation via the serial resistance of 3Ω or less				
	⊿c/c	Within ±20% of initial value	at a temperature of 85±2°C, leave the sample at room				
	Df (tan δ)	Less than 300% of initial limit	temperature / humidity for over 24h and measure the value.				
Terminal	Capacitance	The measured value should be stable.	As per // 35 IIS C 5101-1				
strength	Appearance	There should be no significant abnormality.	As per 4.35 JIS C 5101-1 As per 4.9 JIS C 5101-3 A force is applied to the terminal until it bends to 1mm and by a prescribed tool maintains the condition for 5s.				
Alleria			(See the figure below)  (Unit : mm)  F (Apply force)  thickness=1.6mm				
Adhesiveness		The terminal should not come off.	As per 4.34 JIS C 5101-1 As per 4.8 JIS C 5101-3 Apply force of 5N in the two directions shown in the figure below for 10±1s after mounting the terminal on a circuit board.				
Dimensions		Refer to "External dimensions"	Measure using a caliper of JIS B 7507 Class 2 or higher grade.				
Resistance to solvents		The indication should be clear.	As per 4.32 JIS C 5101-1 As per 4.18 JIS C 5101-3 Dip in the isopropyl alcohol for 30±5s, at room temperature.				
Solderability		3/4 or more surface area of the solder coated terminal dipped in the soldering bath should be covered with the new solder.	As per 4.15.2 JIS C 5101-1 As per 4.7 JIS C 5101-3 Dip speed: 25±2.5mm / s Pre-treatment (accelerated aging): Leave the sample on the boiling distilled water for 1 h. Solder temp.: 245±5°C Duration: 3±0.5s Solder: M705 Flux: Rosin 25% IPA 75%				
Vibration Capacitano		Measure value should not fluctuate during the measurement.	As per 4.17 JIS C 5101-1 Frequency : 10 to 55 to 10Hz/min. Amplitude : 1.5mm				
			Amplitude: 1.5mm				

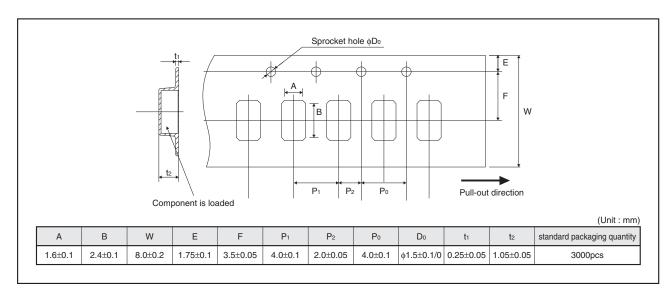
#### Standard products list

Part No.	Rated voltage 85°C	Category voltage 105°C	Surge voltage 85°C	Cap. 120Hz	Tolerance	Leakage current 25°C		Df 120Hz (%)		ESR 100kHz
	(V)	(V)	(V)	(μF)	(%)	1WV.5min (μA)	–55°C	25°C	105°C	(mΩ)
* TCSO PL 0E 107 M8R	25	2	3.2	100	± 20	25.0	15	15	20	200
TCSO PL 0J 476 M8R-ZF	6.3	5	8	47	± 20	29.7	15	15	20	150
TCSO PL 0J 476 M8R-ZD	6.3	5	8	47	± 20	29.7	15	15	20	200
* TCSO PL 1A 336 M8R	10	8	13	33	± 20	33.0	15	15	20	200

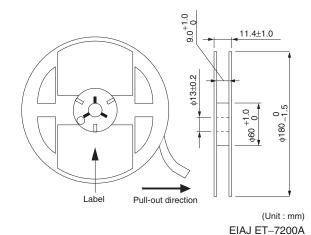
<sup>\* =</sup> Under development

Please contact us for specification of low ESR products.

#### Packaging specifications

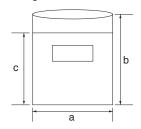


#### ●Reel dimensions



#### ●Damp proof package

- ① One reel is packed in aluminum bag.
  The size of aluminum bag is 240(a) x 250(b)mm.
  The size up to 230(c)mm is to zipper.
- ② A desiccant is packed with a reel.
- The aluminum bag is heat-sealed.
- The label of the same as the label on the reel is placed on the aluminum bag.



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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA	
CLASSⅢ	CLASSⅢ	CLASS II b	CL ACCIII	
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII	

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  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### **Precautions Regarding Application Examples and External Circuits**

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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QR code printed on ROHM Products label is for ROHM's internal use only.

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## TCSOPL0J476M8R-ZF - Web Page

Part Number	TCSOPL0J476M8R-ZF
Package	TCSOPL
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes

## **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ROHM Semiconductor: TCSOPL0J476M8R-ZF1



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