

Product Summary

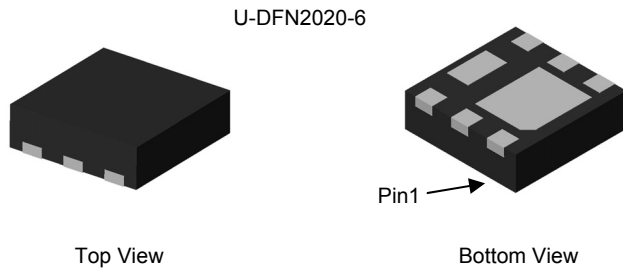
$V_{(BR)DSS}$	$R_{DS(ON)}$ max	I_D max $T_A = +25^\circ C$
60V	16m Ω @ $V_{GS} = 10V$	8.9A
	27m Ω @ $V_{GS} = 4.5V$	6.8A

Description

This new generation MOSFET has been designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Synchronous Rectifiers
- Boost Converters
- Power Management Functions

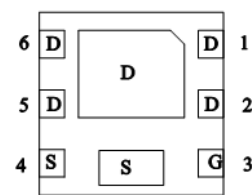


Features and Benefits

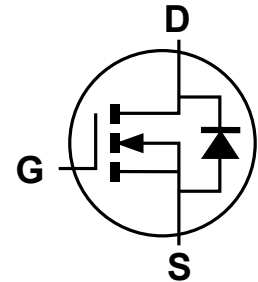
- 100% Unclamped Inductive Switch (UIS) test in production
- 0.6mm profile – ideal for low profile applications
- PCB footprint of 4mm²
- Low On-Resistance
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 ^(e4)
- Weight: 0.007 grams (approximate)



Pin Out
Bottom View



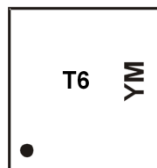
Equivalent Circuit

Ordering Information (Note 4)

Part Number	Marking	Reel size (inches)	Quantity per reel
DMT6016LDFD-7	T6	7	3,000
DMT6016LDFD-13	T6	13	10,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



T6 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: A = 2014)
 M = Month (ex: 9 = September)

Date Code Key

Year	2014	2015	2016	2017	2018	2019	2020
Code	B	C	D	E	F	G	H

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	V_{DSS}	60	V	
Gate-Source Voltage	V_{GSS}	± 20	V	
Continuous Drain Current (Note 6) $V_{GS} = 10\text{V}$	Steady State	$T_A = +25^\circ\text{C}$	8.9	A
		$T_A = +70^\circ\text{C}$	7.1	
	t < 10s	$T_A = +25^\circ\text{C}$	11.1	A
		$T_A = +70^\circ\text{C}$	8.9	
Pulsed Drain Current (10 μs pulse, duty cycle = 1%)	I_{DM}	60	A	
Maximum Body Diode Continuous Current	I_S	2.2	A	
Avalanche Current (Note 7) L = 0.1mH	I_{AS}	15.3	A	
Avalanche Energy (Note 7) L = 0.1mH	E_{AS}	11.7	mJ	

Thermal Characteristics

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	P_D	$T_A = +25^\circ\text{C}$	0.82	W
		$T_A = +70^\circ\text{C}$	0.52	
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	Steady state	153	$^\circ\text{C/W}$
		t < 10s	97	
Total Power Dissipation (Note 6)	P_D	$T_A = +25^\circ\text{C}$	1.9	W
		$T_A = +70^\circ\text{C}$	1.2	
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	Steady state	66	$^\circ\text{C/W}$
		t < 10s	42	
Thermal Resistance, Junction to Case (Note 6)	$R_{\theta JC}$	14.7	$^\circ\text{C}$	
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$	

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	60	—	—	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	μA	$V_{DS} = 48\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	± 100	nA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(th)}$	1.0	—	3.0	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(on)}$	—	—	16	m Ω	$V_{GS} = 10\text{V}, I_D = 10\text{A}$
		—	—	27		$V_{GS} = 4.5\text{V}, I_D = 6\text{A}$
Diode Forward Voltage	V_{SD}	—	0.7	1.2	V	$V_{GS} = 0\text{V}, I_S = 1\text{A}$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C_{iss}	—	864	—	pF	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$ $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	282	—		
Reverse Transfer Capacitance	C_{rss}	—	27.1	—		
Gate Resistance	R_G	—	1.35	—	Ω	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Total Gate Charge ($V_{GS} = 10\text{V}$)	Q_g	—	17	—	nC	$V_{DS} = 30\text{V}, I_D = 10\text{A}$
Total Gate Charge ($V_{GS} = 4.5\text{V}$)	Q_g	—	8.4	—		
Gate-Source Charge	Q_{gs}	—	3.1	—		
Gate-Drain Charge	Q_{gd}	—	4.3	—		
Turn-On Delay Time	$t_{D(on)}$	—	3.4	—	nS	$V_{GS} = 10\text{V}, V_{DD} = 30\text{V}, R_G = 6\Omega,$ $I_D = 10\text{A}$
Turn-On Rise Time	t_r	—	5.2	—		
Turn-Off Delay Time	$t_{D(off)}$	—	12.9	—		
Turn-Off Fall Time	t_f	—	6.8	—		
Body Diode Reverse Recovery Time	t_{rr}	—	22	—	nS	$I_S = 10\text{A}, dI/dt = 100\text{A}/\mu\text{s}$
Body Diode Reverse Recovery Charge	Q_{rr}	—	11.1	—	nC	$I_S = 10\text{A}, dI/dt = 100\text{A}/\mu\text{s}$

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 - I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep $T_J = +25^\circ\text{C}$.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

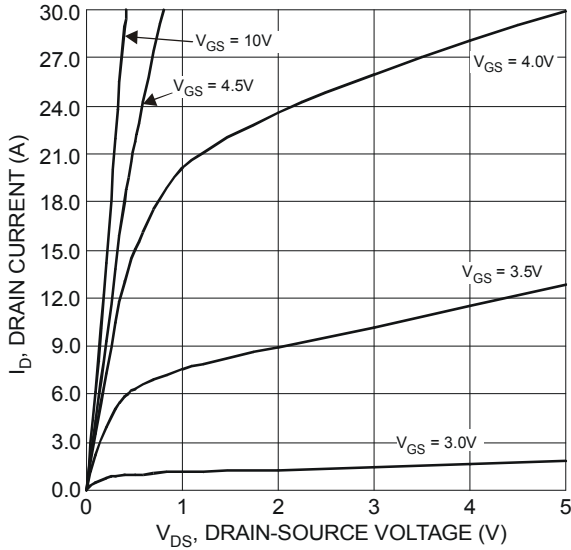


Figure 1 Typical Output Characteristics

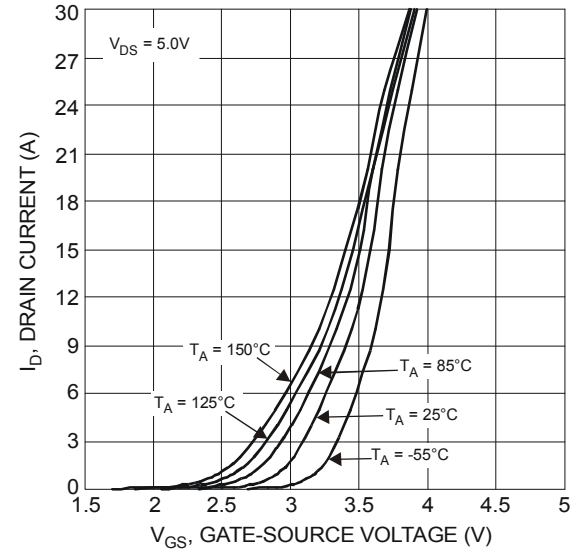


Figure 2 Typical Transfer Characteristics

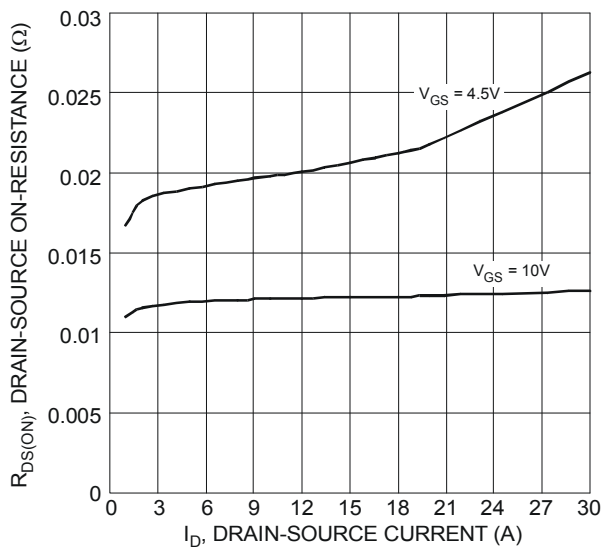


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

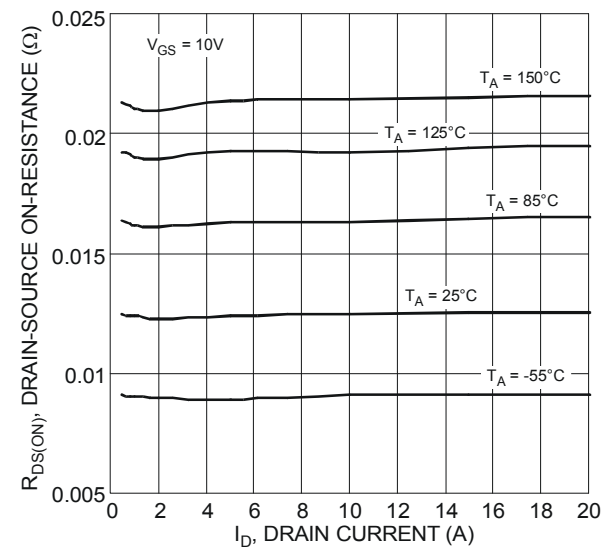


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

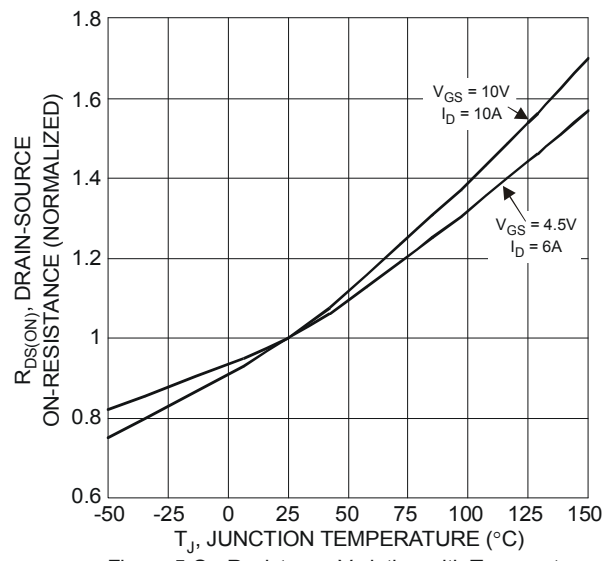


Figure 5 On-Resistance Variation with Temperature

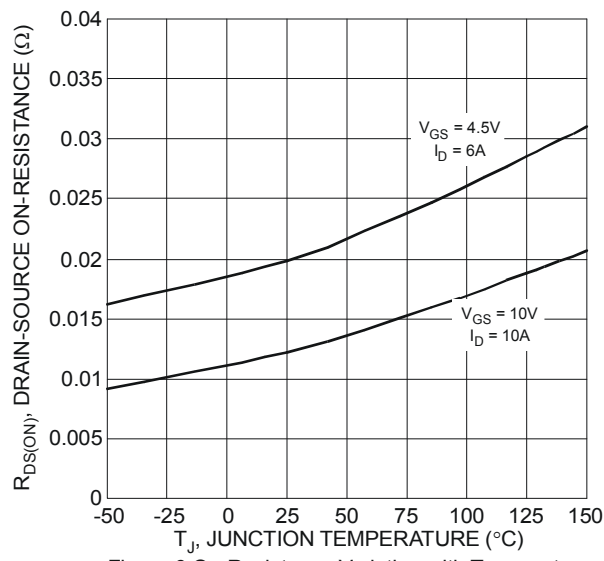


Figure 6 On-Resistance Variation with Temperature

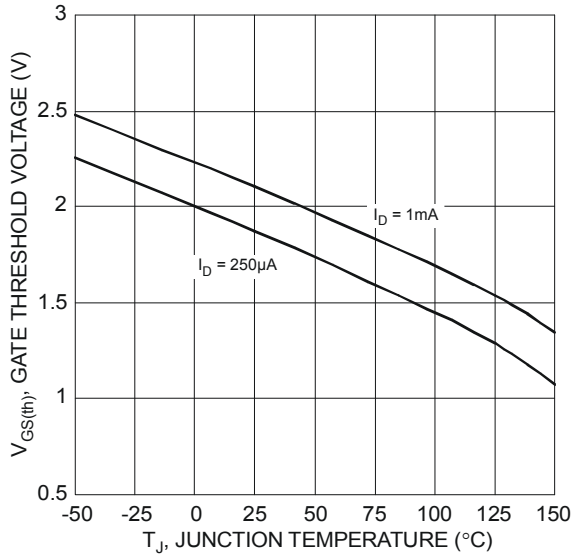


Figure 7 Gate Threshold Variation vs. Ambient Temperature

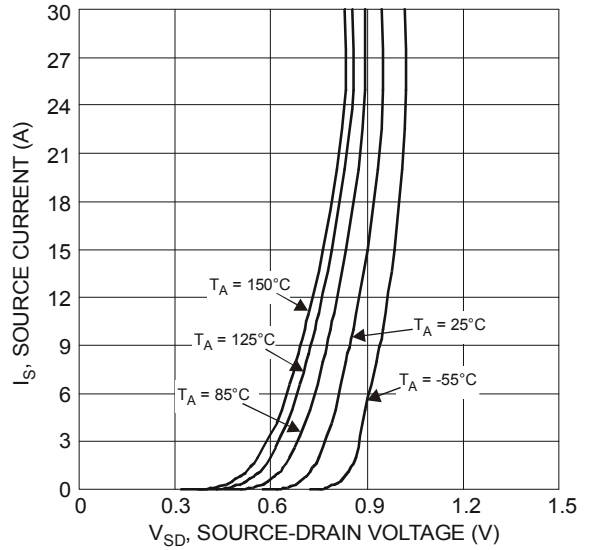


Figure 8 Diode Forward Voltage vs. Current

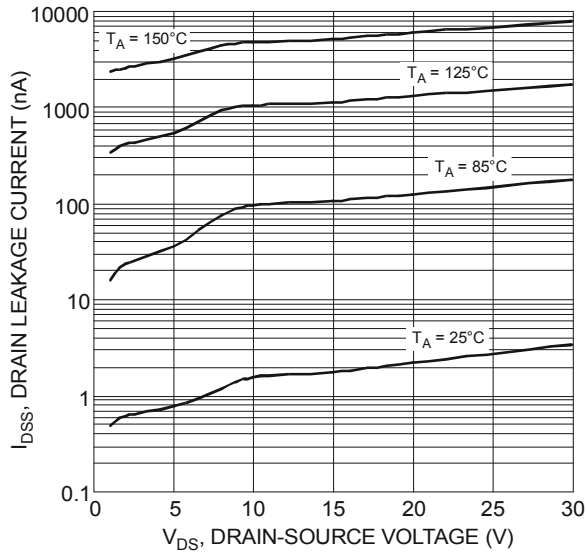


Figure 9 Typical Drain-Source Leakage Current vs. Voltage

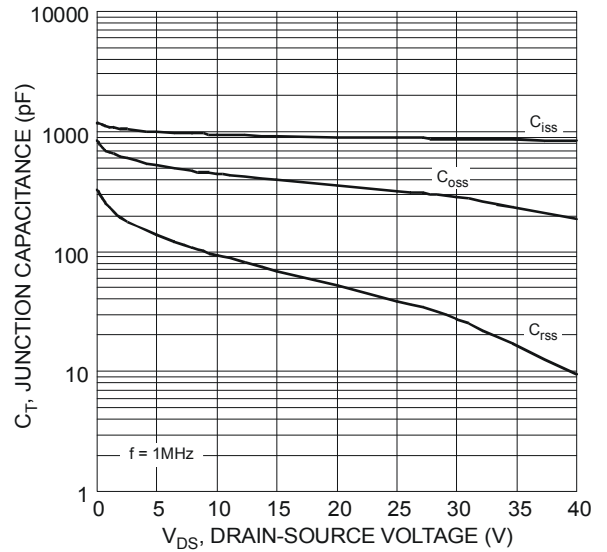


Figure 10 Typical Junction Capacitance

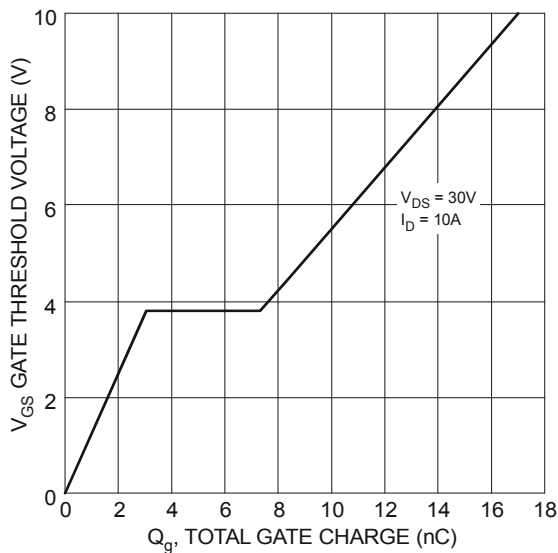


Figure 11 Gate Charge

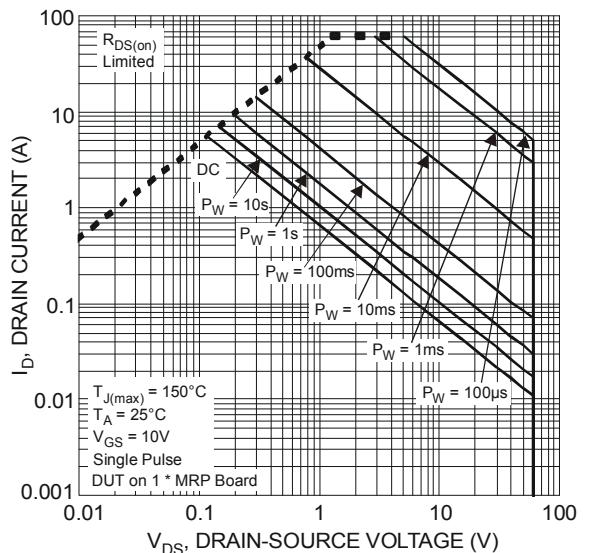


Figure 12 SOA, Safe Operation Area

NEW PRODUCT

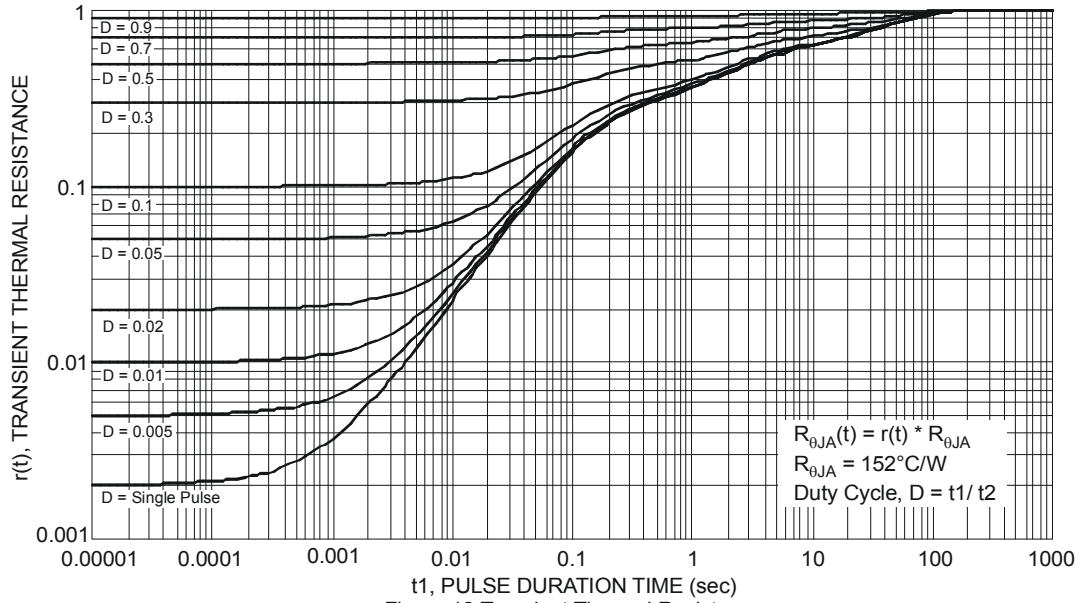
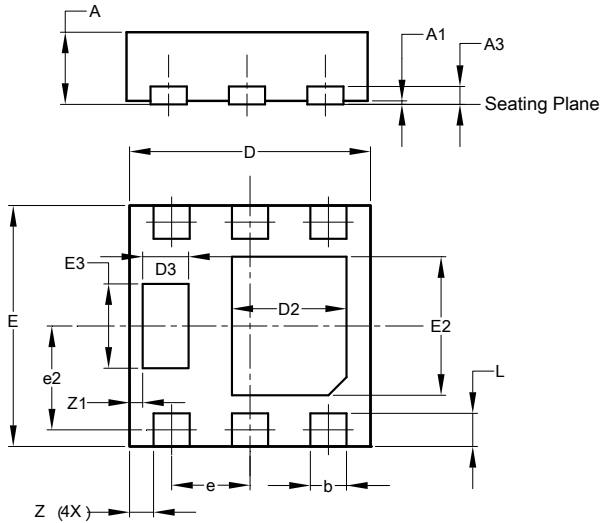


Figure 12 Transient Thermal Resistance

Package Outline Dimensions

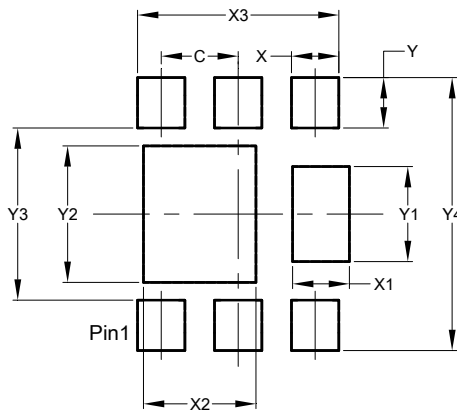
Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for latest version.



U-DFN2020-6			
Dim	Min	Max	Typ
A	0.57	0.63	0.60
A1	0	0.05	0.03
A3	-	-	0.15
b	0.25	0.35	0.30
D	1.95	2.05	2.00
D2	0.85	1.05	0.95
D3	0.33	0.43	0.38
e	0.65 BSC		
e2	0.863 BSC		
E	1.95	2.05	2.00
E2	1.05	1.25	1.15
E3	0.65	0.75	0.70
L	0.225	0.325	0.275
Z	0.20 BSC		
Z1	0.110 BSC		
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	0.650
X	0.400
X1	0.480
X2	0.950
X3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300

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