

Description

The AP2181D and AP2191D are integrated high-side power switches optimized for Universal Serial Bus (USB) and other hot-swap applications. The family of devices complies with USB 2.0 and available with both polarities of Enable input. They offer current and thermal limiting and short circuit protection as well as controlled rise time and under-voltage lockout functionality. A 7ms deglitch capability on the opendrain Flag output prevents false over-current reporting and does not require any external components.

All devices are available in SO-8, MSOP-8, MSOP-8-EP, SOT25, and DFN2018-6 packages.

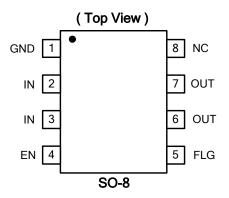
Features

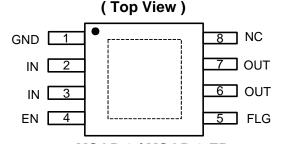
- Single USB port power switches with output discharge
- · Over-current and thermal protection
- 2.1A accurate current limiting
- Fast transient response
- · Reverse Current Blocking
- 90mΩ on-resistance
- Input voltage range: 2.7V 5.5V
- 0.6ms typical rise time
- Very low shutdown current: 1uA (max)
- Fault report (FLG) with blanking time (7ms typ)
- ESD protection: 4KV HBM, 300V MM
- Active high (AP2191D) or active low (AP2181D) enable
- Ambient temperature range -40°C to 85°C
- SO-8, MSOP-8, MSOP-8-EP (Exposed Pad), SOT25, and DFN2018-6: Available in "Green" Molding Compound (No Br, Sb)
- Lead Free Finish / RoHS Compliant (Note 1)
- UL Recognized, File Number E322375 (pending)
- IEC60950-1 CB Scheme Certified

Applications

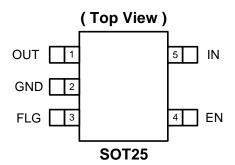
- Consumer electronics LCD TV & Monitor, Game Machines
- Communications Set-Top-Box, GPS, Smartphone
- Computing Laptop, Desktop, Servers, Printers, Docking Station, HUB

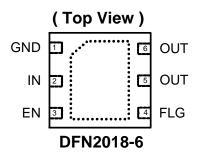
Pin Assignments





MSOP-8 / MSOP-8-EP
Note: latter with exposed pad (dotted line)

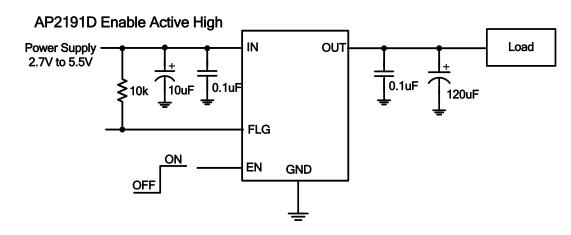




Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/products/lead_free.html.



Typical Application Circuit



Available Options

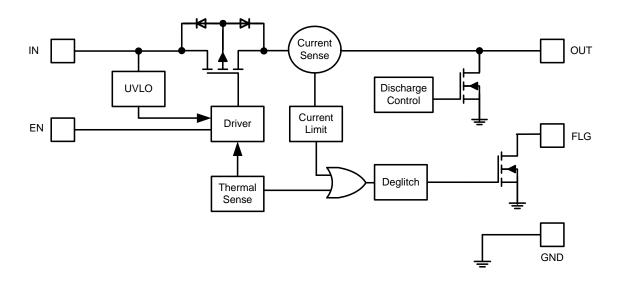
Part Number	Channel	Enable pin (EN)	Current limit (typical)	Recommended maximum continuous load current
AP2181D	1	Active Low	2.1A	1.5A
AP2191D	1	Active High	2.1A	1.5A

Pin Descriptions

	F	in Number		
Pin	SO-8			Deceriptions
Name	MSOP-8	SOT25	DFN2018-6	Descriptions
	MSOP-8-EP			
GND	1	2	1	Ground
IN	2, 3	5	2	Voltage input pin (all IN pins must be tied together externally)
EN	4	4	3	Enable input, active low (AP2181D) or active high (AP2191D)
FLG	5	3	4	Over-current and over-temperature fault report; open-drain flag is active low when triggered
OUT	6, 7	1	5, 6	Voltage output pin (all OUT pins must be tied together externally)
NC	8	N/A	N/A	No internal connection; recommend tie to OUT pins



Functional Block Diagram



Absolute Maximum Ratings

Symbol	Parameter	Ratings	Unit
ESD HBM	ESD HBM Human Body Model ESD Protection		KV
ESD MM	Machine Model ESD Protection	300	V
V _{IN}	Input Voltage	6.5	V
V _{OUT}	Output Voltage	V _{IN} + 0.3	V
V_{EN}, V_{FLG}	Enable Voltage	6.5	V
I _{load}	Maximum Continuous Load Current	Internal Limited	Α
T_{Jmax}	Maximum Junction Temperature	150	°C
T _{ST} Storage Temperature Range (Note 2)		-65 to 150	°C

Notes: 2. UL Recognized Rating from -30°C to 70°C (Diodes qualified T_{ST} from -65°C to 150°C)

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V _{IN}	Input voltage	2.7	5.5	V
l _{OUT}	Output Current	0	1.5	Α
T _A	Operating Ambient Temperature	-40	85	°C



Electrical Characteristics (T_A = 25°C, V_{IN} = +5.0V, unless otherwise stated)

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Unit	
V _{UVLO}	Input UVLO			1.6	1.9	2.5	V	
I _{SHDN}	Input Shutdown Current	Disabled, I _{OU}	_T = 0			0.5	1	μА
IQ	Input Quiescent Current	Enabled, I _{OUT}	= 0			45	70	μΑ
I _{LEAK}	Input Leakage Current	Disabled, OU	T grounded			0.1	1	μΑ
I _{REV}	Reverse Leakage Current	Disabled, V _{IN}	= 0V, V _{OUT} =	5V, I _{REV} at V _{IN}		0.1	1	μΑ
		V _{IN} = 5V,	T _A = 25°C	SOT25, MSOP-8, MSOP-8-EP, SO-8		95	115	
R _{DS(ON)}	Switch on-resistance	I _{OUT} = 1.5A		DFN2018-6		90	110	_
, ,			-40°C ≤ T _A :	≤ 85°C			140	mΩ
			T _A = 25°C			120	140	
		I _{OUT} = 1.5A	-40°C ≤ T _A :	≤ 85°C			170	
I _{SHORT}	Short-Circuit Current Limit	Enabled into	short circuit,	C _L =120μF		2.0		Α
I _{LIMIT}	Over-Load Current Limit	V _{IN} = 5V, V _{OUT}	_Γ = 4V, C _L =12	20μF, -40°C ≤ T _A ≤85°C	1.6	2.1	2.6	Α
I _{Trig}	Current limiting trigger threshold	Output Curre	nt Slew rate	(<100A/s) , C _L =120μF		2.6		Α
V _{IL}	EN Input Logic Low Voltage	$V_{IN} = 2.7V$ to	5.5V				0.8	V
V_{IH}	EN Input Logic High Voltage	V _{IN} = 2.7V to 5.5V		2			V	
I _{SINK}	EN Input leakage	V _{EN} = 5V				1	μΑ	
$T_{D(ON)}$	Output turn-on delay time	$C_L=1\mu F, R_{load}=10\Omega$			0.05		ms	
T_R	Output turn-on rise time	$C_L=1\mu F,\ R_{load}=10\Omega$			0.6	1.5	ms	
$T_{D(OFF)}$	Output turn-off delay time	$C_L=1\mu F,R_{load}=10\Omega$			0.05		ms	
T _F	Output turn-off fall time	$C_L=1\mu F, R_{load}=10\Omega$			0.05	0.1	ms	
R _{FLG}	FLG output FET on-resistance	I _{FLG} =10mA				20	40	Ω
T_{Blank}	FLG blanking time	C _{IN} =10uF, C _L	=22μF		4	7	15	ms
T _{DIS}	Discharge time	$C_L=1\mu F, V_{IN}$	= 5V, disable	ed to V _{OUT} < 0.5V		0.6		ms
R _{DIS}	Discharge resistance (Note 3)	V _{IN} = 5V, disabled, I _{OUT} = 1mA			100		Ω	
T_{SHDN}	Thermal Shutdown Threshold	Enabled, R_{load} =1 $k\Omega$			140		°C	
T _{HYS}	Thermal Shutdown Hysteresis				25		°C	
		SOT25 (Note 4)			170			
	Thermal Resistance Junction-to- Ambient	SO-8 (Note 4)			127		°C/W	
θ_{JA}		MSOP-8 (Note 4)			118			
		MSOP-8-EP (Note 5)			67			
		DFN2018-6 (Note 5)			70			

Notes:

- 3. The discharge function is active when the device is disabled (when enable is de-asserted). The discharge function offers a resistive discharge path for the external storage capacitor.
- 4. Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.
- 5. Device mounted on 2" x 2" FR-4 substrate PCB, 2oz copper, with minimum recommended pad on top layer and thermal vias to bottom layer ground plane.



Typical Performance Characteristics

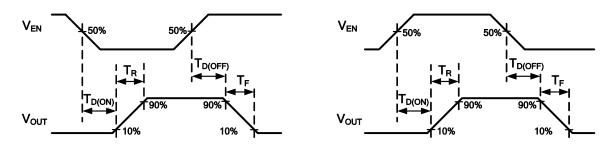
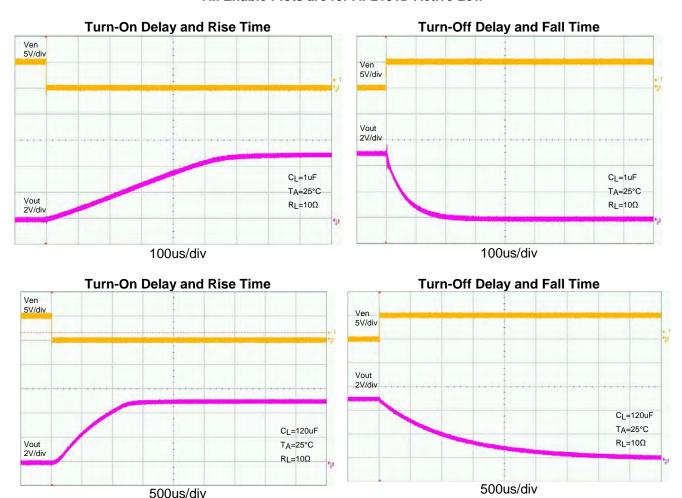


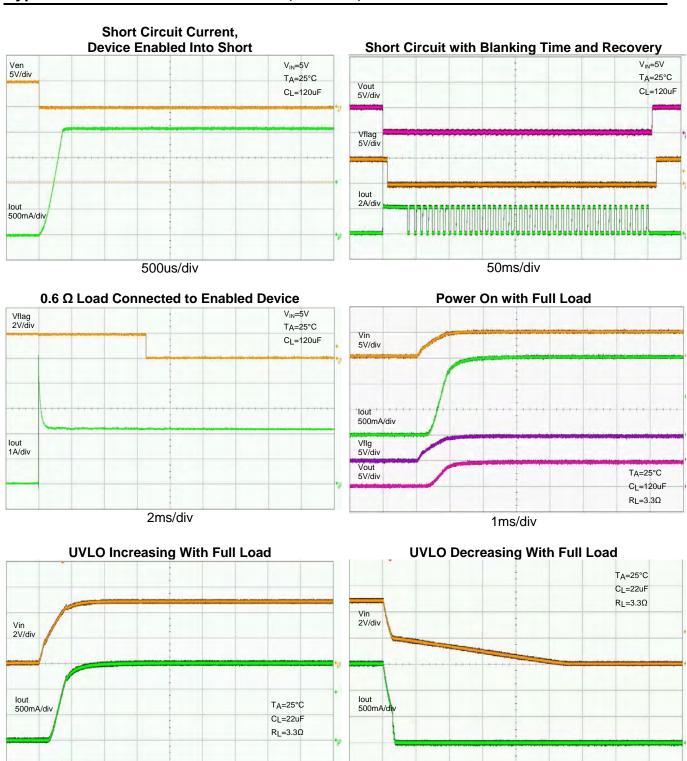
Figure 1. Voltage Waveforms: AP2181D (left), AP2191D (right)

All Enable Plots are for AP2181D Active Low





Typical Performance Characteristics (Continued)

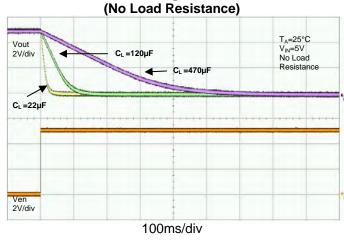


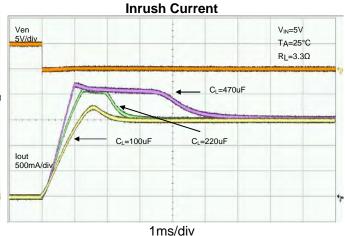
1ms/div

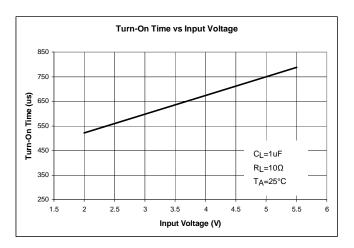


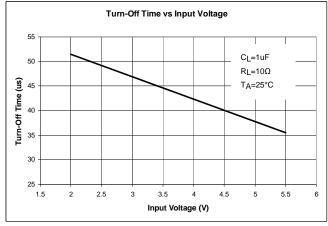
Typical Performance Characteristics (Continued)

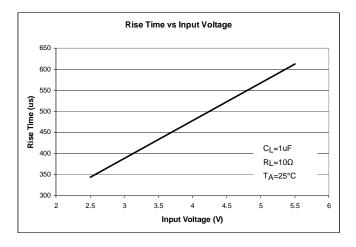
Discharge Time

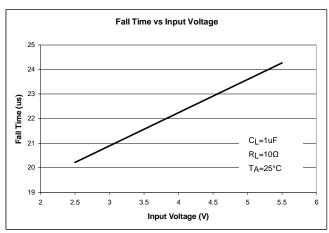






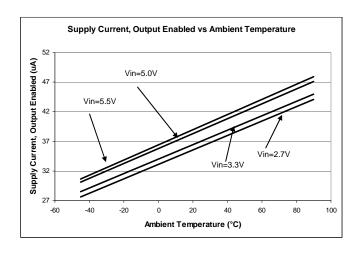


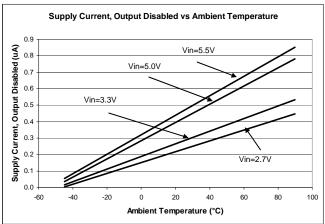


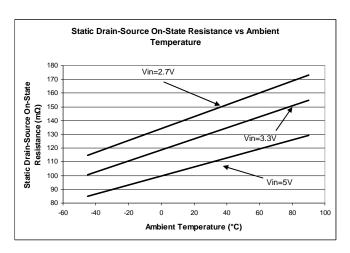


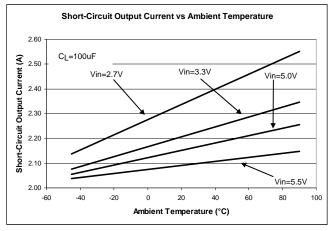


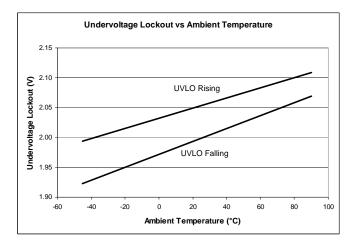
Typical Performance Characteristics (Continued)

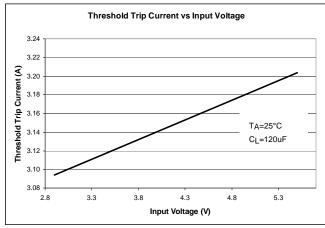














Application Note

The AP2181D and AP2191D are integrated high-side power switches optimized for Universal Serial Bus (USB) that requires protection functions. The power switches are equipped with a driver that controls the gate voltage and incorporates slew-rate limitation. This, along with the various protection features and special functions, makes these power switches ideal for hot-swap or hot-plug applications.

Protection Features:

Under-voltage Lockout (UVLO)

Under-voltage lockout function (UVLO) guarantees that the internal power switch is initially off during start-up. The UVLO functions only when the switch is enabled. Even if the switch is enabled, the switch is not turned ON until the power supply has reached at least 1.9V. Whenever the input voltage falls below approximately 1.9V, the power switch is turned off. This facilitates the design of hot-insertion systems where it is not possible to turn off the power switch before input power is removed.

Over-current and Short Circuit Protection

An internal sensing FET is employed to check for over-current conditions. Unlike current-sense resistors, sense FETs do not increase the series resistance of the current path. When an overcurrent condition is detected, the device maintains a constant output current and reduces the output voltage accordingly. Complete shutdown occurs only if the fault stays long enough to activate thermal limiting.

The different overload conditions and the corresponding response of the AP2181D/AP2191D are outlined below:

S. NO	Conditions	Explanation	Behavior of the AP2181D/AP2191D
1	Short circuit condition at start-up	Output is shorted before input voltage is applied or before the part is enabled	The IC senses the short circuit and immediately clamps output current to a certain safe level namely I _{LIMIT.}
2	Short-circuit or Overcurrent condition	Short-Circuit or Overload condition that occurs when the part is enabled.	 At the instance the overload occurs, higher current may flow for a very short period of time before the current limit function can react. After the current limit function has tripped (reached the over-current trip threshold), the device switches into current limiting mode and the current is clamped at I_{LIMIT}.
3	Gradual increase from nominal operating current to ILIMIT	Load increases gradually until the current-limit threshold.(I _{TRIG})	The current rises until I_{TRIG} or thermal limit. Once the threshold has been reached, the device switches into its current limiting mode and is set at I_{LIMIT} .

Note that when the output has been shorted to GND at extremely low temperature (< -20°C), a minimum 120- μ F electrolytic capacitor on the output pin is recommended. A correct capacitor type with capacitor voltage rating and temperature characteristics must be properly chosen so that capacitance value does not drop too low at the extremely low temperature operation. A recommended capacitor should have temperature characteristics of less than 10% variation of capacitance change when operated at extremely low temp. Our recommended aluminum electrolytic capacitor type is Panasonic FC series.



Application Note (Continued)

Thermal Protection

Thermal protection prevents the IC from damage when the die temperature exceeds safe margins. This mainly occurs when heavy-overload or short-circuit faults are present for extended periods of time. The AP2181D/AP2191D implements a thermal sensing to monitor the operating junction temperature of the power distribution switch. Once the die temperature rises to approximately 140°C, the Thermal protection feature gets activated as follows: The internal thermal sense circuitry turns the power switch off and the FLG output is asserted thus preventing the power switch from damage. Hysteresis in the thermal sense circuit allows the device to cool down to approximately 25°C before the output is turned back on. This built-in thermal hysteresis feature is an excellent feature, as it avoids undesirable oscillations of the thermal protection circuit. The switch continues to cycle in this manner until the load fault is removed, resulting in a pulsed output. The FLG open-drain output is asserted when an over-current occurs with 7-ms deglitch.

Reverse Current Protection

In a normal MOSFET switch, current can flow in reverse direction (from the output side to the input side) when the output side voltage is higher than the input side, even when the switch is turned off. A reverse-current blocking feature is implemented in the AP21x1 series to prevent such back currents. This circuit is activated by the difference between the output voltage and the input voltage. When the switch is disabled, this feature blocks reverse current flow from the output back to the input.

Special Functions:

Discharge Function

When enable is de-asserted, the discharge function is active. The output capacitor is discharged through an internal NMOS that has a discharge resistance of 100Ω . Hence, the output voltage drops down to zero. The time taken for discharge is dependent on the RC time constant of the resistance and the output capacitor.

FLG Response

The FLG open-drain output goes active low for any of the two conditions: Over-Current or Over-Temperature. The time from when a fault condition is encountered to when the FLG output goes low is 7-ms (TYP). The FLG output remains low until both over-current and over-temperature conditions are removed. Connecting a heavy capacitive load to the output of the device can cause a momentary Over-current condition, which does not trigger the FLG due to the 7-ms deglitch timeout. This 7-ms timeout is also applicable for Over-current recovery and Thermal recovery. The AP2181D/AP2191D is designed to eliminate erroneous Over-current reporting without the need for external components, such as an RC delay network.

Applications Information:

Power Supply Considerations

A 0.01-µF to 0.1-µF X7R or X5R ceramic bypass capacitor between IN and GND, close to the device, is recommended. This limits the input voltage drop during line transients. Placing a high-value electrolytic capacitor on the input (10-µF minimum) and output pin(s) is recommended when the output load is heavy. This precaution also reduces power-supply transients that may cause ringing on the input. Additionally, bypassing the output with a 0.01-µF to 0.1-µF ceramic capacitor improves the immunity of the device to short-circuit transients. This capacitor also prevents output from going negative during turn-off due to inductive parasitics.



Application Note (Continued)

Power Dissipation and Junction Temperature

The low on-resistance of the internal MOSFET allows the small surface-mount packages to pass large current. Using the maximum operating ambient temperature (TA) and RDS(ON), the power dissipation can be calculated by: $PD = RDS(ON) \times I^{2}$

Finally, calculate the junction temperature:

TJ = PD x RθJA + TA

Where:

TA= Ambient temperature °C ROJA = Thermal resistance PD = Total power dissipation

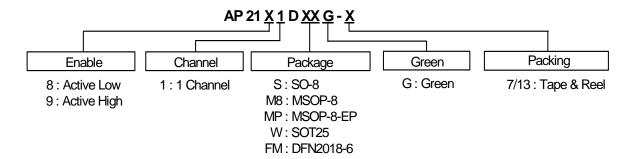
Generic Hot-Plug Applications

In many applications it may be necessary to remove modules or pc boards while the main unit is still operating. These are considered hot-plug applications. Such implementations require the control of current surges seen by the main power supply and the card being inserted. The most effective way to control these surges is to limit and slowly ramp the current and voltage being applied to the card, similar to the way in which a power supply normally turns on. Due to the controlled rise times and fall times of the AP2181D/AP2191D, these devices can be used to provide a softer start-up to devices being hot-plugged into a powered system. The UVLO feature of the AP2181D/AP2191D also ensures that the switch is off after the card has been removed, and that the switch is off during the next insertion.

By placing the AP2181D/AP2191D between the Vcc input and the rest of the circuitry, the input power reaches these devices first after insertion. The typical rise time of the switch is approximately 1ms, providing a slow voltage ramp at the output of the device. This implementation controls system surge current and provides a hotplugging mechanism for any device.



Ordering Information

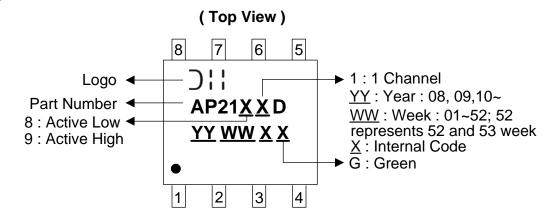


	Device	Package	Packaging	7"/13" Tape and Reel		
	Device	Code	(Note 6)	Quantity	Part Number Suffix	
Po	AP21X1DSG-13	S	SO-8	2500/Tape & Reel	-13	
Pb ,	AP21X1DM8G-13	M8	MSOP-8	2500/Tape & Reel	-13	
Pb ,	AP21X1DMPG-13	MP	MSOP-8-EP	2500/Tape & Reel	-13	
Pb ,	AP21X1DWG-7	W	SOT25	3000/Tape & Reel	-7	
PD,	AP21X1DFMG-7	FM	DFN2018-6	3000/Tape & Reel	-7	

Notes: 6. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

Marking Information

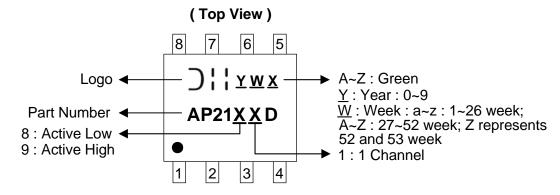
(1) SO-8



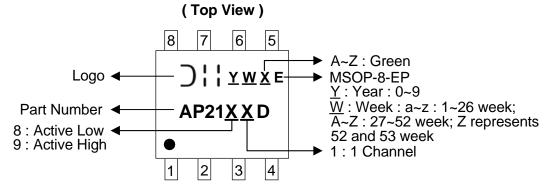


Marking Information (Continued)

(2) MSOP-8

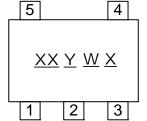


(3) MSOP-8-EP



(4) SOT25

(Top View)



XX: Identification code

Y: Year 0~9

 \underline{W} : Week: A~Z: 1~26 week;

a~z: 27~52 week; z represents

52 and 53 week X: A~Z: Green

Device	Package type	Identification Code
AP2181DW	SOT25	JE
AP2191DW	SOT25	JF



Marking Information (Continued)

(5) DFN2018-6

(Top View)

 XX: Identification Code

Y: Year: 0~9

 $\overline{\underline{W}}$: Week: A~Z: 1~26 week;

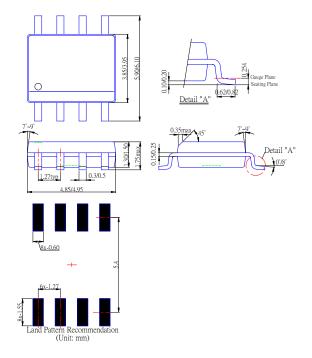
a~z: 27~52 week; z represents

52 and 53 week X : A~Z : Green

Device	Package type	Identification Code
AP2181DFM DFN2018-6		JE
AP2191DFM	DFN2018-6	JF

Package Outline Dimensions (All Dimensions in mm)

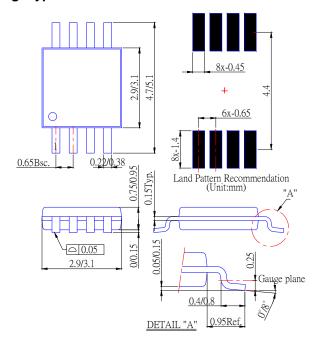
(1) Package type: SO-8



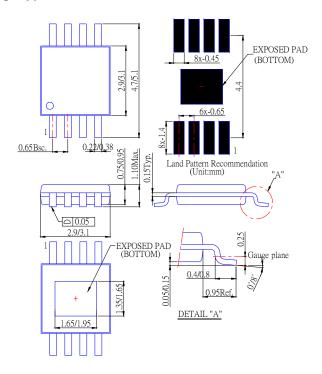


Package Outline Dimensions (Continued)

(2) Package type: MSOP-8



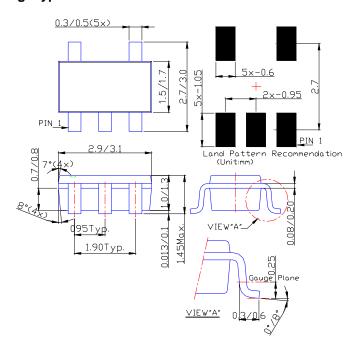
(3) Package type: MSOP-8-EP



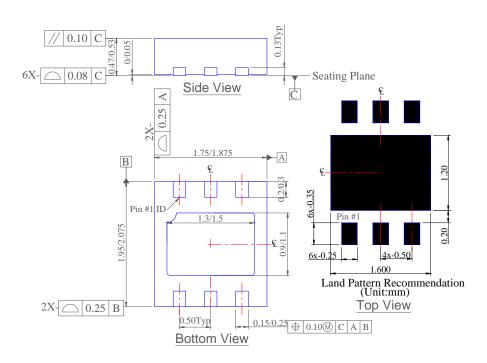


Package Outline Dimensions (Continued)

(4) Package type: SOT25



(5) Package type: DFN2018-6

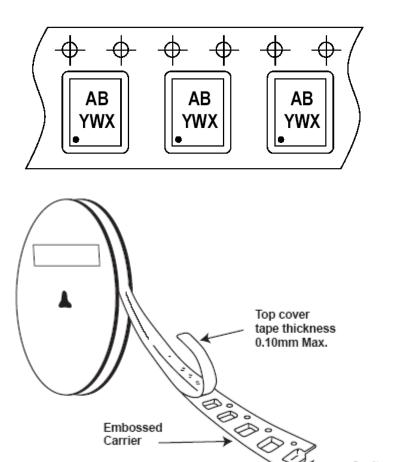




Cavity

Taping Orientation (Note 7)

For DFN2018-6



Notes: 7. The taping orientation of the other package type can be found on our website at http://www.diodes.com/datasheets/ap02007.pdf

Carrier



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- Поставка оригинальных импортных электронных компонентов напрямую с производств Америки, Европы и Азии, а так же с крупнейших складов мира;
- Широкая линейка поставок активных и пассивных импортных электронных компонентов (более 30 млн. наименований);
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Помощь Конструкторского Отдела и консультации квалифицированных инженеров;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Поставка электронных компонентов под контролем ВП;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- При необходимости вся продукция военного и аэрокосмического назначения проходит испытания и сертификацию в лаборатории (по согласованию с заказчиком);
- Поставка специализированных компонентов военного и аэрокосмического уровня качества (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Actel, Aeroflex, Peregrine, VPT, Syfer, Eurofarad, Texas Instruments, MS Kennedy, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Компания «Океан Электроники» является официальным дистрибьютором и эксклюзивным представителем в России одного из крупнейших производителей разъемов военного и аэрокосмического назначения «JONHON», а так же официальным дистрибьютором и эксклюзивным представителем в России производителя высокотехнологичных и надежных решений для передачи СВЧ сигналов «FORSTAR».



«JONHON» (основан в 1970 г.)

Разъемы специального, военного и аэрокосмического назначения:

(Применяются в военной, авиационной, аэрокосмической, морской, железнодорожной, горно- и нефтедобывающей отраслях промышленности)

«**FORSTAR**» (основан в 1998 г.)

ВЧ соединители, коаксиальные кабели, кабельные сборки и микроволновые компоненты:

(Применяются в телекоммуникациях гражданского и специального назначения, в средствах связи, РЛС, а так же военной, авиационной и аэрокосмической отраслях промышленности).



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