

Circuits from the Lab™ Reference Circuits

Circuits from the Lab™ reference circuits are engineered and tested for quick and easy system integration to help solve today's analog, mixed-signal, and RF design challenges. For more information and/or support, visit www.analog.com/CN0325.

Devices Connected/Referenced

AD7795	16-Bit Sigma-Delta ADC, 40nV Noise
AD8226	36V Instrumentation Amplifier, >90dB CMR
ADT7310	±0.5°C Digital Temperature Sensor
ADP2441	36V, 1A, Step-Down DC-DC Regulator
ADR441	Ultralow Noise, LDO XFET® 2.5V Voltage Reference
ADP1720	50 mA, High Voltage, Linear Regulator
ADuM3471	Quad Isolator with Integrated Transformer Driver and PWM

PLC/DCS Universal Analog Input Using Either 4 or 6 Pin Terminal Block

EVALUATION AND DESIGN SUPPORT

Circuit Evaluation Boards

[CN0325 Evaluation Board \(EVAL-CN0325-SDPZ\)](#)

[System Demonstration Platform \(EVAL-SDP-CB1Z\)](#)

Design and Integration Files

[Schematics, Layout Files, Bill of Materials](#)

CIRCUIT FUNCTION AND BENEFITS

The circuit shown in Figure 1 provides two, 16-bit, fully isolated, universal analog input channels suitable for programmable logic controllers (PLCs) and distributed control system (DCS) modules. Both channels are software programmable and support a number of voltage, current ranges and thermocouple, RTD types as specified in Figure 1. The inputs are protected for dc overvoltage conditions of ±30 V.

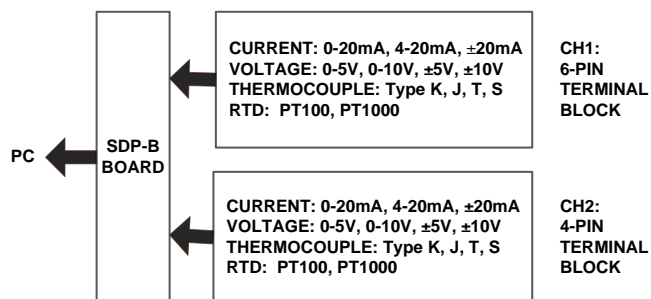


Figure 1. Universal Analog Input Overview

The demonstration board contains two different fully isolated universal input channels, one with a 4-pin terminal block (CH2), and one with a 6-pin terminal block (CH1). For the 4 terminal block channel (CH2), the voltage, current,

thermocouple and RTD inputs all share the same 4 terminals, thus minimizing the number of terminal pins required. For the 6-pin terminal block channel (CH1) the voltage and current inputs share a set of 3 terminal, and the thermocouple and RTD inputs share another set of 3 terminals; this requires more terminals but has a lower part count and component cost. Figure 2 shows a photo of the PCB, and Figure 3 shows a more detailed schematic of the circuit.

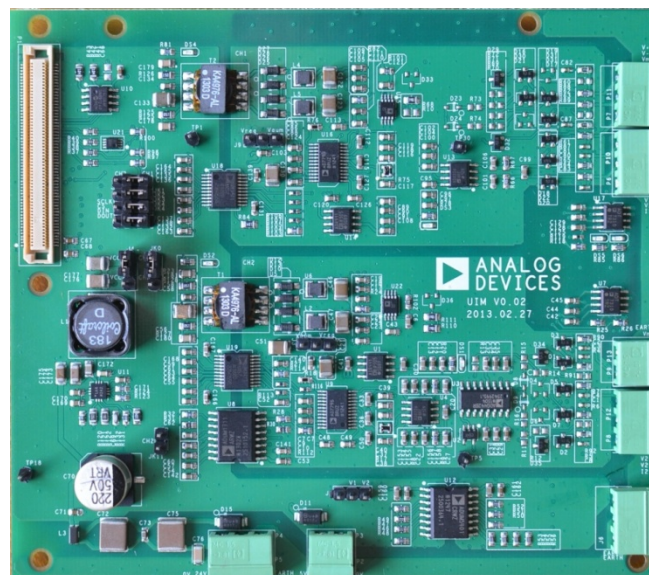


Figure 2. Universal Analog Input Board

Rev. PrA

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CIRCUIT DESCRIPTION

The [ADR441](#), an ultralow noise, low dropout XFET® 2.5 V voltage reference is used as the reference for the ADC.

Digital and power isolation is achieved using ADuM3471, a PWM controller and transformer driver with quad-channel isolator which is used to generate an isolated ± 15 V supply using an external transformer. The ADuM1311, triple-channel digital isolator is also used in the 4-pin terminal block circuit to isolate the control lines for the ADG442 switches.

The [ADP2441](#), 36 V step-down dc-to-dc regulator has a wide tolerance on its input supply making it ideal for accepting a 24 V industrial supply. It accepts up to 36 V, thereby making reliable transient protection of the supply input more easily

achievable. It steps the input voltage down to 5 V to power the [ADuM3471](#) as well as all other controller-side circuitry. The circuit also includes standard external protection on the 24 V supply terminals.

The [ADP2441](#) also features a number of other safety and reliability functions, such as undervoltage lockout (UVLO), a precision enable, a power good pin, and overcurrent-limit protection. It also can achieve up to 90% efficiency in the 24 V input, 5 V output configuration.

HARDWARE

Figure 2 shows the location of the channel containing a 4-pin terminal block and the channel with a 6-pin terminal block. It also shows the location of the 24 V supply input.

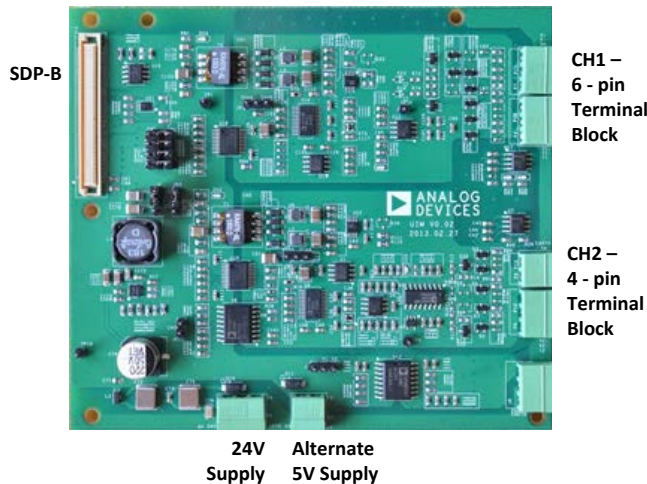


Figure 4. Channel Locations

Channel Selection

Jumpers need to be inserted and switched to configure both supply and SPI signals between CH1 and CH2 as shown in Table 1.

Table 1. Channel selection configuration settings

Link No.	Digital Function	Link position to select CH1, 6 pin terminal block	Link position to select CH2, 4 pin terminal block
JK0	5V supply	CH1	CH2
JK1	SCLK	CH1	CH2
JK2	/CS	CH1	CH2
JK3	DIN	CH1	CH2
JK4	Dout	CH1	CH2
JK11	TEMP_CS	Not Inserted	Inserted

Power Configurations

A 24 V supply is used to power the controller side of the board. Alternately a 5 V supply can be used, bypassing the [ADP2441](#) circuitry. This 5 V input does not have any overvoltage

protection and should not exceed 6 V. The supply used should be configured using the J4 link option as described in Table 2.

Table 2. External Power Supply configuration settings

	Link position to select 24V Input (default)	Link position to select 5V Input (default)
J4	VCC1	VCC2

For the analog input side of the isolation barrier, there are two options for powering a regulated 5 V for the analog circuitry. Either the [ADP1720](#) linear regulator can be used to step the 15 V down to 5 V, or else the [ADuM3471](#)'s internal 5 V regulator can be used. The link configurations for each is shown in Table 3.

Table 3. Field 5V supply configuration settings

	Link position for ADP1720, 5V regulator (default)	Link position for ADuM3471's 5V regulator
J3	Vreg	Vaum
J9	Vreg	Vaum

CH2: 4-PIN TERMINAL BLOCK CHANNEL

Input Connectors

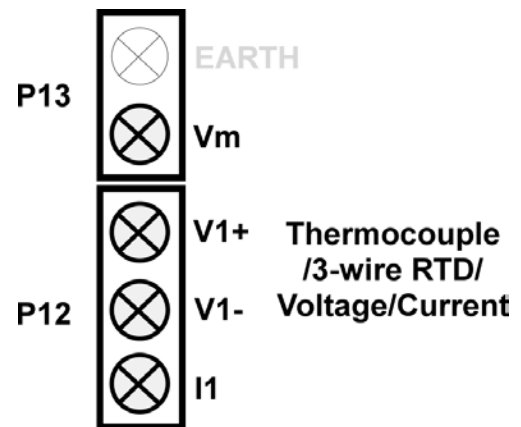


Figure 5. CH2 input connectors

Voltage and Current

The P12 connector is used for voltage and current input connections. Figure 11 and Figure 12 show simplified schematics for this input connection and configuration. This allows differential inputs in the ranges: 0 V to 5V, 0 V to 10V, $\pm 5V$, $\pm 10V$, 0 mA to 20 mA, 4 mA to 20 mA and ± 20 mA. Connect voltage or current inputs between V1+ and V1-, because current inputs also short V1+ and I1 pins together. This connects a 249 Ω precision current sensing resistor with 0.1% accuracy and 0.25 W rating.

Thermocouple

The P12 connector is also for thermocouple inputs. Various thermocouple types can be connected including J, K, T, and S. The thermocouple is connected between the V1+ and V1- inputs (see Figure 5). Figure 6 below shows how to connect a thermocouple (Type T, for example) to the universal analog input board. See Figure 13 for a simplified schematic of the thermocouple input.

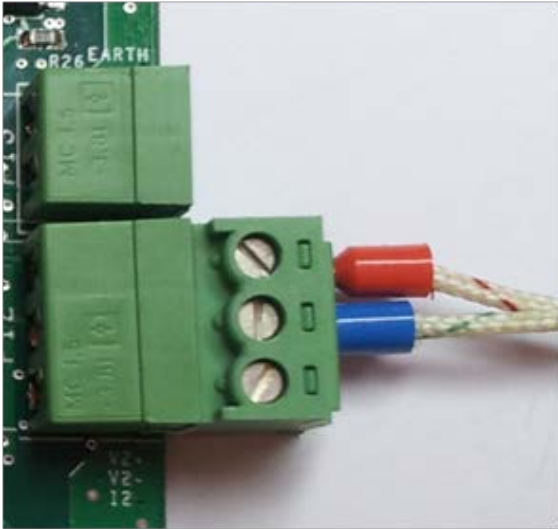


Figure 6. CH2 Thermocouple connector

RTD

The P12, P13 connectors are used for RTD inputs. The hardware can support both 1000 Ω and 100 Ω platinum RTD inputs. For 3-wire mode, the two common wires are connected to V1+ and V1- and the return is connected to Vm (see Figure 5). Figure 7 below shows how to connect a 3-wire RTD sensor to the universal analog input board. See Figure 14 for a simplified schematic of the RTD input.

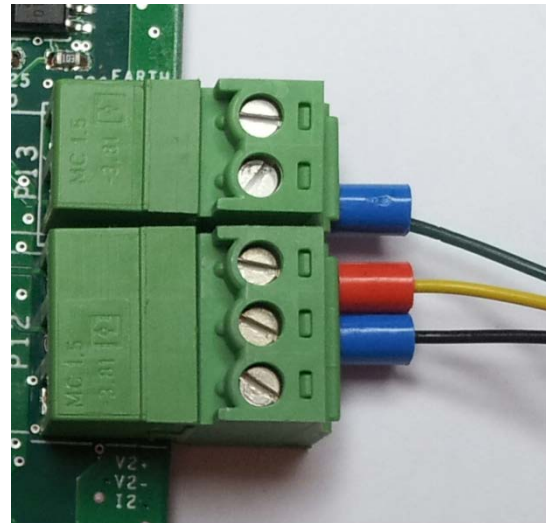


Figure 7. CH2 RTD connector

CH1: 6-PIN TERMINAL BLOCK CHANNEL

Input Connectors

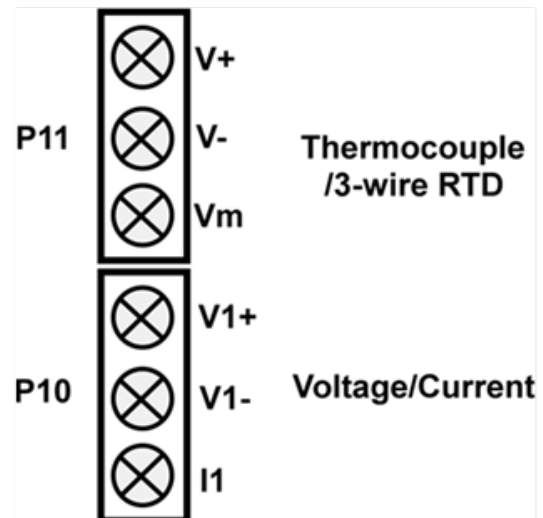


Figure 8. CH1 channel input connectors. See Figure 13 for a simplified input diagram.

Voltage and Current

The P10 connector is used for voltage and current input connections. This allows differential inputs in the ranges: 0 V to 5 V, 0 V to 10V, $\pm 5V$, $\pm 10V$, 0 mA to 20 mA, 4 mA to 20mA, and ± 20 mA. Connect voltage or current inputs between V1+ and V1- (see Figure 13), though for current inputs also short V1+ and I1 pins together, this connects a 249 Ω precision current sensing resistor with 0.1% accuracy and 0.25 W rating.

Thermocouple

The P11 connector is for thermocouple inputs. Various thermocouple types can be connected including J, K, T and S. The thermocouple is connected between the V+ and V- inputs (see Figure 8). Figure 9 below shows how to connect a thermocouple (Type T in this example) to the universal analog input board.

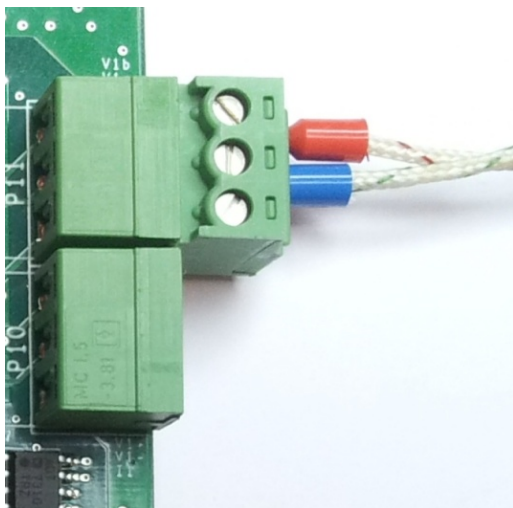


Figure 9. CH1 Thermocouple Connector

RTD

The P11 connector is also for RTD input. The hardware can support both 1000 Ω and 100 Ω platinum RTD inputs. For 3-wire mode, the two common wires are connected to V+ and V-,

and the return is connected to Vm (see Figure 8). Figure 10 below shows how to connect a 3-wire RTD sensor to the universal analog input board.

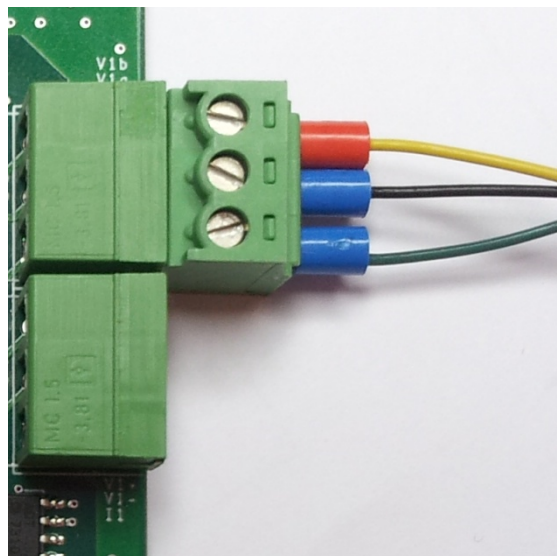


Figure 10. CH1 RTD Connector

SIMPLIFIED INPUT CIRCUIT DIAGRAMS

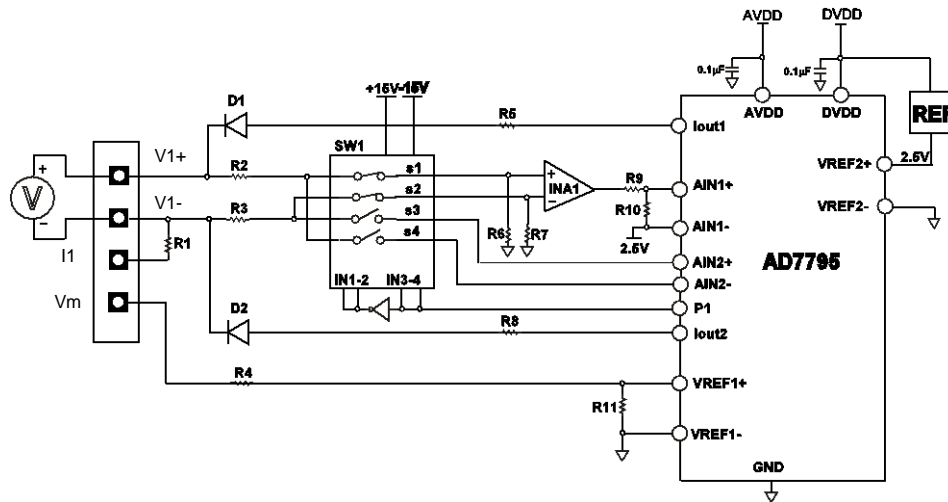


Figure 11. CH2 Simplified Voltage Input diagram

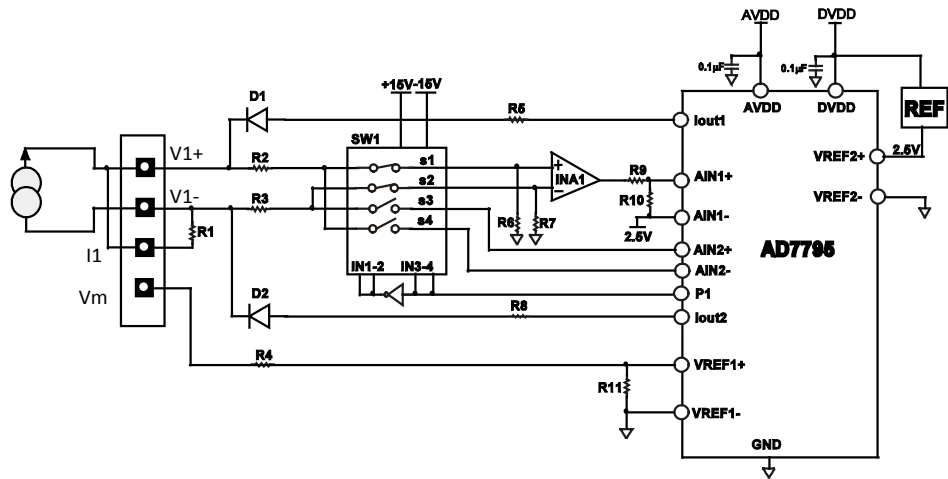


Figure 12. CH2 Simplified Current Input diagram

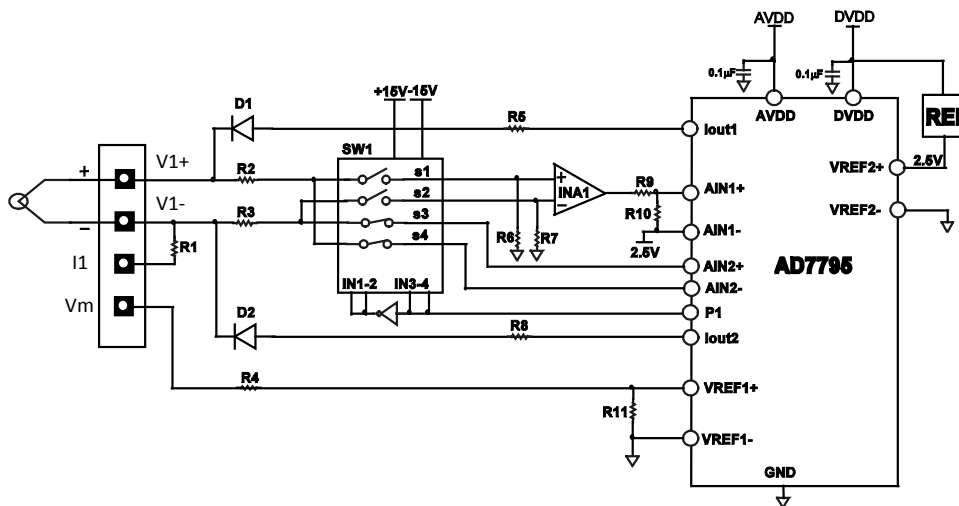


Figure 13. Ch2 Simplified Thermocouple Input diagram

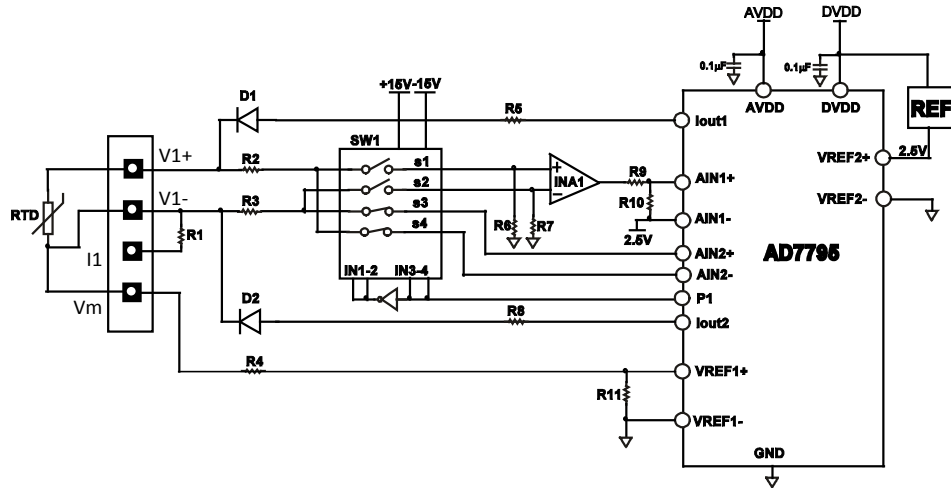


Figure 14. Ch2 Simplified RTD Input diagram

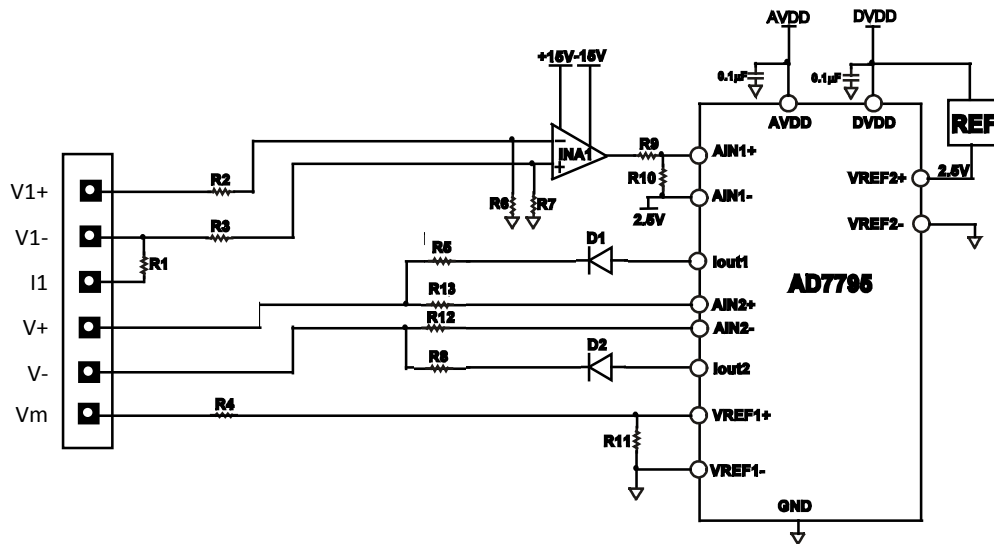


Figure 15. CH1, simplified Input diagram

SOFTWARE DESCRIPTION

The universal analog input board is shipped with a CD ROM containing the evaluation software designed using Labview. This can be installed onto a standard PC with Windows XP (SP2), Vista (32-bit and 64-bit) or Windows 7 (32-bit and 64-bit). To use the evaluation software, the SDP-B (System Demonstration Platform Version B) is required.

When the evaluation software is run, the optimized default configurations as well as calibrated parameters from onboard EEPROM are loaded into the software. The evaluation software allows users to acquire data from the universal analog input board, which can be analyzed or saved to a file. The analysis results are displayed on the screen with plots and digital format. A user can set up their own configuration and calibration values, save this into the on board EEPROM and the software will record the configuration and uploaded them automatically next time the software is run.

Software Installation

1. Insert the CD ROM (or download the software installation package from <http://ftp.analog.com/pub/cftl/CN0325/>)
2. Locate the file Setup.exe on the CD of downloaded files. Double click the file to start the installation procedure.
3. Follow the On-Screen instructions to complete the installation.

Install the evaluation software before connecting the evaluation board and SDP board to the USB port of the PC to ensure that the evaluation system and SDP board is correctly recognized when connected to the PC. Once installed;

1. Connect the [EVAL-SDP-CB1Z](#) via the USB port of the PC using the supplied cable.
2. Connect the [EVAL-CN0325-SDPZ](#) evaluation board to either of the SDP connectors.

3. Power up the [EVAL-CN0325-SDPZ](#) (Ensure jumpers are setup correctly as in the “Hardware” section)
4. Start the [EVAL-CN0325-SDPZ](#) software (“CN0325.exe”) and proceed through any dialog boxes that appear. This completes the installation.

USING THE SOFTWARE

The main panel of the software is shown in Figure 16. The hardware is configured via the **Configuration** tab, which is divided into three sub-tabs for the different function aspects. The **Acquisition Result** tab shows all the data from ADC and converts results to the relevant units. The **Calibration** tab allows users to calibrate any of the ranges. Details about the connected SDP board and software can be found in the **S/W Version Info** tab.

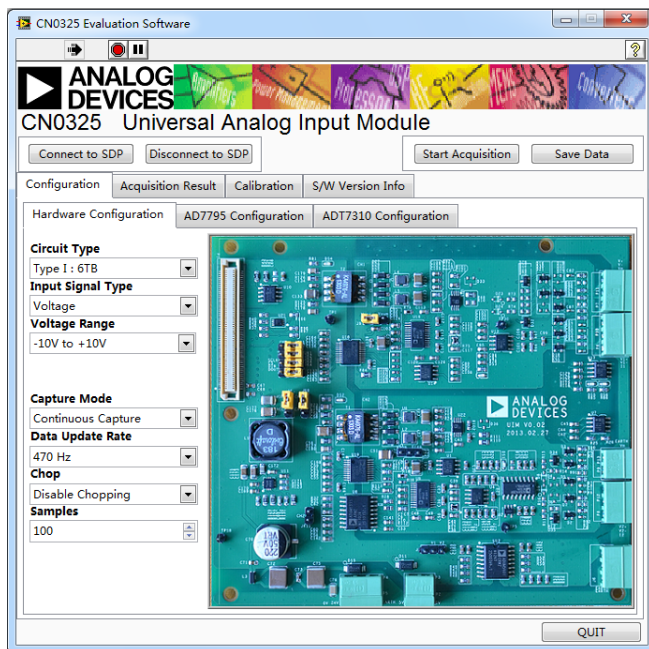


Figure 16. Main panel of evaluation software.

Main Window Buttons

Connect to SDP : Click to set up the connection between SDP board and evaluation board..

Disconnect to SDP : Click to stop the connection between SDP board and evaluation board.

Single Capture : Click to start single capture.

Start (Stop) Acquisition : Click to start (stop) a continuous capture.

Save Data : Save the data shown from the software into a file for further analysis.

Quit : Quit the application.

Configuration tab

Hardware Configuration Sub-tab

The picture on the **Hardware Configuration** sub-tab (see Figure 17) shows the correct way for jumpers setting and wire connection based on the input selection. To ensure correct results, the jumpers setting on the hardware should be the same with the picture. The different input selection options are outlined below;

Circuit Type: There are two fully isolated, universal analog input circuits to choose from. “Type1- 6TB”: the lowest cost solution with 6 terminals for sensor and signal connection or “Type2- 4TB”: a more compact solution with only 4-pins terminals for sensor and signal connection.

Input Signal Type: The evaluation board can convert multi-types of signals including Voltage, Current, thermocouple and RTD. Once selected the desired range of thermocouple/RTD type must be chosen.

Capture Mode : Setting the method of capturing the data. Either Single Capture mode only the specified number of samples are captured. Or Continues Capture mode, where data will be continuously captured until the user stops the acquisition.

AD7795 Configuration Sub-tab

For each type of input signal range selected in the **Hardware configuration** tab there is a default configuration loaded to the universal analog input board. This **AD7795 Configuration** tab (see Figure 17) allows more advanced configuration and provides the flexibility to evaluate the board with the configuration different from the default value.

Using this tab requires specific knowledge of the [AD7795](#) registers, functions and hardware structure. The incorrect configuration may cause acquisition and operation error. The button **Recover All (Current) range to Default** is used to recover the default configurations for the range(s).

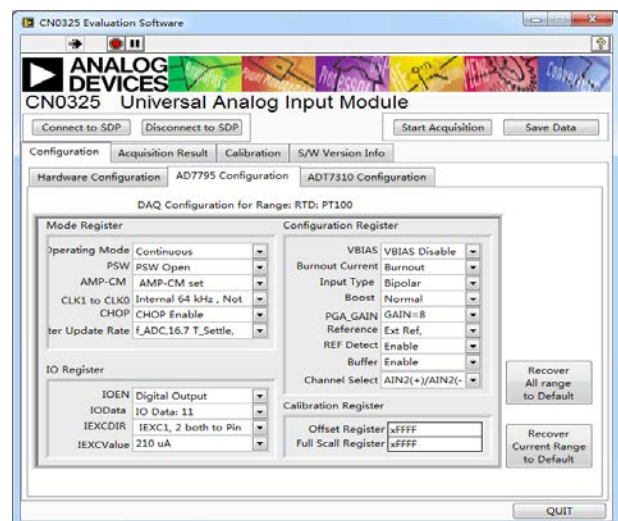


Figure 17. AD7795 Configuration Page.

ADT7310 Configuration Sub-tab

There is an on-board temperature sensor chip, the [ADT7310](#), which is placed close to the terminal blocks for cold-junction compensation during thermocouple measurements. Typically a default configuration loaded to the universal analog input board. This [ADT7310](#) Configuration tab (see Figure 18) allows more advanced configuration and provides the flexibility to evaluate the board with the configuration different from the default value. Using this tab requires specific knowledge of the [ADT7310](#) registers, functions and hardware structure.

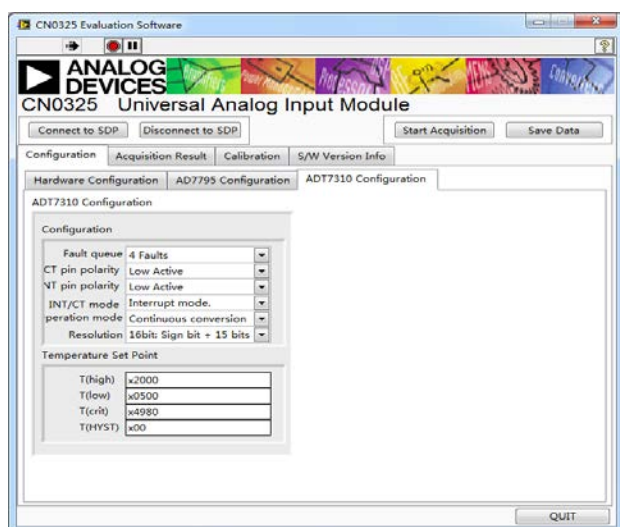


Figure 18. ADT7310 Configuration Page.

Acquisition Result tab

Converted Result Sub-tab

A result is converted to the relevant units based on the RAW data from data converter along with the channel configuration and calibration values. The data is shown on waveform chart on this sub-tab (See Figure 19). The data is also analyzed to provide the number of samples, the mean, the min and max values, the rms and peak-to-peak noise as well as the rms and peak-to-peak resolution.

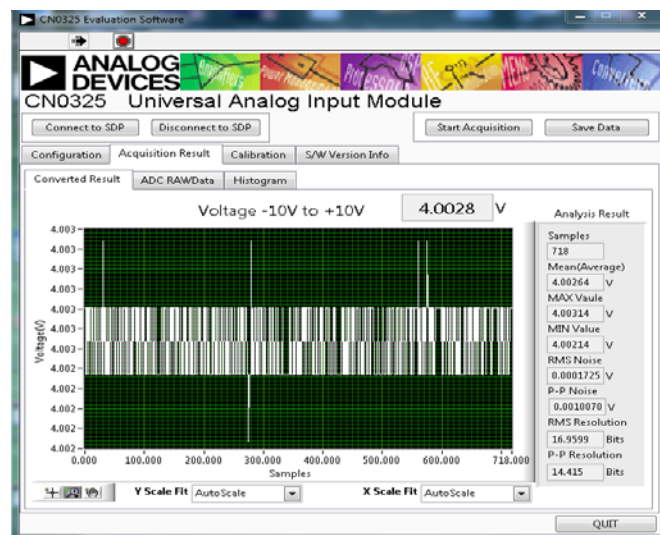


Figure 19. Acquisition Result tab, Converted Result Sub-tab

ADC RAW Data Sub-tab

The acquisition data directly read from ADC is shown on the waveform chart on this sub-tab. The data is also analyzed to provide the number of samples, the mean, the min and max values, the rms and peak-to-peak noise as well as the rms and peak-to-peak resolution.

Histogram Sub-tab

The histogram tab (See Figure 20) clearly shows the distribution of the RAW ADC data that has been captured. This chart can be used to evaluate the noise and acquisition stability.

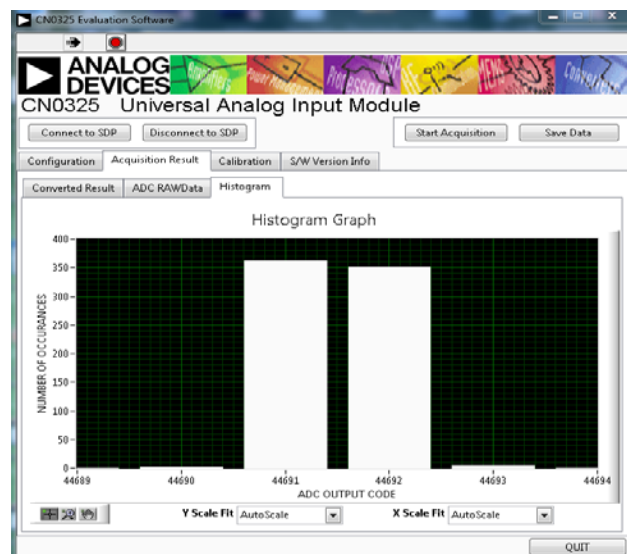


Figure 20. Acquisition Result tab, Histogram Sub-tab

Calibration tab

This software also provides independent calibrated parameters for each input signal and sensor type. This allows users to accurately calibrate the offset and gain of the system to achieve a high level of dc precision for the system (see Figure 21). The calibrated parameters can be stored into on-board EEPROM for later reuse. A complete calibration requires both a zero-scale calibration and full-scale calibration.

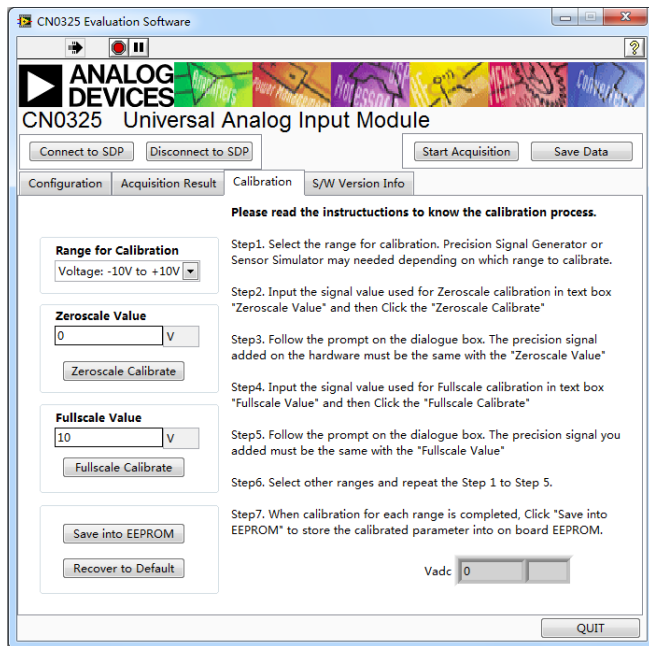


Figure 21. Calibration Page.

The following steps will ensure a correct calibration.

1. Select the input range to be calibrated
2. Apply the correct input signal specified in **Zeroscale Value** and click the button **Zero Calibrate**. Follow the prompt to complete the zero-scale calibration.
3. Apply the correct input signal specified in **Fullscale Value** and click the button **Zero Calibrate**. Follow the prompt to complete the Full-scale calibration.
4. Click the button **Save into EEPROM**

The calibrated parameters will be placed into the internal calibration register of ADC. Only if the user clicks the button **Save into EEPROM** will the new calibration values be permanently saved to the EEPROM and loaded the next time the range is selected.

Copies of the factory default calibrated values are stored in the on board EEPROM. By clicking the **Recover to Default** all the calibrated v will return to their factory default values.

S\W Version Info tab

Figure 22 shows an image of the S\W Version Info tab. This tab provides information on the connected SDP board.

Clicking the **Flash LED** button will flash the LED on SDP, also indicating that the connection between SDP and evaluation board is successfully set up.

Clicking the **Read Firmware** button will to read the information about the current code on SDP platform.

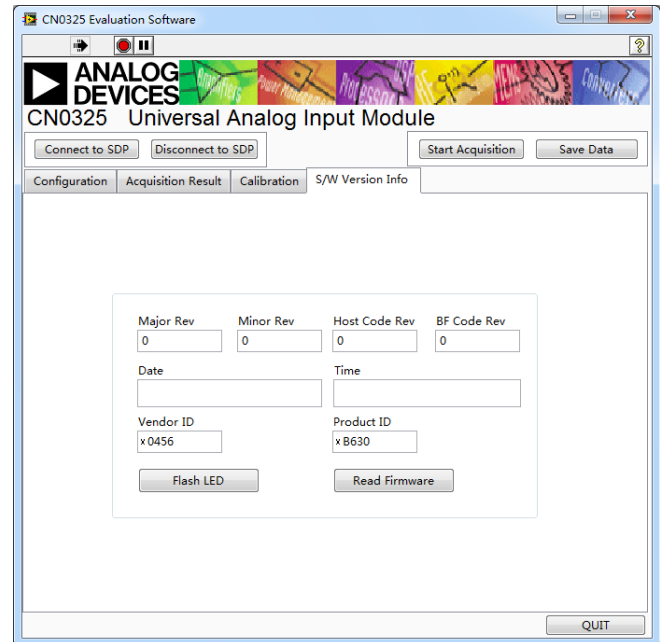


Figure 22. Software Version Information tab

The meaning of each parameter is listed below;

Major Rev : The major code revision number

Minor Rev : The minor code revision number

HostCode Rev : The version of the host code with which the firmware was developed

BlackFinCode Rev : The BlackFin code revision number of the firmware

Date : Date the code was compiled

Time: Time the code was compiled

LEARN MORE

CN-0325 Design Support Package:

<http://www.analog.com/CN0325-DesignSupport>

AD779x Instrumentation Converters -Frequently Asked Questions

MT-004 The Good, the Bad, and the Ugly Aspects of ADC Input Noise—Is No Noise Good Noise?

MT-022 Tutorial,ADC Architectures III: Σ - Δ ADC Basics, Analog Devices.

MT-023 Tutorial, ADC Architectures IV: Σ - Δ ADC Advanced Concepts and Applications, Analog Devices.

MT-031 Tutorial, Grounding Data Converters and Solving the Mystery of "AGND" and "DGND", Analog Devices.

MT-101 Tutorial, Decoupling Techniques, Analog Devices.

Chen, Baoxing. 2006. iCoupler Products with isoPower Technology: Signal and Power Transfer Across Isolation Barrier Using Microtransformers, Analog Devices.

Inside iCoupler® Technology:ADuM347x PWM Controller and Transformer Driver with Quad-Channel Isolators Design Summary. By Flow Zhao, Design Engineer, Analog Devices.

Slattery, Colm, Derrick Hartmann, and Li Ke. "PLC Evaluation Board Simplifies Design of Industrial Process Control Systems."Analog Dialogue (April 2009), Analog Devices.

Data Sheets and Evaluation Boards

[AD7795 Data Sheet](#)

[AD8226 Data Sheet](#)

[ADT7310 Data Sheet](#)

[ADP2441 Data Sheet](#)

[ADR441 Data Sheet](#)

[ADP1720 Data Sheet](#)

[ADuM3471 Data Sheet](#)

[System Demonstration Platform \(EVAL-SDP-CB1Z\)](#)

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CN0325-0-4/13(PrA)

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