

## FEATURES

- J-STD-020D reflow
- RoHS compliant
- Maxim MAX253 compatible
- 3.3V and 5V versions
- Isolation to 4kV<sub>dc</sub>
- Frequency range to 500kHz
- Toroidal construction
- Industry-standard pinout
- UL 94 V-0 package materials
- Low profile
- Industrial temperature range

## DESCRIPTION

The 78253 series of converter transformers are specifically designed for use with the MAX253 chip set to provide isolated power supplies. The 5V input version can supply 1W and the 3.3V input version can supply 500mW. A centre tapped secondary winding allows for full bridge, half bridge or voltage doubling.

For through hole versions see 78253 series datasheet.

## SELECTION GUIDE

Order Code	Input Voltage	Output Voltage	Max. Output Current	Isolation Voltage	Turns Ratio
	V	V	mA	V <sub>dc</sub>	
<b>78253/35JC</b>	3.3	5.0	100	1500	1:2.27
<b>78253/55JC</b>	5.0	5.0	200	1500	1:1.31
<b>78253/35JVC</b>	3.3	5.0	100	4000	1:2.14
<b>78253/55JVC</b>	5.0	5.0	200	4000	1:1.33

## ORDER CODE DETAILS

Order Code	Package Type	Packaging Type	Quantity
<b>78253/XXJC</b>	6 Pin SM	Tube	50
<b>78253/XXJC-R</b>	6 Pin SM	Tape & Reel	500

## 78253/35JC CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Primary Inductance, L <sub>p</sub>	100kHz, 250mV	0.27	0.36	0.50	mH
Secondary Inductance, L <sub>s</sub>	100kHz, 250mV	1.60	2.00	2.40	mH
Leakage Inductance, L <sub>l</sub>	100kHz, 250mV		0.32	1.00	μH
Interwinding Capacitance, C <sub>ww</sub>	100kHz, 250mV		24	50	pF
Primary D.C. Resistance, R <sub>dc</sub>	>0.1VDC		0.5	1.00	Ω
Volt-time Product, Et	Pins 1/2 or 2/3	30	49		Vμs

## 78253/55JC CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Primary Inductance, L <sub>p</sub>	100kHz, 250mV	0.57	0.83	1.10	mH
Secondary Inductance, L <sub>s</sub>	100kHz, 250mV	1.10	1.40	1.70	mH
Leakage Inductance, L <sub>l</sub>	100kHz, 250mV		0.51	1.00	μH
Interwinding Capacitance, C <sub>ww</sub>	100kHz, 250mV		30	50	pF
Primary D.C. Resistance, R <sub>dc</sub>	>0.1VDC		0.70	1.50	Ω
Volt-time Product, Et	Pins 1/2 or 2/3	40	70		Vμs

## 78253/35JVC CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Primary Inductance, L <sub>p</sub>	100kHz, 20mV	110	142	203	μH
Secondary Inductance, L <sub>s</sub>	100kHz, 20mV	550	710	850	μH
Leakage Inductance, L <sub>l</sub>	100kHz, 250mV		3.46	5.00	μH
Interwinding Capacitance, C <sub>ww</sub>	100kHz, 250mV		2	8.00	pF
Primary D.C. Resistance, R <sub>dc</sub>	>0.1VDC		0.30	0.50	Ω
Volt-time Product, Et	Pins 1/2 or 2/3	18	30		Vμs

## 78253/55JVC CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Primary Inductance, L <sub>p</sub>	100kHz, 20mV	170	240	335	μH
Secondary Inductance, L <sub>s</sub>	100kHz, 20mV	350	444	540	μH
Leakage Inductance, L <sub>l</sub>	100kHz, 250mV		6.28	8.00	μH
Interwinding Capacitance, C <sub>ww</sub>	100kHz, 250mV		3.4	8.00	pF
Primary D.C. Resistance, R <sub>dc</sub>	>0.1VDC		0.40	0.60	Ω
Volt-time Product, Et	Pins 1/2 or 2/3	25	37		Vμs

All specifications typical at T<sub>a</sub>=25°C



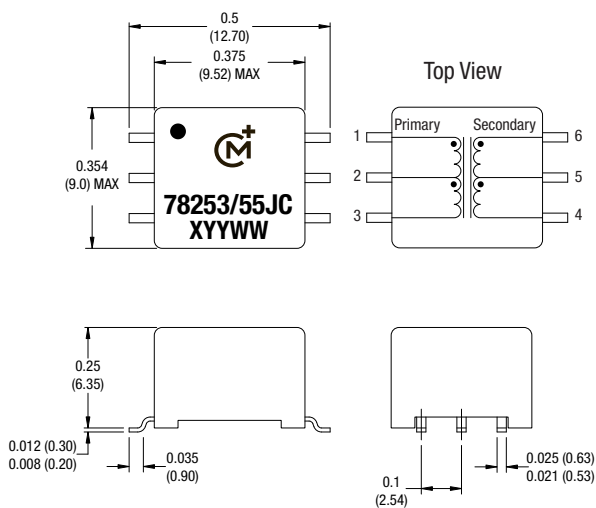
For full details go to  
[www.murata-ps.com/rohs](http://www.murata-ps.com/rohs)

## ABSOLUTE MAXIMUM RATINGS

Operating free air temperature range	-40°C to 85°C
Storage temperature range	-50°C to 125°C
Peak current $I_{PK}$	400mA
Isolation voltage 78253/XXJC (flash tested for 1 second)	1500Vdc
Isolation voltage 78253/XXJVC (flash tested for 1 second)	4000Vdc

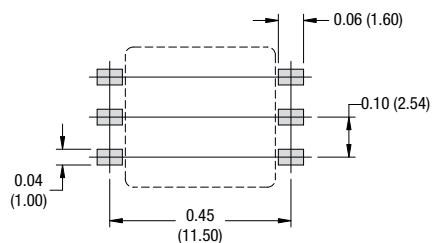
## PACKAGE SPECIFICATIONS

### MECHANICAL DIMENSIONS



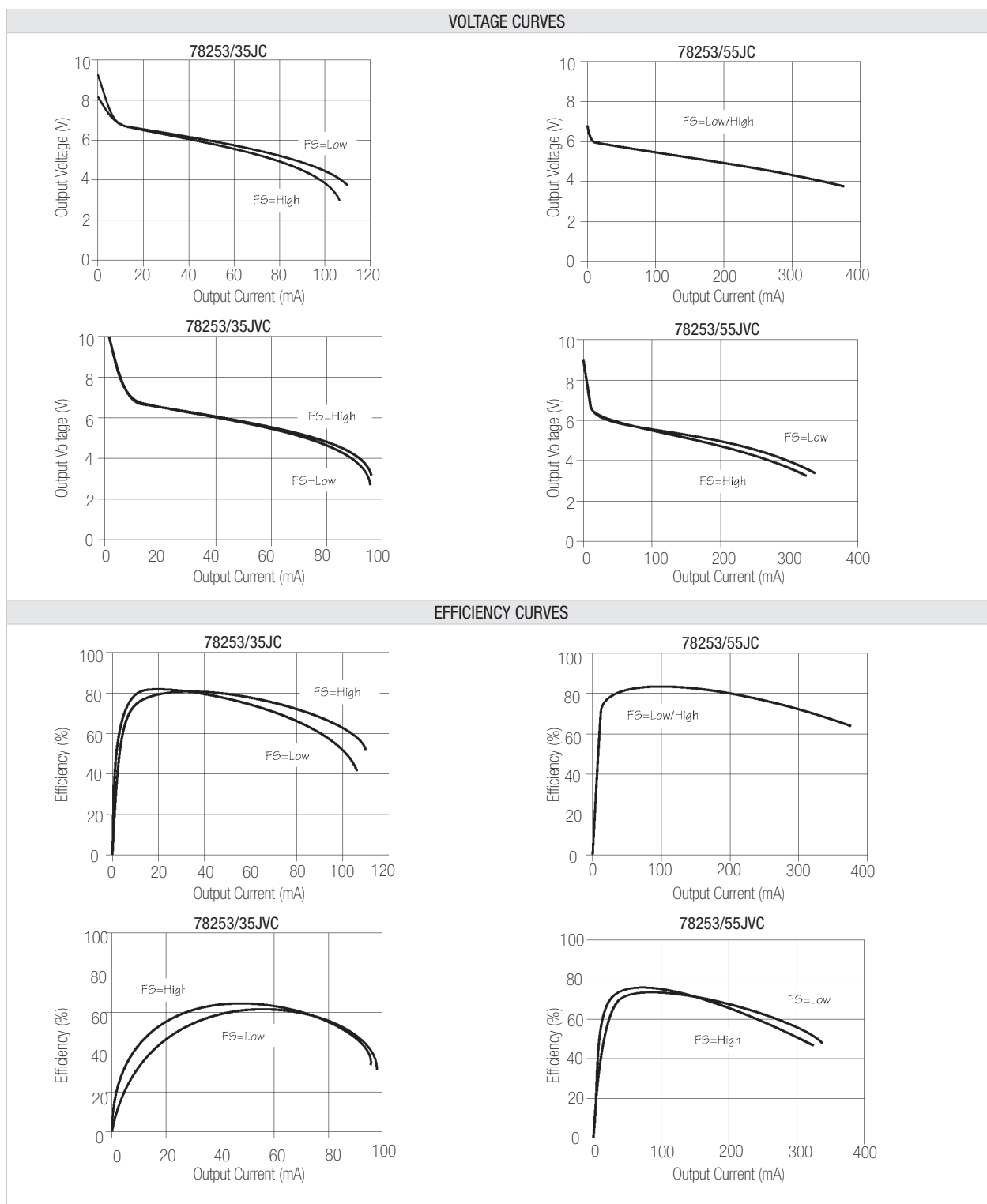
Unless otherwise stated all dimensions in inches (mm)  $\pm 0.01$  (0.25).  
All pins on a 0.1 (2.54) pitch and within  $\pm 0.01$  (0.25) of true position.  
Package weight 1.0g Typ.

### RECOMMENDED FOOTPRINT DETAILS



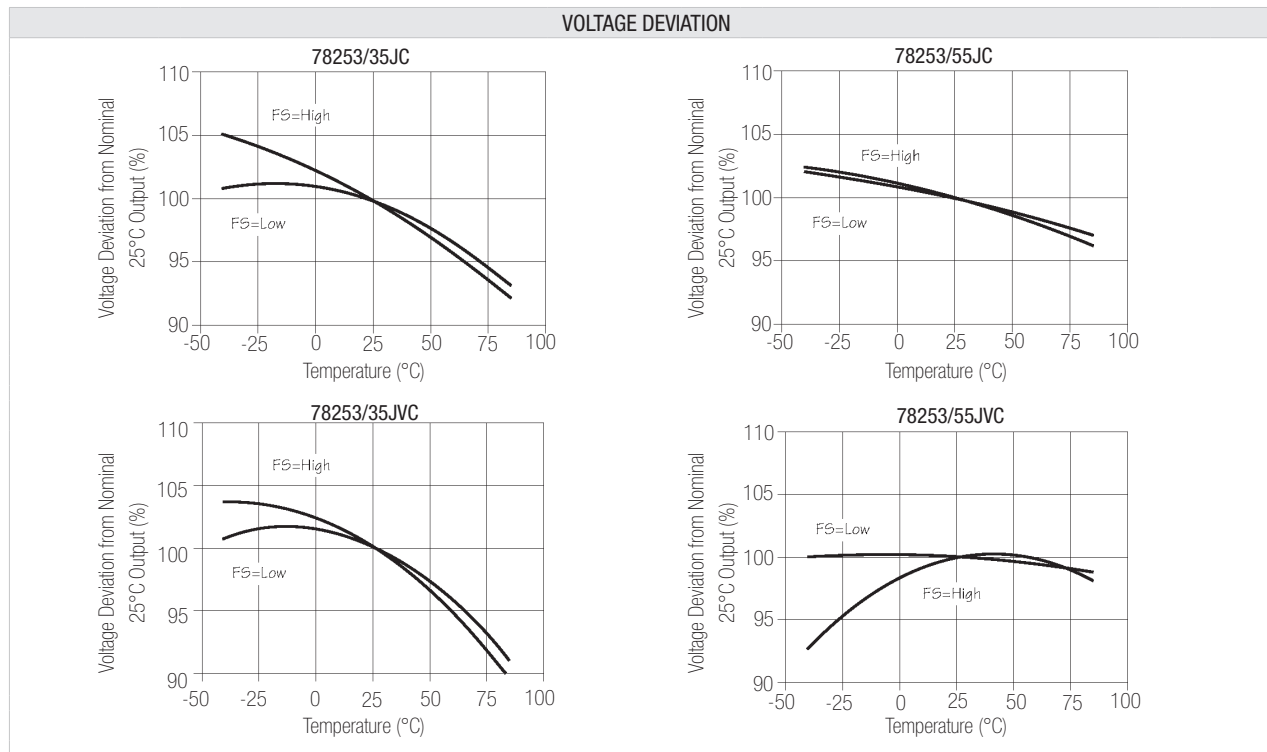
Unless otherwise stated all dimensions in inches (mm)  $\pm 0.01$  (0.25).  
All pins on a 0.1 (2.54) pitch and within  $\pm 0.01$  (0.25) of true position.

**TYPICAL CHARACTERISTICS (VOLTAGE CURVES)**



All curves are derived from testing with the Maxim MAX235 IC using the circuit shown in application note MPAN-03 (download at <http://www.murata-ps.com/data/apnotes/mpan-03.pdf>).

**TYPICAL CHARACTERISTICS (VOLTAGE CURVES) continued**



**TECHNICAL NOTES**

**ISOLATION VOLTAGE**

'Hi Pot Test', 'Flash Test', 'Withstand Voltage', 'Proof Voltage', 'Dielectric Withstand Voltage' & 'Isolation Test Voltage' are all terms that relate to the same thing, a test voltage, applied for a specified time, across a component designed to provide electrical isolation, to verify the integrity of that isolation.

All products in this series are 100% production tested at their stated isolation voltage.

A question commonly asked is, "What is the continuous voltage that can be applied across the part in normal operation?"

For a part holding no specific agency approvals both input and output should normally be maintained within SELV limits i.e. less than 42.4V peak, or 60VDC. The isolation test voltage represents a measure of immunity to transient voltages and the part should never be used as an element of a safety isolation system. The part could be expected to function correctly with several hundred volts offset applied continuously across the isolation barrier; but then the circuitry on both sides of the barrier must be regarded as operating at an unsafe voltage and further isolation/insulation systems must form a barrier between these circuits and any user-accessible circuitry according to safety standard requirements.

**REPEATED HIGH-VOLTAGE ISOLATION TESTING**

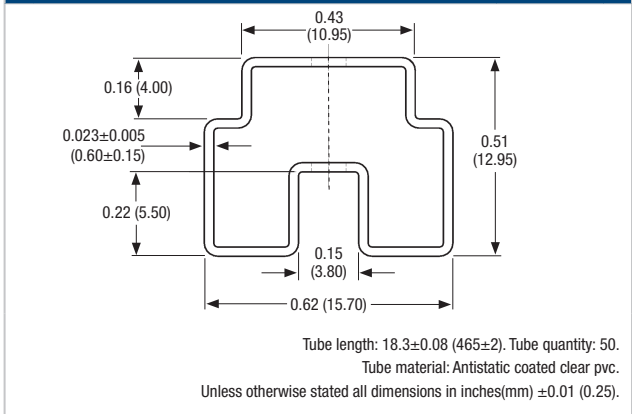
It is well known that repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment. This series has toroidal isolation transformers, with no additional insulation between primary and secondary windings of enameled wire. While parts can be expected to withstand several times the stated test voltage, the isolation capability does depend on the wire insulation. Any material, including this enamel (typically polyurethane) is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage.

This consideration equally applies to agency recognized parts rated for better than functional isolation where the wire enamel insulation is always supplemented by a further insulation system of physical spacing or barriers.

**SOLDERING INFORMATION<sup>1</sup>**

Pin finish	Matte tin
Max. peak reflow temperature	245°C
Moisture sensitivity level <sup>2</sup>	1
Max. time above liquidous (217°C)	100s

**TUBE OUTLINE DIMENSIONS**

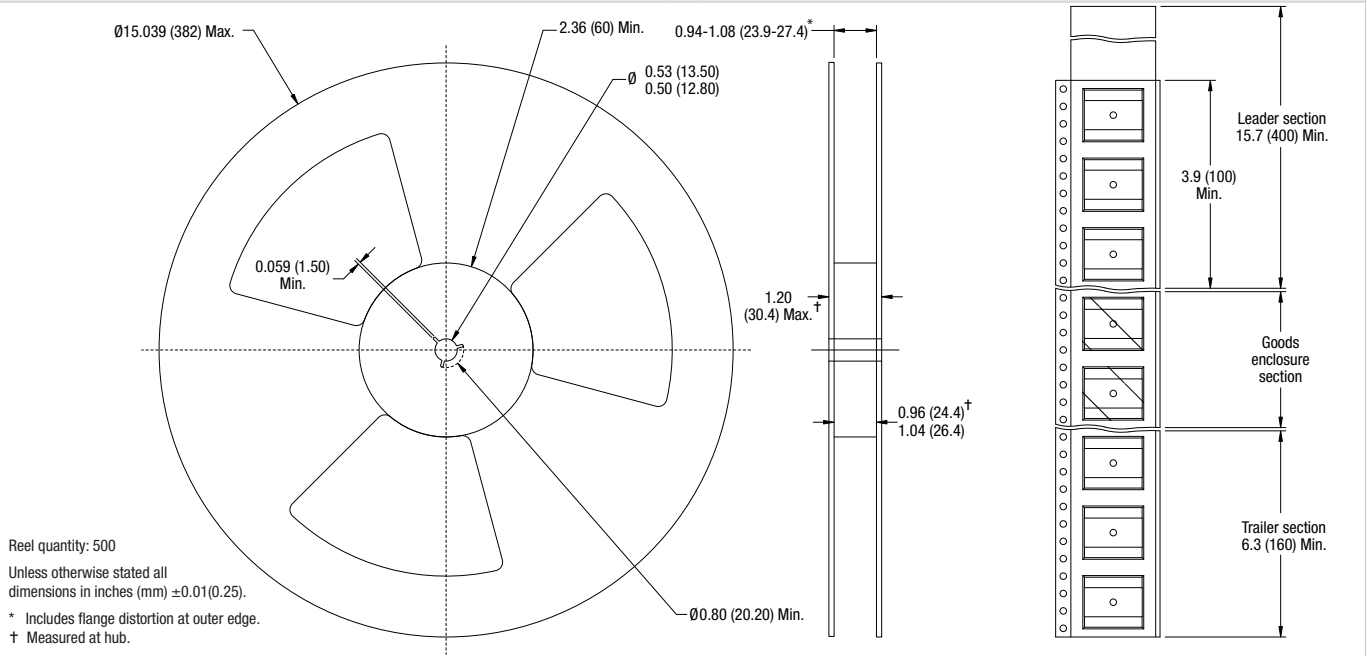


All specifications typical at  $T_a=25^\circ\text{C}$

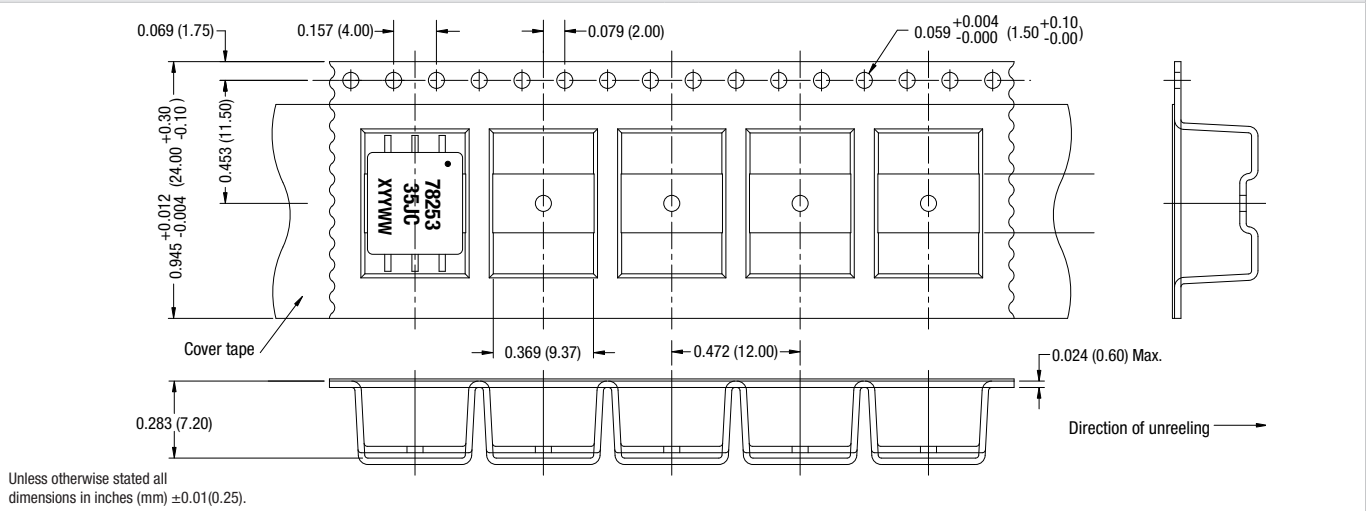
- 1 For further information, please visit [www.murata-ps.com/rohs](http://www.murata-ps.com/rohs)
- 2 Representative samples of the product were subjected to the conditioning described in IPC/JEDEC J-STD-020D and passed electrical testing, package coplanarity and visual inspection which revealed no external cracks or changes in package body flatness.

**TAPE & REEL SPECIFICATIONS**

**REEL OUTLINE DIMENSIONS**



**TAPE OUTLINE DIMENSIONS**



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