Nch 600V 20A Power MOSFET

V_{DSS}	600V
R _{DS(on)} (Max.)	0.25Ω
I _D	±20A
P _D	100W

Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Gate-source voltage (V_{GSS}) guaranteed to be $\pm 30V$.
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.
- 6) Pb-free lead plating; RoHS compliant

Application

Switching Power Supply

● **Absolute maximum ratings** (T_a = 25°C)

•	LPT(S)	(2)
)	-	
1		
1		(1)
	• Inner circuit	(' '

Outline

(1) Gate (2) Drain (3) Source *1 Body Diode

 Packaging specifications

 Packing
 Embossed Tape

 Reel size (mm)
 330

 Tape width (mm)
 24

 Basic ordering unit (pcs)
 1000

 Taping code
 TL

 Marking
 R6020ANJ

Parameter		Symbol	Value	Unit
Drain - Source voltage		V _{DSS}	600	V
O- officers of dealer assessed	T _C = 25°C	I _D *1	±20	Α
Continuous drain current	T _C = 100°C	I _D *1	±9.8	Α
Pulsed drain current	I _{D,pulse} *2	±80	Α	
Gate - Source voltage		V _{GSS}	±30	V
Avalanche energy, single pulse		E _{AS} *3	26.7	mJ
Avalanche energy, repetitive		E _{AR} *4	8.4	mJ
Avalanche current		I _{AR} *3	10	Α
Power dissipation (T _c = 25°C)		P _D	100	W
Junction temperature		T _j	150	°C
Range of storage temperature		T _{stg}	-55 to +150	°C
Reverse diode dv/dt		dv/dt	15	V/ns

● Absolute maximum ratings

Parameter	Symbol	Conditions	Values	Unit
Drain - Source voltage slope	d∨/dt	$V_{DS} = 480V, I_{D} = 20A$ $T_{j} = 125^{\circ}C$	50	V/ns

●Thermal resistance

Doromotor	Symbol	Values			Unit
Parameter	Symbol	Min.	Тур.	Max.	Uniit
Thermal resistance, junction - case	R _{thJC}	-	-	1.25	°C/W
Thermal resistance, junction - ambient	R _{thJA}	-	-	80	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

• Electrical characteristics $(T_a = 25^{\circ}C)$

Parameter	Symbol Conditions -			Values		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	600	-	1	V
Drain - Source avalanche breakdown voltage	V _{(BR)DS}	V _{GS} = 0V, I _D = 10A	-	700	1	٧
Zero gate voltage drain current	I _{DSS}	$V_{DS} = 600V, V_{GS} = 0V$ $T_j = 25^{\circ}C$ $T_j = 125^{\circ}C$	-	0.1	100 1000	μΑ
Gate - Source leakage current	I _{GSS}	V _{GS} = ±30V, V _{DS} = 0V	-	-	±100	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} = 10V, I _D = 1mA	2.5	-	4.5	V
Static drain - source on - state resistance	R _{DS(on)} *6	$V_{GS} = 10V, I_D = 10A$ $T_j = 25^{\circ}C$ $T_j = 125^{\circ}C$	-	0.19 0.37	0.25	Ω
Gate input resistance	R_{G}	f = 1MHz, open drain	-	13.4	-	Ω

● Electrical characteristics (T_a = 25°C)

Darameter	Cumb of	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Uffil
Transconductance	g _{fs} *6	V _{DS} = 10V, I _D = 10A	7	14	-	S
Input capacitance	C _{iss}	V _{GS} = 0V	-	2040	-	
Output capacitance	C _{oss}	V _{DS} = 25V	ı	1660	1	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	1	70	1	
Effective output capacitance, energy related	C _{o(er)}	V _{GS} = 0V,	-	69.8	-	
Effective output capacitance, time related	C _{o(tr)}	V _{DS} = 0V to 480V	-	259	-	- pF
Turn - on delay time	t _{d(on)} *6	$V_{DD} \simeq 300V$, $V_{GS} = 10V$	-	40	-	
Rise time	t _r *6	I _D = 10A	-	60	-	20
Turn - off delay time	t _{d(off)} *6	$R_L = 30\Omega$	-	230	460	ns
Fall time	t _f *6	$R_G = 10\Omega$		70	140	

● Gate charge characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			1.114
		Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Q_g^{*6}	V _{DD} ≈ 300V	-	65	-	
Gate - Source charge	Q _{gs} *6	I _D = 20A	-	10	-	nC
Gate - Drain charge	Q _{gd} *6	V _{GS} = 10V	-	25	-	
Gate plateau voltage	V _(plateau)	V _{DD} ≈ 300V, I _D = 20A	-	5.9	-	V

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} L $^{\sim}$ 500 μ H, V_{DD}=50V, R_G=25 Ω , starting T_j=25 $^{\circ}$ C

^{*4} L $^{\sim}500\mu\text{H}$, V_{DD}=50V, R_G=25 Ω , starting T_i=25 $^{\circ}$ C, f = 10kHz

^{*5} Reference measurement circuits Fig.5-1.

^{*6} Pulsed

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Daramatar	Cymah al	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Oriil	
Inverse diode continuous, forward current	I _S *1	T - 25°0	-	-	20	А	
Inverse diode direct current, pulsed	I _{SM} *2	T _C = 25°C	-	-	80	А	
Forward voltage	V _{SD} *6	V _{GS} = 0V, I _S = 10A	-	-	1.5	V	
Reverse recovery time	t _{rr} *6		-	493	-	ns	
Reverse recovery charge	Q _{rr} *6	I _S = 20A di/dt = 100A/µs	-	7.43	-	μC	
Peak reverse recovery current	I _{rm} *6	- α//αι – 100/-γμ3	-	30.2	-	Α	
Peak rate of fall of reverse recovery current	di _{rr} /dt	T _j = 25°C	-	800	-	A/µs	

● Typical transient thermal characteristics

Symbol	Value	Unit
R _{th1}	0.0462	
R _{th2}	0.17	K/W
R _{th3}	0.6	

Symbol	Value	Unit
C _{th1}	0.00308	
C _{th2}	0.0118	Ws/K
C _{th3}	0.232	

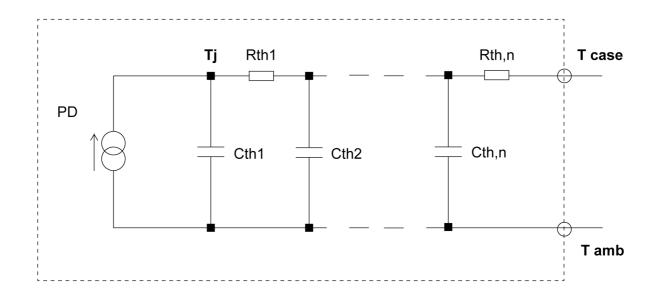


Fig.1 Power Dissipation Derating Curve

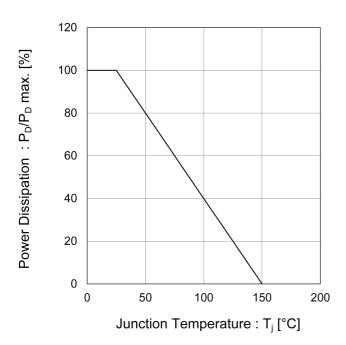


Fig.2 Maximum Safe Operating Area

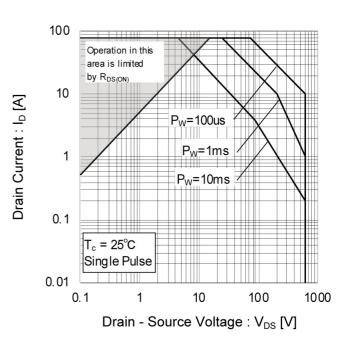


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

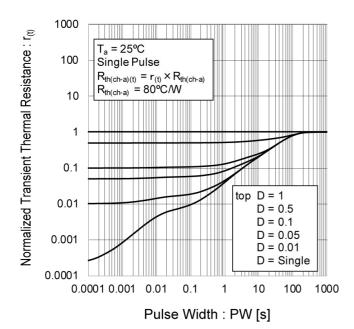


Fig.4 Avalanche Current vs. Inductive Load

14 T_a = 25°C $V_{DD} = 50V$, $R_{G} = 25\Omega$ $V_{GF} = 10V$, $V_{GR} = 0V$ 12 Avalanche Current : IAR [A] 10 8 6 4 2 0 0.01 0.1 10 100 1 Coil Inductance : L [mH]

Fig.5 Avalanche Power Losses

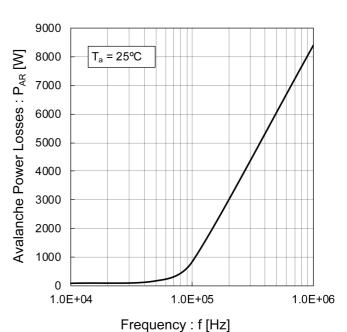


Fig.6 Avalanche Energy Derating Curve vs. Junction Temperature

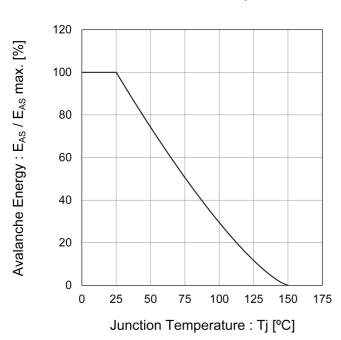


Fig.7 Typical Output Characteristics(I)

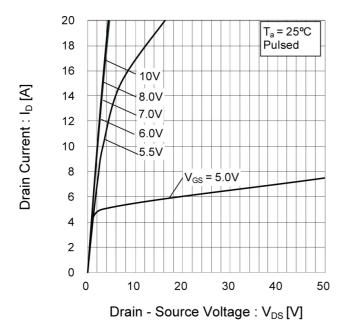
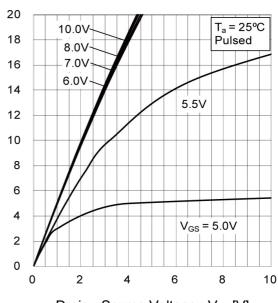


Fig.8 Typical Output Characteristics(II)



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.9 Tj = 150°C Typical Output Characteristics (I)

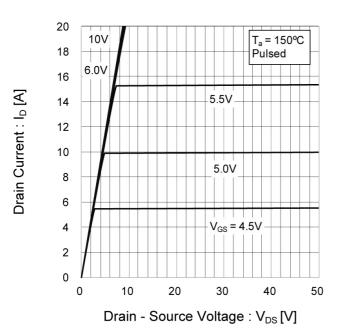
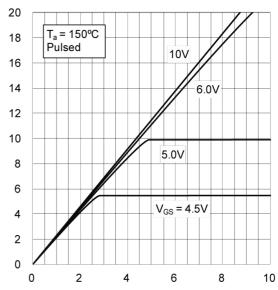


Fig.10 Tj = 150°C Typical Output Characteristics (II)



Drain Current : I_D [A]

Fig.11 Breakdown Voltage vs. Junction Temperature

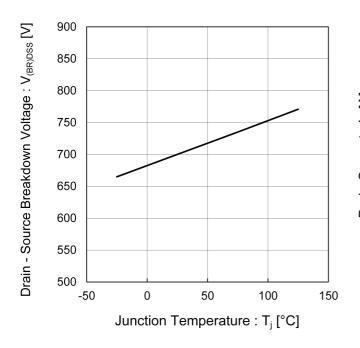


Fig.12 Typical Transfer Characteristics

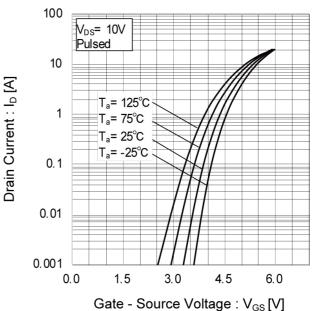


Fig.13 Gate Threshold Voltage vs. Junction Temperature

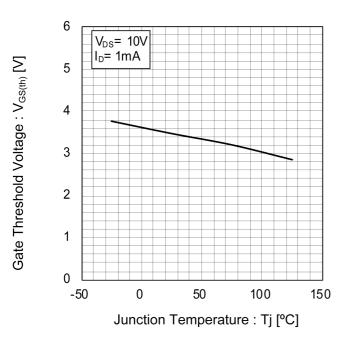


Fig.14 Transconductance vs. Drain Current

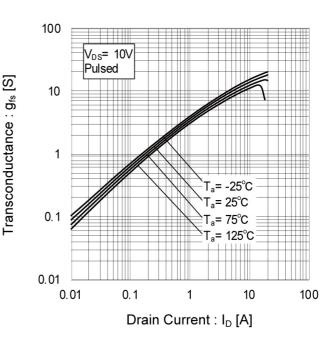


Fig.15 Static Drain - Source On - State Resistance vs. Gate Source Voltage

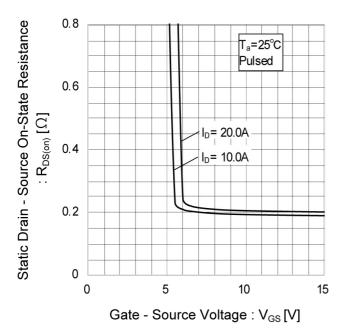


Fig.16 Static Drain - Source On - State Resistance vs. Junction Temperature

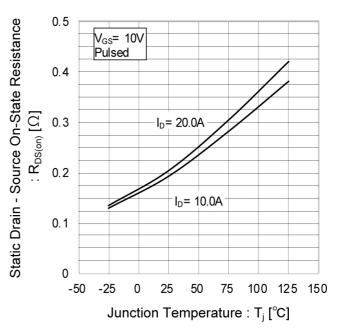


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current

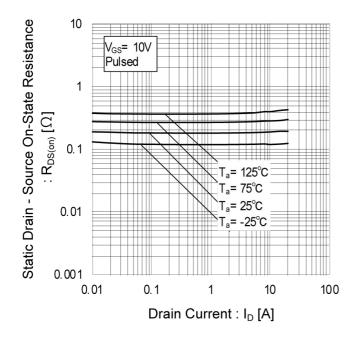


Fig.18 Typical Capacitance vs. Drain - Source Voltage

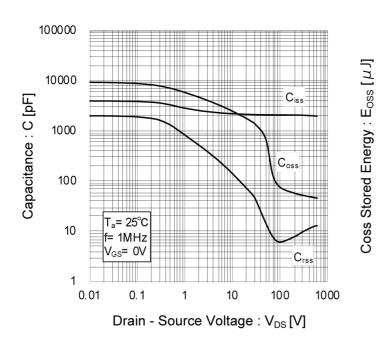


Fig.19 Coss Stored Energy

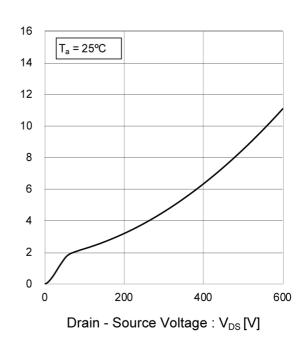


Fig.20 Switching Characteristics

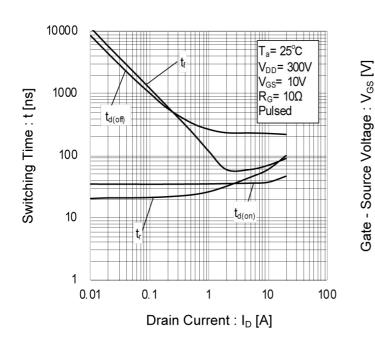


Fig.21 Dynamic Input Characteristics

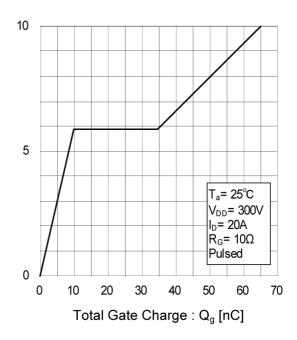


Fig.22 Inverse Diode Forward Current vs. Source - Drain Voltage

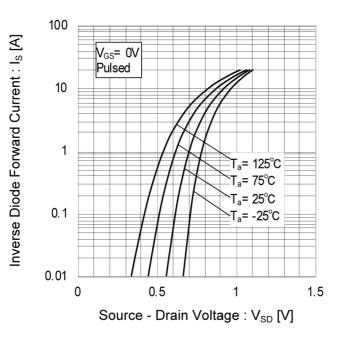
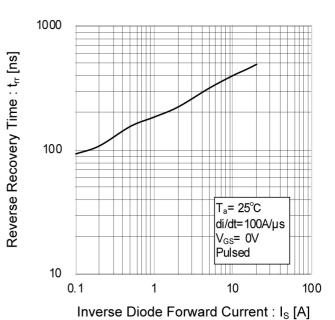


Fig.23 Reverse Recovery Time vs. Inverse Diode Forward Current



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Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

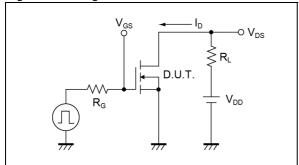


Fig.2-1 Gate Charge Measurement Circuit

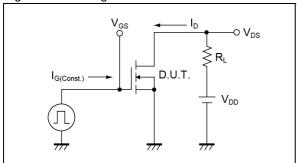


Fig.3-1 Avalanche Measurement Circuit

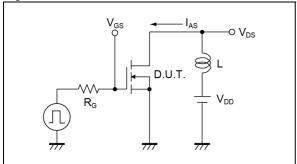


Fig.4-1 dv/dt Measurement Circuit

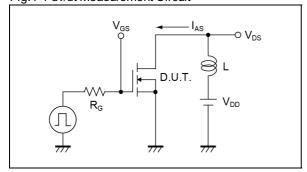


Fig.5-1 di/dt Measurement Circuit

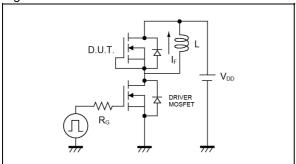


Fig.1-2 Switching Waveforms

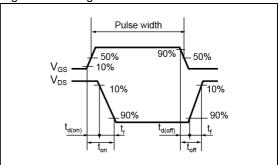


Fig.2-2 Gate Charge Waveform

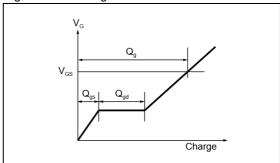


Fig.3-2 Avalanche Waveform

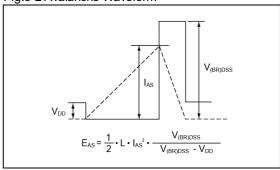


Fig.4-2 dv/dt Waveform

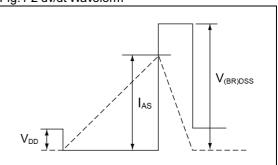
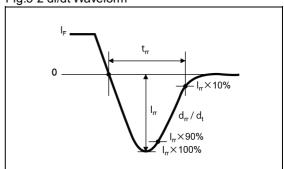
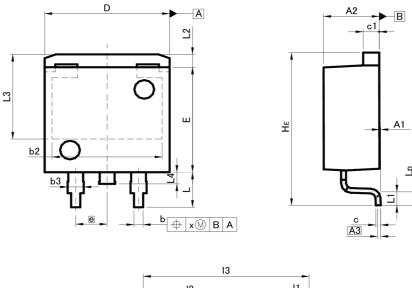


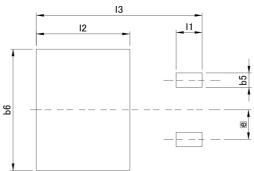
Fig.5-2 di/dt Waveform



Dimensions

LPTS





Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIMETERS		INC	HES
	MIN	MAX	MIN	MAX
A1	0.00	0.30	0.000	0.012
A2	4.30	4.70	0.169	0.185
A3	0.	25	0.0	10
b	0.68	0.98	0.027	0.039
b2	8.	90	0.3	50
b3	1.14	1.44	0.045	0.057
С	0.30	0.60	0.012	0.024
c1	1.10	1.50	0.043	0.059
D	9.80	10.40	0.386	0.409
E	8.80	9.20	0.346	0.362
е	2.	54	0.1	00
HE	12.80	13.40	0.504	0.528
L	2.70	3.30	0.106	0.130
L1	0.90	1.50	0.035	0.059
L2	1.	10	0.0	43
L3	7.	25	0.2	85
L4	1.	00	0.0	39
Lp	0.90	1.50	0.035	0.059
Х	-	0.25	ı	0.010

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b5	-	1.23	-	0.049
b6	-	10.40	_	0.409
- 11	-	2.10	ı	0.083
12	-	7.55	ı	0.297
13	-	13.40	-	0.528

Dimension in mm/inches



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«**FORSTAR**» (основан в 1998 г.)

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