

Overview

KEMET's COG dielectric features a 125°C maximum operating temperature and is considered "stable." The Electronics Components, Assemblies & Materials Association (EIA) characterizes COG dielectric as a Class I material. Components of this classification are temperature compensating and are suited for resonant circuit applications or those where Q and stability of capacitance characteristics are required. C0G exhibits no change in capacitance with respect to time and voltage and boasts a negligible change in capacitance with reference to ambient temperature. Capacitance change is limited to ±30 ppm/°C from -55°C to +125°C.

Benefits

- -55°C to +125°C operating temperature range
- · Lead (Pb)-Free, RoHS, and REACH compliant
- EIA 0201, 0402, 0603, 0805, 1206, 1210, 1808, 1812, 1825, 2220, and 2225 case sizes
- DC voltage ratings of 10 V, 16 V, 25 V, 50 V, 100 V, 200 V and 250 V
- Capacitance offerings ranging from 0.5 pF up to 0.47 μF
- Available capacitance tolerances of ± 0.10 pF, ± 0.25 pF, ± 0.5 pF, $\pm 1\%,$ $\pm 2\%,$ $\pm 5\%,$ $\pm 10\%,$ and $\pm 20\%$
- · No piezoelectric noise
- · Extremely low ESR and ESL
- High thermal stability
- High ripple current capability



С	1206	С	104	J	3	G	Α	С	TU
Ceramic	Case Size (L" x W")	Specification/ Series ¹	Capacitance Code (pF)	Capacitance Tolerance ²	Voltage	Dielectric	Failure Rate/ Design	Termination Finish ³	Packaging/Grade (C-Spec) ⁴
	0201 0402 0603 0805 1206 1210 1808 1812 1825 2220 2225	C = Standard	2 significant digits + number of zeros. Use 9 for 1.0 – 9.9 pF Use 8 for 0.5 – .99 pF e.g., 2.2 pF = 229 e.g., 0.5 pF = 508	$B = \pm 0.10 \text{ pF}$ C = ±0.25 pF D = ±0.5 pF F = ±1% G = ±2% J = ±5% K = ±10% M = ±20%	8 = 10 V 4 = 16 V 3 = 25 V 5 = 50 V 1 = 100 V 2 = 200 V A = 250 V	G = C0G	A = N/A	C = 100% Matte Sn	Blank = Bulk TU = 7" Reel Unmarked

Ordering Information

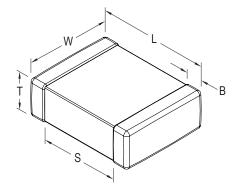
¹ Flexible termination option is available. Please see FT-CAP product bulletin C1062_C0G_FT-CAP_SMD

² Additional capacitance tolerance offerings may be available. Contact KEMET for details.

³ Additional termination finish options may be available. Contact KEMET for details.

⁴ Additional reeling or packaging options may be available. Contact KEMET for details.

Dimensions – Millimeters (Inches)



EIA Size Code	Metric Size Code	L Length	W Width	T Thickness	B Bandwidth	S Separation Minimum	Mounting Technique
0201	0603	0.60 (.024) ± 0.03 (.001)	0.30 (.012) ± 0.03 (.001)		0.15 (.006) ± 0.05 (.002)	N/A	Solder Beflow Only
0402	1005	1.00 (.040) ± 0.05 (.002)	0.50 (.020) ± 0.05 (.002)		0.30 (.012) ± 0.10 (.004)	0.30 (.012)	Solder Reflow Only
0603	1608	1.60 (.063) ± 0.15 (.006)	0.80 (.032) ± 0.15 (.006)		0.35 (.014) ± 0.15 (.006)	0.70 (.028)	
0805	2012	2.00 (.079) ± 0.20 (.008)	1.25 (.049) ± 0.20 (.008)		0.50 (0.02) ± 0.25 (.010)	0.75 (.030)	Solder Wave or Solder Reflow
1206	3216	3.20 (.126) ± 0.20 (.008)	1.60 (.063) ± 0.20 (.008)		0.50 (0.02) ± 0.25 (.010)		
1210	3225	3.20 (.126) ± 0.20 (.008)	2.50 (.098) ± 0.20 (.008)	See Table 2 for Thickness	0.50 (0.02) ± 0.25 (.010)		
1808	4520	4.70 (.185) ± 0.50 (.020)	2.00 (.079) ± 0.20 (.008)		0.60 (.024) ± 0.35 (.014)		
1812	4532	4.50 (.177) ± 0.30 (.012)	3.20 (.126) ± 0.30 (.012)		0.60 (.024) ± 0.35 (.014)	N/A	Ostidas Deflava Osta
1825	4564	4.50 (.177) ± 0.30 (.012)	6.40 (.252) ± 0.40 (.016)	1	0.60 (.024) ± 0.35 (.014)		Solder Reflow Only
2220	5650	5.70 (.224) ± 0.40 (.016)	5.00 (.197) ± 0.40 (.016)	1	0.60 (.024) ± 0.35 (.014)		
2225	5664	5.60 (.220) ± 0.40 (.016)	6.40 (.248) ± 0.40 (.016)		0.60 (.024) ± 0.35 (.014)		

Benefits cont'd

- · Preferred capacitance solution at line frequencies and into the MHz range
- · No capacitance change with respect to applied rated DC voltage
- Negligible capacitance change with respect to temperature from -55°C to +125°C
- · No capacitance decay with time
- Non-polar device, minimizing installation concerns
- 100% pure matte tin-plated termination finish allowing for excellent solderability
- SnPb plated termination finish option available upon request (5% minimum)

Applications

Typical applications include critical timing, tuning, circuits requiring low loss, circuits with pulse, high current, decoupling, bypass, filtering, transient voltage suppression, blocking and energy storage.



Qualification/Certification

Commercial Grade products are subject to internal qualification. Details regarding test methods and conditions are referenced in Table 4, Performance and Reliability.

Environmental Compliance

Lead (Pb)-Free, RoHS, and REACH compliant without exemptions.

Electrical Parameters/Characteristics

Item	Parameters/Characteristics
Operating Temperature Range	-55°C to +125°C
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	±30 ppm/°C
Aging Rate (Maximum % Capacitance Loss/Decade Hour)	0%
Dielectric Withstanding Voltage (DWV)	250% of rated voltage (5 ±1 seconds and charge/discharge not exceeding 50 mA)
Dissipation Factor (DF) Maximum Limit @ 25°C	0.1%
Insulation Resistance (IR) Limit @ 25°C	1,000 megohm microfarads or 100 G Ω (Rated voltage applied for 120 ±5 seconds @ 25°C)

To obtain IR limit, divide $M\Omega$ - μ F value by the capacitance and compare to $G\Omega$ limit. Select the lower of the two limits.

Capacitance and Dissipation Factor (DF) measured under the following conditions:

1 MHz ±100 kHz and 1.0 Vrms ±0.2 V if capacitance \leq 1,000 pF

1 kHz \pm 50 Hz and 1.0 Vrms \pm 0.2 V if capacitance > 1,000 pF

Note: When measuring capacitance it is important to ensure the set voltage level is held constant. The HP4284 and Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON."

Post Environmental Limits

	High Temperatu	ıre Life, Biased	Humidity, Mois	ture Resistance	•
Dielectric	Rated DC Voltage	Capacitance Value	Dissipation Factor (Maximum %)	Capacitance Shift	Insulation Resistance
C0G	All	All	0.5	0.3% or ±0.25 pF	10% of Initial Limit



Table 1A – Capacitance Range/Selection Waterfall (0201 – 1206 Case Sizes)

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0.75 pF	758													BB	BB								CF	CF	CF				DN			DN										
1.0 – 9.1 pF* 1.1 pF	109 – 919* 119	B												BB BB	BB BB								CF CF	CF CF	CF CF		CF		DN DN			DN DN		DN		EB EB	EB EB		EB EB	EB EB	EB EB	
1.1 pF 1.2 pF	129													BB	BB								CF	CF	CF	1.1						DN				EB			EB			
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1.5 pF	159	В												BB	BB							CF	CF	CF	CF		CF		DN			DN		DN		EB	EB		EB		EB	
1.6 pF	169	В) C)									BB	BB	BB	BB					CF	CF	CF	CF	CF	CF	=	DN	DN	DN	DN	DN	DN	1	EB	EB		EB	EB	EB	
1.8 pF	189	В	3 0) C)									BB	BB	BB	BB					CF	CF	CF	CF	CF	CF	=	DN	DN	DN	DN	DN	DN	1	EB	EB		EB	EB	EB	
2.0 pF	209	В	-	_	_									BB	BB			_			_		CF	CF	CF	_	_	_	DN	_	_	DN	_	DN	_	EB	EB		EB	EB	EB	
2.2 pF	229	B												BB		BB						CF	CF	CF	CF		CF		DN			DN				EB		EB		EB		
2.4 pF	249	B												BB	BB								CF	CF	CF				DN			DN				EB	EB			EB	EB	
2.7 pF 3.0 pF	279 309	B												BB BB	BB BB							CF CF	CF CF	CF CF	CF CF		CF CF		DN DN			DN DN				EB EB	EB	EB EB		EB EB		
3.3 pF	309	B												BВ		BB						CF	CF	CF	CF							DN				EB			EB	EB		
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3.9 pF	399	В												BB		BB							CF	CF	CF				DN			DN		DN		EB	EB		EB		EB	
4.3 pF	439	В) D)									BB	BB	BB	BB					CF	CF	CF	CF	CF	CF	=	DN	DN	DN	DN	DN	DN	1	EΒ	EB	EB	EB	EB	EB	
4.7 pF	479	В	C											BB	BB	BB	BB					CF	CF	CF	CF	CF	CF	=	DN	DN	DN	DN	DN	DN	1	EB	EB	EB	EB	EB	EB	
5.1 pF	519	В	-	_	_									BB	BB			_				CF	CF	CF	CF	CF	-	_	DN		_	DN	_	_	_	EB	EB	EB	EB	EB	EB	
5.6 pF	569	B												BB		BB						CF	CF	CF	CF	-	-			DN						EB	EB		EB	EB	EB	
6.2 pF	629	B												BB	BB							CF	CF	CF	CF	1.1	1.1		DN		1	DN			-	EB	EB		EB	EB	EB	
6.8 pF	689	B												BB BB	BB BB		BB					CF CF	CF CF	CF CF	CF CF	-	-			DN		DN				EB EB	EB	EB EB	EB EB	EB EB		
7.5 pF 8.2 pF	759 829	B												BВ		BB						CF	CF	CF	CF											EB			EB	EB		
9.1 pF	919						Т	Т	Т	Г				BB	BB	_					_	CF	CF	CF	CF	-			DN			DN				EB	EB				EB	
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12 pF	120						G				3² A	B²	AB²			BB						CF	CF	CF	CF		-	=	DN	DN	DN	DN	DN	DN	1	EB	EB		EB			
13 pF	130	L			_	_	G	_	< M					BB	BB	_	_	_				CF	CF	CF	CF	_	_	_	DN	_	_	DN	_	DN	_	EB	EB	EB	EB	EB	EB	
15 pF	150				F		GJ				3² A	.B²	AB²		BB							CF	CF	CF	CF	-	-			DN						EB	EB		EB	EB	EB	
16 pF	160				F		GJ				D2 A	D2	AB²	BB	BB							CF CF	CF CF	CF CF	CF CF	CF			DN			DN DN				EB EB	EB EB		EB EB	EB	EB EB	
18 pF 20 pF	180 200				F		G J G J				A	D-	AD*	BB	BB BB							CF	CF	CF	CF	-	-		DN DN			DN				EB	EB		EB	EB EB	EB	
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27 pF	270				F		G				3² A	B²	AB²		BB							CF	CF	CF	CF			:	DN	DN	DN	DN	DN	DN	1	EB	EB		EB	EB	EB	
30 pF	300				F		G		(M					BB	BB							CF	CF	CF	CF	CF			DN			DN		DN		EB	EB	EB	EB	EB	EB	
33 pF	330						G				3² A	B²	AB²		BB							CF	CF	CF	CF	CF			DN			DN		DN		EB	EB		EB		EB	
36 pF	360								K M		2 .	D 2	A D 2				BB						CF	CF	CF					DN						EB			EB			
39 pF	390						j .		K M K M	A	5 ² A	B ²	AB²				BB							CF			CF			DN						EB		EB		EB		
43 pF 47 pF	430 470						G	J P J L			22 A	R ²	ΔR ²				BB							CF CF						DN DN						EB			EB EB			
47 pF 51 pF	470 510					= (K M								BB													DN									EB			
56 pF	560				F	= 0	3.		M	A	32 A	B ²	AB²	BB	BB	BB	BB							CF						DN									EB			
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68 pF	680						G	J۴	(M	A	3² A	B²		BB	BB	BB	BB					CF	CF	CF	CF	CF	CF	=	DN	DN	DN	DN	DN	DN	1	EB	EB	EB	EB	EB	EB	
75 pF	750						G							BB	BB	BB	BB							CF						DN									EB			
82 pF	820										3² A	B²	AB²	BB	BB	BB	BB							CF						DN									EB			
91 pF	910						GJ				72 4	D2	102	BB	BB	BB	BB	D												DN						EB	EB	EB	EB	EB	EB	
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*Capacitance range Includes E24 decade values only. (i.e., 10, 11, 12, 13, 15, 16, 18, 20, 22, 24, 27, 30, 33, 36, 39, 43, 47, 51, 56, 62, 68, 75, 82 and 91).

 xx^1 Available only in D, J, K,M tolerance

 xx^2 Available only in J, K, M tolerance.

These products are protected under US Patents 7,172,985 & 7,670,981, other patents pending, and any foreign counterparts.



Table 1A – Capacitance Range/Selection Waterfall (0201 – 1206 Case Sizes) cont'd

Cap Code Series CU2/IC CU3/IC CU3/IC CU3/IC CU3/IC CU2/IC Image:			Case Size/																									Γ						
Code Vertige Code i				C0	20	1C			C0	40	2C					C)60	3C					C)80	5C					C	120	6C		
Code Rated Values (NOC) P R P R	Сар		Voltage Code	8	4	3	8	4	3	5	1	2	Α	8	4	3	5	1	2	A	8	4	3	5	1	2	Α	8	4	3	5	1	2	A
Capacitance Product Availability and Chip Thickness Dimensions 110-180 pF 111-181 F G j k k in BB BB BB BB BB C C C C C C C C C C C C		Code	Rated Voltage (VDC)	ę	16	25	10	16	25	50	100	200	250	÷	16	25	50	<u>1</u> 0	200	250	ę	16	25	50	1 0	200	250	5	16	25	50	<u>10</u>	200	250
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1 800 pF 162 F G J K M BB BB BB BB CF <	1,300 pF	132												CF		CF		-		_		_	_							EB	EB	EC	EC	
1800 pF 182 F G J K M BB BB <t< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>BB</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>1.1</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></t<>										BB									1.1															
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*Capacitance range Includes E24 decade values only. (i.e., 10, 11, 12, 13, 15, 16, 18, 20, 22, 24, 27, 30, 33, 36, 39, 43, 47, 51, 56, 62, 68, 75, 82 and 91). xx¹ Available only in D, J, K,M tolerance

xx² Available only in J, K, M tolerance.

These products are protected under US Patents 7,172,985 & 7,670,981, other patents pending, and any foreign counterparts.



Table 1B – Capacitance Range/Selection Waterfall (1210 – 2225 Case Sizes)

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*Capacitance range Includes E24 decade values only. (i.e., 10, 11, 12, 13, 15, 16, 18, 20, 22, 24, 27, 30, 33, 36, 39, 43, 47, 51, 56, 62, 68, 75, 82 and 91). These products are protected under US Patents 7,172,985 & 7,670,981, other patents pending, and any foreign counterparts.



Table 2 – Chip Thickness/Packaging Quantities

Thickness	Case	Thickness ±	Paper G	Quantity	Plastic (Quantity			
Code	Size	Range (mm)	7" Reel	13" Reel	7" Reel	13" Reel			
AB BB BD CF CH	0201 0402 0402 0603 0603	$\begin{array}{c} 0.30 \pm 0.03 \\ 0.50 \pm 0.05 \\ 0.55 \pm 0.05 \\ 0.80 \pm 0.07^* \\ 0.85 \pm 0.07 \end{array}$	15,000 10,000 10,000 4,000 4,000 4,000	0 50,000 50,000 15,000 10,000	0 0 0 0 0	0 0 0 0 0			
DM DN DP DE DF	0805 0805 0805 0805 0805 0805	$\begin{array}{c} 0.00 \pm 0.01 \\ 0.70 \pm 0.20^{*} \\ 0.78 \pm 0.10^{*} \\ 0.90 \pm 0.10^{*} \\ 1.00 \pm 0.10 \\ 1.10 \pm 0.10 \end{array}$	4,000 4,000 4,000 0 0	15,000 15,000 15,000 0 0	0 0 2,500 2,500	0 0 0 10,000 10,000			
DG EB EC ED EE	0805 1206 1206 1206 1206	$\begin{array}{c} 1.10 \pm 0.10 \\ 1.25 \pm 0.15 \\ 0.78 \pm 0.10 \\ 0.90 \pm 0.10 \\ 1.00 \pm 0.10 \\ 1.10 \pm 0.10 \end{array}$	0 4,000 0 0 0	0 10,000 0 0 0	2,500 4,000 4,000 2,500 2,500	10,000 10,000 10,000 10,000 10,000 10,000			
EF EH FB FC FE	1206 1206 1210 1210 1210	$\begin{array}{c} 1.20 \pm 0.15 \\ 1.60 \pm 0.20 \\ 0.78 \pm 0.10 \\ 0.90 \pm 0.10 \\ 1.00 \pm 0.10 \end{array}$	0 0 0 0 0	0 0 0 0 0	2,500 2,000 4,000 4,000 2,500	10,000 8,000 10,000 10,000 10,000			
FF FG FH FM FJ	1210 1210 1210 1210 1210 1210	$\begin{array}{c} 1.10 \pm 0.10 \\ 1.25 \pm 0.15 \\ 1.55 \pm 0.15 \\ 1.70 \pm 0.20 \\ 1.85 \pm 0.20 \end{array}$	0 0 0 0	0 0 0 0 0	2,500 2,500 2,000 2,000 2,000	10,000 10,000 8,000 8,000 8,000 8,000			
FK NC LF GB GD	1210 1706 1808 1812 1812	$\begin{array}{c} 2.10 \pm 0.20 \\ 1.00 \pm 0.15 \\ 1.00 \pm 0.15 \\ 1.00 \pm 0.10 \\ 1.25 \pm 0.15 \end{array}$	0 0 0 0	0 0 0 0 0	2,000 4,000 2,500 1,000 1,000	8,000 10,000 10,000 4,000 4,000			
GH GG GK GJ GN	1812 1812 1812 1812 1812 1812	$\begin{array}{c} 1.40 \pm 0.15 \\ 1.55 \pm 0.10 \\ 1.60 \pm 0.20 \\ 1.70 \pm 0.15 \\ 1.70 \pm 0.20 \end{array}$	0 0 0 0	0 0 0 0 0	1,000 1,000 1,000 1,000 1,000 1,000	4,000 4,000 4,000 4,000 4,000			
GM HB HE HG JB	1812 1825 1825 1825 2220	$2.00 \pm 0.20 1.10 \pm 0.15 1.40 \pm 0.15 1.60 \pm 0.20 1.00 \pm 0.15 $	0 0 0 0	0 0 0 0 0	500 1,000 1,000 1,000 1,000	2,000 4,000 4,000 4,000 4,000			
JD JE JG JL	2220 2220 2220 2220 2220 2220	$\begin{array}{c} 1.30 \pm 0.15 \\ 1.40 \pm 0.15 \\ 1.50 \pm 0.15 \\ 1.70 \pm 0.15 \\ 2.00 \pm 0.20 \end{array}$	0 0 0 0	0 0 0 0 0	1,000 1,000 1,000 1,000 500	4,000 4,000 4,000 4,000 2,000			
KE	2225	1.40 ± 0.15	0 7" Reel	0 13" Reel	1,000 4,00 7" Reel 13" R				
Thickness Code	Case Size	Thickness ± Range (mm)		Quantity	7" Reel 13" Ree 13" Ree Plastic Quantity				

Package quantity based on finished chip thickness specifications.



Table 3 – Chip Capacitor Land Pattern Design Recommendations per IPC–7351

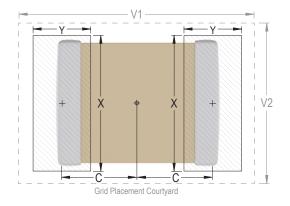
EIA Size Code	Metric Size Code			sity Lev mum (I rotrusio	/lost))		Media	sity Lev an (Nor rotrusio)			sity Lev num (L rotrusio	east))
oouc	oode	С	Y	X	V1	V2	С	Y	X	V1	V2	С	Y	X	V1	V2
0201	0603	0.38	0.56	0.52	1.80	1.00	0.33	0.46	0.42	1.50	0.80	0.28	0.36	0.32	1.20	0.60
0402	1005	0.50	0.72	0.72	2.20	1.20	0.45	0.62	0.62	1.90	1.00	0.40	0.52	0.52	1.60	0.80
0603	1608	0.90	1.15	1.10	4.00	2.10	0.80	0.95	1.00	3.10	1.50	0.60	0.75	0.90	2.40	1.20
0805	2012	1.00	1.35	1.55	4.40	2.60	0.90	1.15	1.45	3.50	2.00	0.75	0.95	1.35	2.80	1.70
1206	3216	1.60	1.35	1.90	5.60	2.90	1.50	1.15	1.80	4.70	2.30	1.40	0.95	1.70	4.00	2.00
1210	3225	1.60	1.35	2.80	5.65	3.80	1.50	1.15	2.70	4.70	3.20	1.40	0.95	2.60	4.00	2.90
1210 ¹	3225	1.50	1.60	2.90	5.60	3.90	1.40	1.40	2.80	4.70	3.30	1.30	1.20	2.70	4.00	3.00
1808	4520	2.30	1.75	2.30	7.40	3.30	2.20	1.55	2.20	6.50	2.70	2.10	1.35	2.10	5.80	2.40
1812	4532	2.15	1.60	3.60	6.90	4.60	2.05	1.40	3.50	6.00	4.00	1.95	1.20	3.40	5.30	3.70
1825	4564	2.15	1.60	6.90	6.90	7.90	2.05	1.40	6.80	6.00	7.30	1.95	1.20	6.70	5.30	7.00
2220	5650	2.75	1.70	5.50	8.20	6.50	2.65	1.50	5.40	7.30	5.90	2.55	1.30	5.30	6.60	5.60
2225	5664	2.70	1.70	6.90	8.10	7.90	2.60	1.50	6.80	7.20	7.30	2.50	1.30	6.70	6.50	7.00

¹ Only for capacitance values \geq 22 μ F

Density Level A: For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes. KEMET only recommends wave soldering of EIA 0603, 0805 and 1206 case sizes.

Density Level B: For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes. **Density Level C:** For high component density product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC Standard 7351 (IPC–7351).

Image below based on Density Level B for an EIA 1210 case size.





Soldering Process

Recommended Soldering Technique:

- Solder wave or solder reflow for EIA case sizes 0603, 0805 and 1206
- · All other EIA case sizes are limited to solder reflow only

Recommended Reflow Soldering Profile:

KEMET's families of surface mount multilayer ceramic capacitors (SMD MLCCs) are compatible with wave (single or dual), convection, IR or vapor phase reflow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET's recommended profile conditions for convection and IR reflow reflect the profile conditions of the IPC/J-STD-020 standard for moisture sensitivity testing. These devices can safely withstand a maximum of three reflow passes at these conditions.

Profile Feature	Terminati	on Finish
FIONETeature	SnPb	100% Matte Sn
Preheat/Soak		
Temperature Minimum (T _{Smin})	100°C	150°C
Temperature Maximum (T _{Smax})	150°C	200°C
Time (t _s) from T_{Smin} to T_{Smax}	60 – 120 seconds	60 – 120 seconds
Ramp-Up Rate $(T_L \text{ to } T_P)$	3°C/second maximum	3°C/second maximum
Liquidous Temperature (T_L)	183°C	217°C
Time Above Liquidous (t_L)	60 – 150 seconds	60 – 150 seconds
Peak Temperature (T _P)	235°C	260°C
Time Within 5°C of Maximum Peak Temperature (t _P)	20 seconds maximum	30 seconds maximum
Ramp-Down Rate $(T_P \text{ to } T_L)$	6°C/second maximum	6°C/second maximum
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum

Note 1: All temperatures refer to the center of the package, measured on the capacitor body surface that is facing up during assembly reflow.

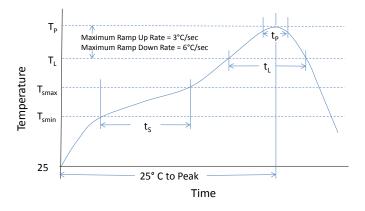




Table 4 – Performance & Reliability: Test Methods and Conditions

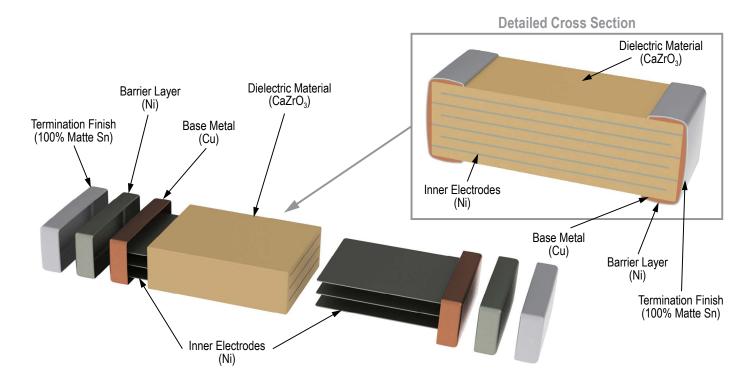
Stress	Reference	Test or Inspection Method
Terminal Strength	JIS-C-6429	Appendix 1, Note: Force of 1.8 kg for 60 seconds.
Board Flex	JIS-C-6429	Appendix 2, Note: Standard termination system – 2.0 mm (minimum) for all except 3 mm for C0G. Flexible termination system – 3.0 mm (minimum).
		Magnification 50 X. Conditions:
Solderability	J-STD-002	a) Method B, 4 hours @ 155°C, dry heat @ 235°C
Solderability	J-31D-002	b) Method B @ 215°C category 3
		c) Method D, category 3 @ 260°C
Temperature Cycling	JESD22 Method JA-104	1,000 Cycles (-55°C to +125°C). Measurement at 24 hours +/- 2 hours after test conclusion.
Die er del hansi dite a		Load Humidity: 1,000 hours 85°C/85% RH and rated voltage. Add 100 K ohm resistor. Measurement at 24 hours +/- 2 hours after test conclusion.
Biased Humidity	MIL-STD-202 Method 103	Low Volt Humidity: 1,000 hours 85°C/85% RH and 1.5 V. Add 100 K ohm resistor. Measurement at 24 hours +/- 2 hours after test conclusion.
Moisture Resistance	MIL-STD-202 Method 106	t = 24 hours/cycle. Steps 7a and 7b not required. Unpowered. Measurement at 24 hours +/- 2 hours after test conclusion.
Thermal Shock	MIL-STD-202 Method 107	-55°C/+125°C. Note: Number of cycles required – 300, maximum transfer time – 20 seconds, dwell time – 15 minutes. Air – Air.
High Temperature Life	MIL–STD–202 Method 108 /EIA–198	1,000 hours at 125°C (85°C for X5R, Z5U and Y5V) with 2 X rated voltage applied.
Storage Life	MIL-STD-202 Method 108	150°C, 0 VDC for 1,000 hours.
Vibration	MIL-STD-202 Method 204	5 g's for 20 min., 12 cycles each of 3 orientations. Note: Use 8" X 5" PCB 0.031" thick 7 secure points on one long side and 2 secure points at corners of opposite sides. Parts mounted within 2" from any secure point. Test from 10 – 2,000 Hz
Mechanical Shock	MIL-STD-202 Method 213	Figure 1 of Method 213, Condition F.
Resistance to Solvents	MIL-STD-202 Method 215	Add aqueous wash chemical, OKEM Clean or equivalent.

Storage and Handling

Ceramic chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature– reels may soften or warp and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40°C and maximum storage humidity not exceed 70% relative humidity. Temperature fluctuations should be minimized to avoid condensation on the parts and atmospheres should be free of chlorine and sulfur bearing compounds. For optimized solderability chip stock should be used promptly, preferably within 1.5 years of receipt.



Construction



Capacitor Marking (Optional):

Laser marking option is not available on:

- C0G, Ultra Stable X8R and Y5V dielectric devices
- · EIA 0402 case size devices
- EIA 0603 case size devices with Flexible Termination option.
- KPS Commercial and Automotive grade stacked devices.

These capacitors are supplied unmarked only.

Tape & Reel Packaging Information

KEMET offers multilayer ceramic chip capacitors packaged in 8, 12 and 16 mm tape on 7" and 13" reels in accordance with EIA Standard 481. This packaging system is compatible with all tape-fed automatic pick and place systems. See Table 2 for details on reeling quantities for commercial chips.

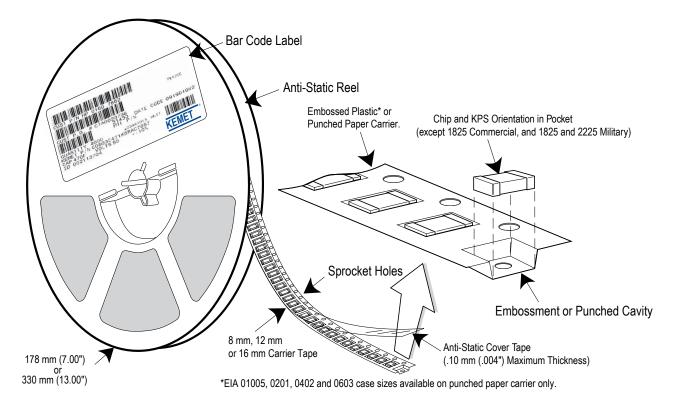


Table 5 – Carrier Tape Configuration, Embossed Plastic & Punched Paper (mm)

		Embosse	ed Plastic	Punche	d Paper
EIA Case Size	Tape size (W)*	7" Reel	13" Reel	7" Reel	13" Reel
		Pitch	(P ₁)*	Pitch	(P ₁)*
01005 – 0402	8			2	2
0603	8			4	4
0805	8	4	4	4	4
1206 – 1210	8	4	4	4	4
1805 – 1808	12	4	4		
≥ 1812	12	8	8		
KPS 1210	12	8	8		
KPS 1812 & 2220	16	12	12		
Array 0508 & 0612	8	4	4		

*Refer to Figures 1 & 2 for W and P₁ carrier tape reference locations. *Refer to Tables 6 & 7 for tolerance specifications.



Figure 1 – Embossed (Plastic) Carrier Tape Dimensions

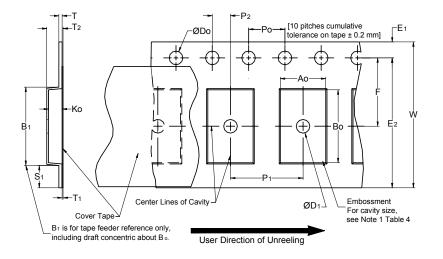


Table 6 – Embossed (Plastic) Carrier Tape Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)									
Tape Size	D ₀	D ₁ Minimum Note 1	E ₁	P ₀	P ₂	R Reference Note 2	S ₁ Minimum Note 3	T Maximum	T ₁ Maximum
8 mm	1.5 +0.10/-0.0 (0.059 +0.004/-0.0)	1.0 (0.039)		4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	25.0 (0.984)	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)
12 mm		1.5	1.75 ±0.10 (0.069 ±0.004)			30 (1.181)			
16 mm		(0.059)							
	Variable Dimensions — Millimeters (Inches)								
Tape Size	Pitch	B ₁ Maximum Note 4	E ₂ Minimum	F	P ₁	T ₂ Maximum	W Maximum	A ₀ ,B ₀	& K ₀
8 mm	Single (4 mm)	4.35 (0.171)	6.25 (0.246)	3.5 ±0.05 (0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	2.5 (0.098)	8.3 (0.327)		
12 mm	Single (4 mm) & Double (8 mm)	8.2 (0.323)	10.25 (0.404)	5.5 ±0.05 (0.217 ±0.002)	8.0 ±0.10 (0.315 ±0.004)	4.6 (0.181)	12.3 (0.484)	Note 5	
16 mm	Triple (12 mm)	12.1 (0.476)	14.25 (0.561)	7.5 ±0.05 (0.138 ±0.002)	12.0 ±0.10 (0.157 ±0.004)	4.6 (0.181)	16.3 (0.642)		

1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.

2. The tape with or without components shall pass around R without damage (see Figure 6).

3. If S₁ < 1.0 mm, there may not be enough area for cover tape to be properly applied (see EIA Standard 481 paragraph 4.3 section b).

4. B, dimension is a reference dimension for tape feeder clearance only.

5. The cavity defined by A_{α} , B_{α} and K_{α} shall surround the component with sufficient clearance that:

(a) the component does not protrude above the top surface of the carrier tape.

(b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.

(c) rotation of the component is limited to 20° maximum for 8 and 12 mm tapes and 10° maximum for 16 mm tapes (see Figure 3).

(d) lateral movement of the component is restricted to 0.5 mm maximum for 8 and 12 mm wide tape and to 1.0 mm maximum for 16 mm tape (see Figure 4).

(e) for KPS Series product, A_0 and B_0 are measured on a plane 0.3 mm above the bottom of the pocket.

(f) see Addendum in EIA Standard 481 for standards relating to more precise taping requirements.



Figure 2 – Punched (Paper) Carrier Tape Dimensions

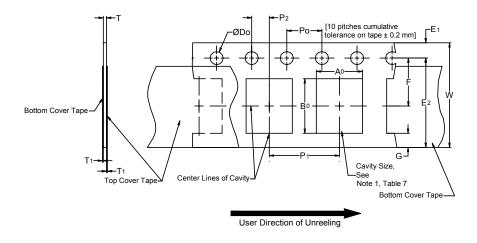


Table 7 – Punched (Paper) Carrier Tape Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)								
Tape Size	D ₀	E ₁	P ₀	P ₂	T₁ Maximum	G Minimum	R Reference Note 2	
8 mm	1.5 +0.10 -0.0 (0.059 +0.004 -0.0)	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	0.10 (0.004) Maximum	0.75 (0.030)	25 (0.984)	
	Variable Dimensions — Millimeters (Inches)							
Tape Size	Pitch	E2 Minimum	F	P ₁	T Maximum	W Maximum	A ₀ B ₀	
8 mm	Half (2 mm)	6.25	3.5 ±0.05 (0.138 ±0.002)	2.0 ±0.05 (0.079 ±0.002)	1.1	8.3 (0.327)	Note 1	
8 mm	Single (4 mm)	(0.246)		4.0 ±0.10 (0.157 ±0.004)	(0.098)	8.3 (0.327)		

1. The cavity defined by A_{α} , B_{α} and T shall surround the component with sufficient clearance that:

a) the component does not protrude beyond either surface of the carrier tape.

b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.

c) rotation of the component is limited to 20° maximum (see Figure 3).

d) lateral movement of the component is restricted to 0.5 mm maximum (see Figure 4).

e) see Addendum in EIA Standard 481 for standards relating to more precise taping requirements.

2. The tape with or without components shall pass around R without damage (see Figure 6).



Packaging Information Performance Notes

- 1. Cover Tape Break Force: 1.0 Kg minimum.
- 2. Cover Tape Peel Strength: The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength			
8 mm	0.1 to 1.0 Newton (10 to 100 gf)			
12 and 16 mm	0.1 to 1.3 Newton (10 to 130 gf)			

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165° to 180° from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of 300 ± 10 mm/minute. **3. Labeling:** Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. *Refer to EIA Standards* 556 *and* 624.

Figure 3 – Maximum Component Rotation

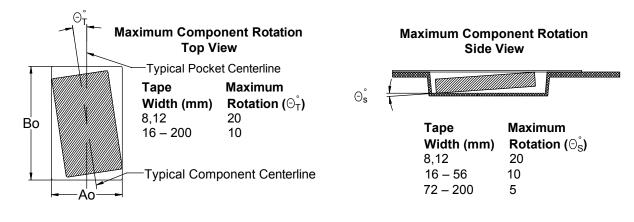


Figure 4 – Maximum Lateral Movement

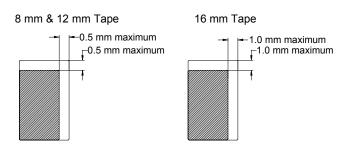


Figure 5 – Bending Radius

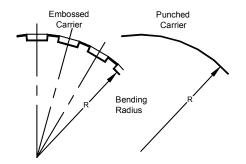
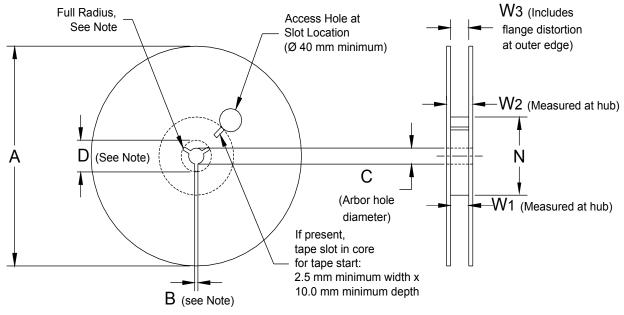




Figure 6 – Reel Dimensions



Note: Drive spokes optional; if used, dimensions B and D shall apply.

Table 8 – Reel Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)								
Tape Size	A	B Minimum	С	D Minimum				
8 mm	178 ±0.20							
12 mm	(7.008 ±0.008) or	1.5 (0.059)	13.0 +0.5/-0.2 (0.521 +0.02/-0.008)	20.2 (0.795)				
16 mm	330 ±0.20 (13.000 ±0.008)	, , , , , , , , , , , , , , , , , , ,	,					
	Variable Dimensions — Millimeters (Inches)							
Tape Size	N Minimum	W ₁	W ₂ Maximum	W ₃				
8 mm		8.4 +1.5/-0.0 (0.331 +0.059/-0.0)	14.4 (0.567)					
12 mm	50 (1.969)	12.4 +2.0/-0.0 (0.488 +0.078/-0.0)	18.4 (0.724)	Shall accommodate tape width without interference				
16 mm		16.4 +2.0/-0.0 (0.646 +0.078/-0.0)	22.4 (0.882)					



Figure 7 – Tape Leader & Trailer Dimensions

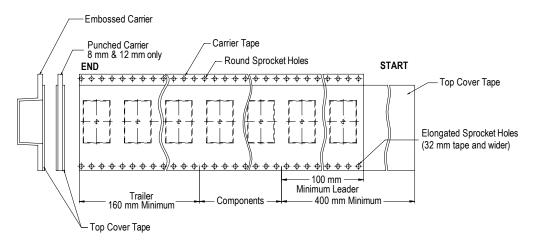
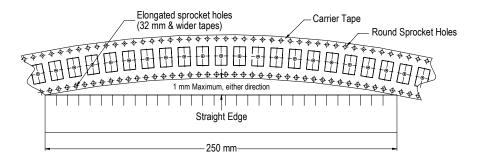
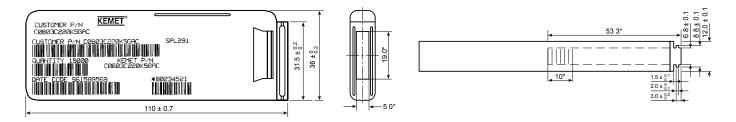


Figure 8 – Maximum Camber



Bulk Cassette Packaging (Ceramic Chips Only)

Meets Dimensional Requirements IEC–286 and EIAJ 7201 Unit mm *Reference



Capacitor Dimensions for Bulk Cassette

Cassette Packaging - Millimeters

EIA Size Code	Metric Size Code	L Length	W Width	B Bandwidth	S Separation Minimum	T Thickness	Number of Pieces/Cassette
0402	1005	1.0 ±0.05	0.5 ±0.05	0.2 to 0.4	0.3	0.5 ±0.05	50,000
0603	1608	1.6 ±0.07	0.8 ±0.07	0.2 to 0.5	0.7	0.8 ±0.07	15,000

Surface Mount Multilayer Ceramic Chip Capacitors (SMD MLCCs) – C0G Dielectric, 10 – 250 VDC (Commercial Grade)



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