Safety Standard Recognized, 900 Series, Radial Disc, Encapsulated, AH Type, X1 400 VAC/Y1 250 VAC (Industrial Grade)

Overview

KEMET's 900 series encapsulated radial through-hole ceramic disc capacitors are specifically designed for interference-suppression AC line filtering applications. Having internationally recognized safety certifications, these capacitors are well-suited for applications that require keeping potentially disruptive or damaging line transients and EMI out of susceptible equipment. They are also an ideal solution when needing to suppress line disturbances at the source.

Safety Certified Capacitors are classified as either X and/or Y capacitors. Class X capacitors are primarily used in line-to line (across-the-line) applications. In this application there is no danger of electric shock to humans should the capacitor fail, but could result in a risk of fire. The class Y capacitor is primarily used in line-to-ground (line by-pass) applications. In this application, failure of the capacitor could lead to danger of electric shock.

With a working voltage of 400 VAC in line-to-line (Class X) and 250 VAC in line-to-ground (Class Y) applications, these safety capacitors meet the impulse test criteria outlined in IEC Standard 60384. Meeting subclass X1 and Y1 requirements, these devices are certified to withstand impulses up to 4 KV (X1) and 8 KV (Y1) respectively. These encapsulated devices also meet the flame test requirements outlined in UL Standard 94V-0.

Electronic Components



Ordering Information

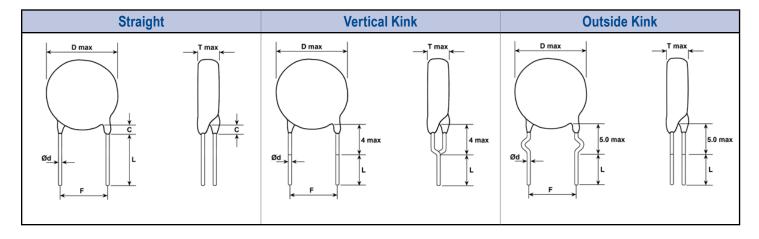
C9	1	1	U	620	J	U	S	D	Α	Α	7317
Ceramic Series	Body Diameter	Lead Spacing ¹	Spec.	Capacitance Code (pF)	Capacitance Tolerance	Rated Voltage	Dielectric/ Temp. Char.	Design	Lead Config. ²	Failure Rate	Packaging (C-Spec) ^{1,2}
C9 = Ceramic 900 Series	0 = 7.0 mm 1 = 8.0 mm 2 = 9.0 mm 3 = 10.0 mm 4 = 11.0 mm 5 = 12.0 mm 7 = 14.0 mm	1 = 10.0 mm	U = Safety	2 significant digits + Number of zeroes Use 9 for 1.0 - 9.9pF e.g., 2.2pF = 229	C = ±0.25pF D = ±0.5pF J = ±5% K = ±10% M = ±10%	U = X1 400 VAC / Y1 250 VAC	N = CH (NP0) S = SL Y = Y5P W = Y5U V = Y5V	D = Disc	A = Straight B = Vertical Kink C = Outside Kink	A = N/A	7317 = Ammo Pack WL30 = Bulk/3.0 mm Lead length WL35 = Bulk/3.5 mm Lead length WL40 = Bulk/4.0 mm Lead length WL45 = Bulk/4.5 mm Lead length WL50 = Bulk/5.0 mm Lead length WL20 = Bulk/20 mm Lead length

¹ "Vertical Kink" and "Outside Kink" lead configurations cannot be combined with the bulk/20 mm lead length option (WL20). 20 mm lead length is only available on capacitors ordered with straight leads (lead configuration ordering code "A"). For nonstandard lead length inquiries, please contact KEMET.

² Bulk packaging lead length availability is dependent upon "Lead Configuration." See "Dimensions" section of this document to verify availability of a specific lead length option. For nonstandard lead length inquiries, please contact KEMET.



Lead Configurations



Dimensions – Millimeters

	Lead	F	Lead		L	Packaging	D	Т	е	Ød
Lead Config. Config. Ordering Code¹		Lead Spacing	Spacing Tolerance	Packaging Type	Lead Length	C-Spec Ordering Code ²	Body Diameter	Body Thickness	Lead Meniscus	Lead Dia.
				Ammo Pack	20.0 +1.5/-1.0	7317				
			±1.0		3.0 ±1.0	WL30				
Straight	А	10.0		Dulk	4.5 ±1.0	WL45				
				Bulk	5.0 ±1.0	WL50				
					20.0 minimum	WL20				
		10.0	±1.0	Ammo Pack	18.0 +2.0/-0	7317		1 - "Product	3.0 maximum	0.5 ±0.1
Vertical Kink (Preformed)	В			D !!	3.5 ±1.0	WL35		Codes and ings"		
(**************************************				Bulk	4.0 ±1.0	WL40				
				Ammo Pack	18.0 +2.0/-0	7317				
Outside Kink	С	10.0	±1.0		3.5 ±1.0	WL35				
(Preformed)		10.0		Bulk	4.0 ±1.0	WL40				
					5.0 ±1.0	WL50				

¹ Lead Configuration is identified in the 13th character of the ordering code. See "Lead Configuration" and "Ordering Information" sections of this document for further details.

² The "Packaging C-Spec" is a 4-digit numeric or alphanumeric code which identifies both the packaging type and lead length requirement. When ordering, this code must be included in the 15th through 18th character positions of the ordering code. See "Ordering Information" section of this document for further details.



Benefits

- Safety Standard Recognized (IEC 60384-14)
- Reliable operation up to 125°C
- · Class X1/Y1
- · 10 mm lead spacing
- · Lead (Pb)-free and RoHS Compliant
- · Halogen Free
- · Capacitance offerings ranging from 2.0 pF up to 4,700 pF
- Available capacitance tolerances of ±0.25 pF, ±0.5 pF, ±5%, ±10%, and ±20%
- · High reliability
- · Preformed (crimped) or straight lead configurations
- · Non-polar device, minimizing installation concerns
- 100% pure matte tin-plated lead finish allowing for excellent solderability
- Encapsulation meets flammability standard UL 94V–0

Applications

Typical applications include:

- · Line-to-line (Class X) filtering
- · Line-to-ground (Class Y) filtering
- · Antenna coupling
- Primary and secondary coupling (switching power supplies)
- · Line disturbances suppression (motors and motor controls, relays, switching power supplies, and inverters)

Approval Standard and Certification No.

Safety Standard	Safety Standard No.		Working Voltage	Certificate No.	
VDE	IEC 60384-14	X1	400 VAC	40026417	
(ENEC)	100 00384-14	Y1	250 VAC	40036417	

These devices are VDE/ENEC recognized for antenna coupling and AC line-to-line (Class X) and line-to-ground (Class Y) applications per IEC60384–14.

Environmental Compliance

These devices are Halogen Free and RoHS Compliant. They meet all requirements set forth by both EU and China RoHS directives.







General Specifications/Performance Characteristics

Dielectric/Temperature Characteristic:	CH(NP0)	SL	Y5P	Y5U	Y5V
Operating Temperature Range:	-25°C to +125°C				
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC):	±60 ppm/°C	-1,000 ~ +350 ppm/°C	±10%	+20%/-55%	~ +30%/-80%
Dielectric Withstanding Voltage	4,000 VAC (60 ±5 seconds at 25°C)				
Quality Factor (Q)	30 pF% and above: ≥ 1,000 Below 30 pF: ≥ 400 +(20xC)* See "Dissipation Factor"				ctor"
Dissipation Factor (tanδ) at +25°C¹	See "Quality Factor"		2.50%	2.50%	5.0%
Insulation Resistance (IR) Limit at +25°C	10,000 MΩ Minimum (500 VDC applied for 60 ±5 seconds @ 25°C)				

^{*} C = Nominal capacitance

CH(NP0) and SL: 1 MHz ±100 kHz and 1.0 ±0.2 Vrms

X5P, Y5U and Y5V: 1 kHz ± 50 Hz and 1.0 ± 0.2 Vrms

Note: When measuring capacitance, it is important to ensure the set voltage level is held constant. The HP4284 & Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON."

Table 1 – Product Ordering Codes and Ratings

Distratuial				Din	nensions (mn	1)	Lead Spacing		
Dielectric/ Temp. Char.	KEMET Part Number	Capacitance	Capacitance Tolerance	Body Diameter (Maximum)	Body Thickness (Maximum)	Lead Diameter	Bulk Packaging	Ammo Packaging	
	C901U209CUND(1)A(2)	2.0 pF							
	C901U309CUND(1)A(2)	3.0 pF	±0.25 pF						
	C901U409CUND(1)A(2)	4.0 pF	±0.23 pi						
	C901U509CUND(1)A(2)	5.0 pF					10 mm		
	C901U609DUND(1)A(2)	6.0 pF		7.0					
	C901U709DUND(1)A(2)	7.0 pF							
	C901U809DUND(1)A(2)	8.0 pF	±0.5 pF						
CH	C901U909DUND(1)A(2)	9.0 pF			5.0	0.5 ±0.1			
(NP0)	C901U100DUND(1)A(2)	10 pF							
	C901U120JUND(1)A(2)	12 pF							
	C911U150JUND(1)A(2)	15 pF							
	C911U180JUND(1)A(2)	18 pF							
	C911U200JUND(1)A(2)	20 pF	±5%	8.0					
	C911U220JUND(1)A(2)	22 pF		0.0					
	C911U240JUND(1)A(2)	24 pF							
	C911U270JUND(1)A(2)	27 pF							
	KEMET Part Number	Capacitance	Capacitance Tolerance	Body Diameter (Maximum)	Body Thickness (Maximum)	Lead Diameter	Lead S	pacing	

⁽¹⁾ To properly complete ordering code, insert the one-digit character code to reflect the required lead configuration: (See "Lead Configuration" section of this document, page 2, for further details.)

- A = Straight
- B = Vertical Kink
- C = Outside Kink

¹ Capacitance and Dissipation Factor (DF) measured under the following conditions:

⁽²⁾ To properly complete ordering code, enter the four-digit numeric or alphanumeric "Packaging C-Spec Ordering Code." See "Dimensions" section of this document, page 2, for available options.



Table 1 – Product Ordering Codes and Ratings cont'd

Dielectric/				Din	nensions (mm	1)	Lead Spacing	
Temp. Char.	KEMET Part Number	Capacitance	Capacitance Tolerance	Body Diameter (Maximum)	Body Thickness (Maximum)	Lead Diameter	Bulk Packaging	Ammo Packaging
	C901U150JUSD(1)A(2)	15 pF						
	C901U180JUSD(1)A(2)	18 pF 20 pF						
	C901U200JUSD(1)A(2) C901U220JUSD(1)A(2)	20 pF						
	C901U240JUSD(1)A(2)	24 pF						
	C901U270JUSD(1)A(2)	27 pF		7.0	5.0 0.5 ±0.1			
	C901U300JUSD(1)A(2)	30 pF						
	C901U330JUSD(1)A(2)	33 pF						
	C901U360JUSD(1)A(2)	36 pF						
SL	C901U390JUSD(1)A(2)	39 pF	±5%			10	mm	
<u>-</u>	C911U470JUSD(1)A(2)	47 pF	=070		0.0	0.0 20.1		
	C911U500JUSD(1)A(2)	50 pF						
	C911U510JUSD(1)A(2)	51 pF		8.0				
	C911U560JUSD(1)A(2)	56 pF						
	C911U620JUSD(1)A(2)	62 pF						
	C921U680JUSD(1)A(2)	68 pF						
	C921U750JUSD(1)A(2)	75 pF		9.0				
	C921U820JUSD(1)A(2)	82 pF						
	C931U101JUSD(1)A(2)	100 pF		10.0				
	C901U101KUYD(1)A(2)	100 pF			5.0		10 mm	
	C901U151KUYD(1)A(2)	150 pF		7.0				
	C901U221KUYD(1)A(2)	220 pF		7.0		0.5 ±0.1		
Y5P	C901U331KUYD(1)A(2)	330 pF	±10%					
	C911U471KUYD(1)A(2)	470 pF	=1070	8.0	0.0	0.0 _0		
	C921U561KUYD(1)A(2)	560 pF		9.0				
	C921U681KUYD(1)A(2)	680 pF						
	C941U102KUYD(1)A(2)	1,000 pF		11.0				
	C911U102MUWD(1)A(2)	1,000 pF		8.0		1		
	C921U152MUWD(1)A(2)	1,000 pF 1,500 pF		9.0				
	C931U222MUWD(1)A(2)	2,200 pF		10.0				
Y5U	C951U332MUWD(1)A(2)	3,300 pF	±20%	12.0	5.0	0.5 ±0.1	10	mm
	C961U392MUWD(1)A(2)	3,900 pF		13.0				
	C971U472MUWD(1)A(2)	4,700 pF		14.0				
	, , , ,							
	C901U102MUVD(1)A(2)	1,000 pF		7.0				
	C911U152MUVD(1)A(2)	1,500 pF		8.0				
Y5V	C921U222MUVD(1)A(2)	2,200 pF	±20%	9.0	5.5	0.5 ±0.1	10	mm
	C941U332MUVD(1)A(2)	3,300 pF		11.0				
	C951U472MUVD(1)A(2)	4,700 pF	L	12.0	L			
						1		
	KEMET Part Number	Capacitance	Capacitance Tolerance	Body Diameter (Maximum)	Body Thickness (Maximum)	Lead Diameter	Lead S	pacing

⁽¹⁾ To properly complete ordering code, insert the one-digit character code to reflect the required lead configuration: (See "Lead Configuration" section of this document, page 2, for further details.)

- A = Straight
- B = Vertical Kink
- C = Outside Kink

⁽²⁾ To properly complete ordering code, enter the four-digit numeric or alphanumeric "Packaging C-Spec Ordering Code." See "Dimensions" section of this document, page 2, for available options.



Table 2 – Performance & Reliability: Test Methods and Conditions

Ite	em	Specif	ication		Test Metl	hod		
Operating Tem	perature Range			-25°C to +125°	С			
	Between lead wires	No fa	ilures	The capacitor shall between the lead v	I not be damaged wh vires for 60 seconds.	en 4,000 VAC(rms) is applied		
Dielectric Strength	Body Insulation	No fa	iilures	The terminals (leads) of the capacitor shall be connected together. A metal foil is tightly wrapped around the body of the capacitor at a distance of about 3 to 4 mm from each terminal. The capacitor is then inserted into a container filled with metal balls approximately 1 mm in diameter. 4,000 VAC(rms) is applied for 60 seconds between the capacitor lead wires and metal balls.				
Insulation Re	esistance (IR)	10,000 MΩ	Ω minimum	The insulation resignation after 60 ±5 second		sured with 500 ±50 VDC applied		
Сарас	citance	Within specif	fied tolerance		<u> </u>			
		Temperature Characteristics	Specification					
		Y5P, Y5U	DF ≤ 2.5%	Y5P, Y5U and Y5V: Capacitance is measured at 1 kHz ±20% and 5 Vrms or less. (20 ±2°C) NP0 and SL: Capacitance is measured at 1 MHz ±20% and 1.0 ±0.2 Vrms (25°C)				
Dissipation Fa	-t (DE) 0	Y5V	DF ≤ 5.0%					
Dissipation Fa	actor (DF) or Q	NP0,SL	≥ 30 pF: Q ≥ 1,000 < 30 pF: Q ≥ 400 +(20 x C) C = Nominal capacitance					
				A capacitance mea	asurement is made a	t each step specified:		
		Temperature	Capacitance Change	Step	Temperature			
		Characteristics		1	+20 ±2°C			
		Y5P	Within ±10%	2	-25 ±2°C			
Temperature (Characteristics	Y5U	Within +22%/-56%	3	+20 ±2°C			
		Y5V	Within ~+30%/-80%	4	+85 ±2°C			
		СН	0 ±60 ppm/°C	5	+20 ±2°C			
		SL	-1,000 ~+350 ppm°C (+20°C ~+85°C)		at 85 ±2°C for 1 hou 2 hours before measi	ur and then placed at room urement.		
	Tensile	Lead wire or capacito	r body shall not break.	With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; a tensile force of 10 N is applied to the termination in the direction of its axis and acting in a direction away from the body of the specimen.				
Terminal Strength	Bending	Lead wire or capacito	r body shall not break.	With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; a mass force of 5 N is then suspended from the end of the termination. The body of the specimen is then inclined within a period of 2 to 3 seconds, through an angle of approximately 90° in the vertical plane and then resumed to its initial position over the same period of time; this operation constitutes one bend. One bend immediately followed by a second bend in the opposite direction.				
Solder	rability	solder in the axial dire	e a uniform coating of ction and over 3/4 of its ference.	The lead wire of the capacitor is dipped into molten solder for 2 ±0.5 seconds. The depth of immersion is up to 1.5 mm (+5/-0 mm) from the root of lead wires. Solder Temperature: Lead free solder (Sn-3Ag – 0.5Cu) 245°C ±5°C.				

 $^{^1}$ "Room Condition" is defined as follows: Temperature: $15 \sim 35$ °C/Humidity: $45 \sim 75$ %/Atmospheric Pressure: $86 \sim 106$ kPa.



Table 2 – Performance & Reliability: Test Methods and Conditions cont'd

Ite	m	Specif	ication	Test N	Method			
	Appearance	No visua	al defect	As shown in the figure below, the le solder up to 1.5 mm (+5/-0 mm) fror	ad wires are immersed in molten			
	IR	1,000) ΜΩ	(root of lead wire). Duration/Solder Temperature: 3.5 ±	:0.5 seconds/350°C ±10°C or 10 ±1			
	Dielectric Strength	Per it	tem 1	seconds/260°C ±5°C Thermal Capacitor				
Soldering Effect (Non-Preheat)	Capacitance	SL, CH (NP0): Withir	5V: Within ±10% n ±2.5% or ±0.25 pF, r is larger.	Pre-treatment: Capacitor is stored at 85°C ±2°C for 1 hour and then placed at room condition¹ for 24 ±2 hours before initial measurements. Post-treatment: Capacitor is stored for 1 to 2 hours at room condition¹.				
	Appearance	No visua	al defect	Capacitor is stored at 120°C +0/-5°	C for 60 +0/-5 seconds. Then, as wires are immersed in molten solder			
	IR	1,000) ΜΩ	up to 1.5 mm (+5/-0mm) from the er	nd of the epoxy meniscus (root of			
	Dielectric Strength	Per it	tem 1	lead wire). Duration/Solder Temperature: 7.5 +0/-1 seconds/260°C ±5°C				
Soldering Effect (Preheat)	Capacitance	SL, CH (NP0): Withir	5V: Within ±10% n ±2.5% or ±0.25 pF, r is larger.	Pre-treatment: Capacitor is stored at 85°C ±2°C for 1 hour and then placed at room condition¹ for 24 ±2 hours before initial measurements. Post-treatment: Capacitor is stored for 1 to 2 hours at room condition¹.				
	Appearance	No visua	al defect	Steady State Humidity:	Load Humidity:			
		Temperature Characteristics	Capacitance Change					
		Y5P	Within ±10%					
	Capacitance	Y5U	Within ±20%					
		Y5V	Within ±30%					
Biased Humidity		SL CH (NP0)	Within ±2.5% or ±0.25 pF, whichever is larger.	90 to 95% humidity at 40°C ±2°C for 500 ±12 hours. Post Treatment:	90 to 95% humidity at 40°C ±2°C for 500 ±12 hours with full rated voltage applied.			
	DF	Y5P and Y5U:	5.0% maximum maximum	Capacitor is stored for 1 to 2 hours at room condition ¹ .	Post Treatment: Capacitor is stored for 1 to 2			
	Q	SL&CH(NP0): Le: 100+1 More than 30 C = Nominal	ss than 30 pF: Q ≥ 0×C/3 0 pF: Q ≥ 200 capacitance		hours at room condition ¹ .			
	IR		3,000 M Ω minimum 1,000 M Ω minimum					
	Dielectric Strength	, ,	ilures					

¹ "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.



Table 2 – Performance & Reliability: Test Methods and Conditions cont'd

Ite	m	Specification	Test Method				
High Temperature Life	Appearance Capacitance Change IR Dielectric Strength	No visual defect Y5P, Y5V and Y5U: Within ±20% SL and CH (NP0): Within ±3 or ±0.3 pF, whichever is larger. 3,000 MΩ minimum SL and CH (NPO): 1,000 MΩ minimum No failures	Impulse Voltage: Each individual capacitor is subjected to three 8 kv impulses prior to life testing. Cx tr td (uF) (uS) (uS) (uS) (uS) (uS) (uS) (uS) (uS				
Flame	· Test	The capacitor flame extinguishes as follows: Cycle Time 1 ~ 4 30 seconds maximum 5 60 seconds maximum	for 15 seconds. This test is repeated for 5 cycles. Capacitor Flame Gas Burner (Unit:mm)				
Active Flammability		The cheesecloth should not ignite.	The capacitors are individually wrapped in at least one, but not more than two, complete layers of cheesecloth. They are then subjected to 20 discharges. The interval between successive discharges is 5 seconds. The VAC is maintained for 2 minutes after the last discharge. C _{1,2} 1 µF ±10% C ₃ 0.033 µF ±5% 10 kV L _{1,4} 1.5 Mh ±20% 16A Rod core choke Cx Test capacitor R 100 ±2% V _{AC} VR ±5% Ct 3 µF ±5% 10 kV V _R Rated Voltage F Fuse, Rated 10A Vt Voltage applied to Ct				

¹ "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.



Table 2 – Performance & Reliability: Test Methods and Conditions cont'd

Ite	em	Specifi	ication			Test Method		
Passive Flammability		The burning time sh seco The tissue paper	The capacitor under test is held into a flame and in a position which best promotes burning. Each specimen is exposed to the flame one time. Test Specimen Time of exposure to flame: Length of flame: Gas burner length: Inside diameter: Outside diameter: Outside diameter: Gas butane gas purity: 95% minimum					
	Appearance	No visua	al defect					
	Capacitance	Temperature Characteristics SL, CH (NP0)	Capacitance Change Within ±5%	The capacitor is subjected to 5 temperature cycles. (Temperature Cycle)				
		Y5P Y5U, Y5V	Within ±10% Within ±20%		Step	Temperature (°C)	Time (minutes)	
		OL OLI (NIDO)	>20 5 - 0 > 250		1	-25 +0/-3	30	
Temperature Cycle		SL, CH (NP0)	≥30 pF: Q ≥ 350 <30 pF: Q ≥ 275		2	Room temperature	3	
			+5/2C C = Nominal		3	125 +3/-0	30	
	DF/Q		capacitance		4	Room temperature	3	
		Y5P	DF ≤ 5%					
		Y5U, Y5V	DF ≤ 7.5%	placed a	it room cond	acitor shall be stored at 85 ition1 for 24 ±2 hours.		
	IR	3,000 MΩ SL and CH (NPO): 1		Post-tre	eatment: Ca	pacitor is stored for 1 to 2	hours at room co	ndition ¹ .
	Dielectric Strength	No fa	ilures					

 $^{^{1}}$ "Room Condition" is defined as follows: Temperature: $15 \sim 35$ °C/Humidity: $45 \sim 75$ %/Atmospheric Pressure: $86 \sim 106$ kPa.



Soldering and Mounting Information

Soldering:

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could reflow the solder joint between the lead and ceramic element and/or may result in thermal shocks that can crack the ceramic element.

When soldering these capacitors with a soldering iron, it should be performed under the following conditions:

- Temperature of iron-tip: 400°C maximum
- · Soldering iron wattage: 50 W maximum
- · Soldering time: 3.5 seconds maximum

Cleaning (ultrasonic cleaning):

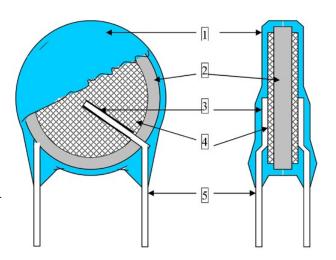
To perform ultrasonic cleaning, observe the following conditions:

- · Rinse bath capacity: Output of 20 watts per liter or less
- · Rinsing time: 5 minute maximum
- Do not vibrate the PCB/PWB directly
- Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires

Construction

Reference	Item	Material				
1	Encapsulation ¹	Epoxy resin, Pigment (Blue/UL 94 V-0)				
2	Dielectric Material	BaTiO ₃				
3	Solder	Sn 96.5, Ag 3, Cu 0.5				
4	Electrodes	Ag (Glass frit)				
5	Lead Wires	Tinned copper clad steel wire (Sn Plating 100% 3-7 µm)				

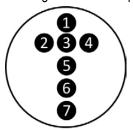
¹ The minimum thickness of the insulation coating (encapsulation) is 0.4 mm Note: Image is exaggerated in order to clearly identify all components of construction.





Capacitor Marking

These capacitors shall be stamped or laser marked with KEMET's trademark, type designation, capacitor class, rated voltage, rated capacitance, and capacitance tolerance codes. In addition, all devices are marked with the recognized approval mark and a date/lot code for traceability. Marking will be supplied either on one side or both sides of the encapsulated capacitor body. All marking shall be legible to allow for clear identification of the component. Marking appears in legible contrast. Illustrated below is an example of the marking format and content. (Two sided marking is limited to capacitors with body diameters ≤ 8.0 mm.)





Location #	Description			Detail				
1	KEMET Trademark			<u>K</u>				
2 ¹	Type Designation (2 characters)	АН						
3 1	Rated Capacitance (3 numeric characters)	First two digits are the significant figures of capacitance. Third digit indicates the additional number of zeros. For example, 2,200 pF is identified as 222. (For values below 10 pF an "R" is used in place of the decimal point, e.g., 2R0 = 2.0 pF.)						
4	Capacitance Tolerance Code (1 character)	C = 0.25 pF, D = 0.5 pF, J = ±5%, K = ±10%, M = ±20%						
6	VDE & ENEC approval mark IEC 60384–14 3rd (2005)	10 DVE						
6	Capacitor Class and Rated Voltage		X1: 400	V~ Y1: 250 V~				
			Date/Lot Co	de, e.g., 3 <u>C</u> 12345				
		3	<u>C</u>	1	2345			
7	Date/Lot Code	Last digit of year, e.g., 3 = 2013	Manufacturing Location Code	Manufacturing Month: 1-9 = Jan - Sept A = October N = November D = December	Last 4 digits of lot no.			

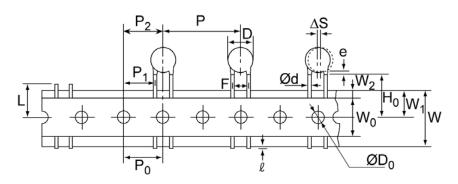
Packaging Quantities

Deckering Type	Loose (Dulle Dee)	Poolsoning Time Loops (Pulls Port)	
Packaging Type	Loose (Bulk Bag)	(25.4 mm Pitch¹)	
Ammo Pack	N/A	500 pieces/box	
Bulk	500 pieces/bag	N/A	

¹ For details regarding component pitch on carrier tape, see "Ammo Pack Taping Format" and "Ammo Pack Taping Specifications" sections of this document.



Figure 1 - Ammo Pack Taping Format (10 mm Lead Spacing)



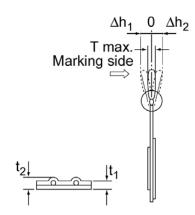


Table 3 – Ammo Pack Taping Specifications

Lead Spacing		10 r	nm
Lead Style		Straight	Preformed ¹
Item		Dimensio	ns (mm)
Lead Spacing	F	10.0 ±1.0	
Component Pitch	Р	25.4 ±2	
Sprocket Hole Pitch	P ₀	12.7 ±0.3	
Sprocket Hole Center to Component Center	P ₂	12.7 ±1.5	
Sprocket Hole Center to Lead Center	P ₁	7.7 ±1.5	
Body Diameter	D	See "Product Ordering Codes and Ratings" section of this document.	
Component Alignment (side/side)	ΔS	0 ±2.0	
Carrier Tape Width	W	18.0 +1.0/-0.5	
Sprocket Hole Position	W ₁	9.0 ±0.5	
Height to Seating Plane ² (preformed leads ¹)	H ₀	N/A	18.0 +2.0/-0
Height to Seating Plane ² (straight leads)	Н	20.0 +1.5/-1.0	N/A
Lead Protrusion	l	2.0 maximum	
Diameter of Sprocket Hole	D ₀	4.0 ±0.2	
Lead Diameter	φd	0.5 ±0.1	
Carrier Tape Thickness	t ₁	0.6 ±0.3	
Total Thickness (Carrier Tape, Hold-Down Tape and Lead)	t ₂	1.5 maximum	
Component Alignment (front/back)	$\Delta h_{_1}$	2.0 maximum	
Component Angrillient (nontroack)	Δh_2	2.0 maximum	
Cut Out Length	L	11.0 maximum	
Hold-Down Tape Width	W _o	11.0 minimum	
Hold-Down Tape Position	W ₂	1.5 ±1.5	
Coating Extension on Leads (meniscus)	е	3.0 maximum for straight lead; not to exceed the bend for preformed lead configurations.	
Body Thickness	Т	See "Product Ordering Codes and	Ratings" section of this document.

¹Preformed (crimped) lead configurations include vertical kink and outside kink. See "Lead Configurations" and "Ordering Information" sections of this document for further details.

²Also referred to as "lead length" in this document.



Application Notes:

Storage and Operating Conditions:

The insulating coating of these devices does not form an air and moisture-tight seal. Avoid exposure to moisture and do not use or store these devices in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt, or the like are present. Before cleaning, bonding or molding these devices, it is important to verify that your process does not affect product quality and performance. KEMET recommends testing and evaluating the performance of a cleaned, bonded or molded product prior to implementing and/or qualifying any of these processes. Store the capacitors where the temperature and relative humidity do not exceed 40 degrees Centigrade and 70% respectively. For optimum solderability, capacitor stock should be used promptly, preferably within 6 months of receipt.

Working Voltage:

Application voltage (Vp-p or Vo-p) must not exceed the voltage rating of the capacitor. Irregular voltages can be generated for a transient period of time when voltage is initially applied and/or removed from a circuit. It is important to choose a capacitor with a voltage rating greater than or equal to these irregular voltages.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage (1)	Pulse Voltage (2)
Positional Measurement	Vo-p	Vo-p	Vp-p	Vp-p	Vp-p

Operating Temperature and Self-Generating Heat:

The surface temperature of a capacitor should be kept below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or similar current, it may self-generate heat due to dielectric loss. Temperature rise due to self-generated heating should not exceed 20°C (while operated at an atmosphere temperature of 25°C).

Handling - Vibration and Impact:

Do not expose these devices or their leads to excessive shock or vibration during use.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.



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SPICE & FIT Software	http://www.kemet.com/spice	
Search Our FAQs: KnowledgeEdge	http://www.kemet.com/keask	
Electrolytic LifeCalculator	http://www.kemet.com:8080/elc	

Product Information		
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Products	http://www.kemet.com/products	
Technical Resources (Including Soldering Techniques)	http://www.kemet.com/technicalpapers	
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