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July 2012

NC7WV07 TinyLogic[®] ULP-A Bual Buffer (Open-Drain Output)

Features

- 0.9V to 3.6V V_{CC} Supply Operation
- 3.6V Over-Voltage Tolerant I/Os at Vcc from 0.9V to 3.6V
- Extremely High Speed tpd
 - 1.0ns: Typical for 2.7V to 3.6V V_{CC}
 - 1.2ns: Typical for 2.3V to 2.7V V_{CC}
 - 2.0ns: Typical for 1.65V to 1.95V V_{CC}
 - 3.2ns: Typical for 1.4V to 1.6V V_{CC}
 - 6.0ns: Typical for 1.1V to 1.3V V_{CC}
 - 13.0ns: Typical for 0.9V V_{CC}
- Power-Off High-Impedance Inputs and Outputs
- High Static Drive (I_{OH}/I_{OL})
 - ±24mA at 3.00V V_{CC}
 - \pm 18mA at 2.30V V_{CC}
 - ± 6 mA at 1.65V V_{CC}
 - $\pm 4mA$ at 1.4V V_{CC}
 - ± 2 mA at 1.1V V_{CC}
 - $\pm 0.1 mA$ at 0.9V V_{CC}
- Uses Proprietary Quiet Series[™] Noise/EMI Reduction Circuitry
- Ultra-Small MicroPak[™] Packages
- Ultra-Low Dynamic Power

Description

The NC7WV07 is a dual buffer with open drain output from Fairchild's Ultra Low Power-A series of TinyLogic® ULP-A is ideal for applications that require extreme high speed, high drive and low power. This product is designed for a wide low voltage operating range (0.9V to 3.6V $V_{\rm CC})$ and applications that require more drive and speed than the TinyLogic ULP series, but still offer best in class low power operation.

The NC7WV07 is uniquely designed for optimized power and speed, and is fabricated with an advanced CMOS technology to achieve high-speed operation while maintaining low CMOS power dissipation.

Ordering Information

Part Number	Top Mark	Package	Packing Method
NC7WV07P6X	V07	6-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3000 Units on Tape & Reel
NC7WV07L6X	ВС	6-Lead MicroPak™, 1.00mm Wide	5000 Units on Tape & Reel

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Battery Life

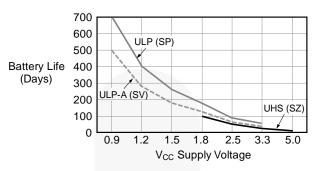


Figure 1. Battery Life vs. V_{CC} Supply Voltage

Notes:

- 1. TinyLogic[®] ULP and ULP-A with up to 50% less power consumption can extend battery life significantly. Battery Life = (V_{battery}•l_{battery}•.9)/(P_{device})/24hrs/day

 where P_{total} = (les V_{col}) + (C_{col} + C_{col}) V_{col} f
- where, $P_{device} = (I_{CC} \bullet V_{CC}) + (C_{PD} + C_L) \bullet V_{CC2} \bullet f$.

 2. Assumes ideal 3.6V Lithium Ion battery with current rating of 900mAH and derated 90% and device frequency at 10MHz, with C_L =15pF load.

Pin Configurations

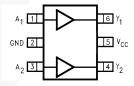


Figure 2. SC70 (Top View)

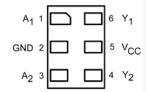


Figure 3. MicroPak (Top Through View)

Pin Definitions

Pin # SC70	Pin # MicroPak	Name	Description
1	1	A ₁	Data Inputs
2	2	GND	Ground
3	3	A_2	Data Inputs
4	4	Y ₂	Output
5	5	V _{CC}	Supply Voltage
6	6	Y ₁	Output

Function Table

Inputs	Output
Α	Y
L	L
Н	*H

H=HIGH Logic Level

L=LOW Logic Level

*H=HIGH Impedance Output Status (Open Drain)

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Par	ameter	Min.	Max.	Unit
V _{CC}	Supply Voltage		-0.5	4.6	V
V _{IN}	DC Input Voltage		-0.5	4.6	V
V _{OUT}	DC Output Voltage		-0.5	4.6	V
I _{IK}	DC Input Diode Current	$V_{IN} < 0V$		-50	mA
I _{OK}	DC Output Diode Current	V _{OUT} < 0V		-50	mA
I _{OL}	DC Output Sink Current		+50	mA	
I _{CC} or I _{GND}	DC V _{CC} or Ground Current per		±50	mA	
T _{STG}	Storage Temperature Range		-65	+150	°C
TJ	Junction Temperature Under B	ias		+150	°C
T∟	Junction Lead Temperature, S	oldering 10 Seconds		+260	°C
		SC70-5		150	
P_{D}	Power Dissipation at +85°C	MicroPak-6		130	mW
		MicroPak2-6		120	
ESD	Human Body Model, JEDEC:JESD22-A114			4000	V
ESD	Charge Device Model, JEDEC	:JESD22-C101	1	2000	\ \ \

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Conditions	Min.	Max.	Unit	
V_{CC}	Supply Voltage		0.9	3.6	V	
V_{IN}	Input Voltage		0	3.6	V	
V _{OUT}	Output Voltage		0	3.6	V	
		V _{CC} =3.0V to 3.6V		+24.0		
		V _{CC} =2.3V to 3.6V		+18.0		
	Output Current in I	V _{CC} =1.65V to 1.95V		+6.0	mA	
I _{OL}	Output Current in I _{OL}	V _{CC} =1.4V to 1.6V		+4.0		
		V _{CC} =1.1V to 1.3V		+2.0		
		V _{CC} =0.9V		+0.1		
T _A	Operating Temperature, Free Air		-40	+85	°C	
Δt/ΔV	Minimum Input Edge Rate	V _{IN} =0.8V to 2.0, V _{CC} =3.0V		10	ns/V	
0	The world Designation	SC70-5		425	°C/W	
$ heta_{\sf JA}$	Thermal Resistance	MicroPak-6		500		

Note:

3. Unused inputs must be held HIGH or LOW. They may not float.

DC Electrical Characteristics

0	D	v	0	T _A =2	25°C	T _A =-40	to 85°C	11
Symbol	Parameter	V _{cc}	Conditions	Min.	Max.	Min.	Max.	Units
		0.90		.65 x V _{CC}		.65 x V _{CC}		
		1.10 ≤ V _{CC} ≤ 1.30		.65 x V _{CC}		.65 x V _{CC}		
.,	HIGH Level Input	1.40 ≤ V _{CC} ≤ 1.60		.65 x V _{CC}		.65 x V _{CC}		١.,
V_{IH}	Voltage	1.65 ≤ V _{CC} ≤ 1.95		.65 x V _{CC}		.65 x V _{CC}		V
		$2.30 \leq V_{CC} \leq 2.70$		1.6		1.6		
		$2.70 \leq V_{CC} \leq 3.60$		2.0		2.0		
		0.90			.35 x V _{CC}		.35 x V _{CC}	
		$1.10 \le V_{CC} \le 1.30$.35 x V _{cc}		.35 x V _{cc}	
	LOW Level Input	$1.40 \le V_{CC} \le 1.60$.35 x V _{cc}		.35 x V _{CC}	
V_{IL}	Voltage	$1.65 \leq V_{CC} \leq 1.95$.35 x V _{cc}		.35 x V _{cc}	V
		$2.30 \leq V_{CC} \leq 2.70$			0.7		0.7	
		$2.70 \leq V_{CC} \leq 3.60$			0.8		0.8	
	0.90	10000		0.1		0.1		
	$1.10 \le V_{CC} \le 1.30$			0.1		0.1		
	$1.40 \le V_{CC} \le 1.60$			0.2		0.2		
		$1.65 \le V_{CC} \le 1.95$	- I _{OL} =100μA -		0.2		0.2	
		$2.30 \leq V_{CC} \leq 2.70$			0.2		0.2	
	-	$2.70 \leq V_{CC} \leq 3.60$			0.2		0.2	
.,	LOW Level Output	$1.10 \le V_{CC} \le 1.30$	I _{OL} =2mA		0.25 x V _{CC}		0.25 x V _{CC}	.,
V_{OL}	Voltage	$1.40 \le V_{CC} \le 1.60$	I _{OL} =4mA		0.25 x V _{CC}		0.25 x V _{CC}	V
		$1.65 \leq V_{CC} \leq 1.95$	I _{OL} =6mA		0.3		0.3	
		$2.30 \leq V_{CC} \leq 2.70$	1 40 4		0.4		0.4	
		$2.70 \leq V_{CC} \leq 3.60$	I _{OL} =12mA		0.4		0.4	
		$2.30 {\leq V_{CC}} {\leq 2.70}$	1. 40 4		0.6		0.6	
		$2.70 \leq V_{CC} \leq 3.60$	I _{OL} =18mA		0.4		0.4	
		$2.70 \leq V_{CC} \leq 3.60$	I _{OL} =24mA		0.55	/	0.55	
I _{IN}	Input Leakage Current	0.90 to 3.60	$0 \leq V_{IN} \leq 3.60$		±0.1		±0.5	μΑ
I _{OFF}	Power Off Leakage Current	0	$\begin{array}{l} 0 \leq \left(V_{\text{IN},} V_{\text{O}}\right) \\ \leq 3.60 \end{array}$		0.5		0.5	μΑ
	Quiescent Supply	0.00 to 2.60	V _{IN} =V _{CC} , or GND		0.9		0.9	
I _{CC}	Current	0.90 to 3.60	$V_{CC} \le V_{IN} \le 3.6V$				±0.9	μA

AC Electrical Characteristics

Symbol	Parameter	V	Conditions		T _A =25°0	2	T _A =-40	to 85°C	Units	Eiguro
Symbol	Symbol Farameter V _{CC}	V _{cc}	Conditions	Min.	Тур.	Max.	Min.	Max.	Ullits	Figure
		0.90	$C_L=15pF$, $R_U=R_D=1M\Omega$		13					
		$1.10 \le V_{CC} \le 1.30$	C _L =15pF,	2.0	6.0	15.0	1.0	18.6		
t _{PZL} , t _{PLZ}	Propagation Delay	$1.40 \le V_{CC} \le 1.60$	$R_U=R_D=2k\Omega$	1.0	3.2	8.7	1.0	9.7	ns	Figure 4 Figure 5
	Delay	$1.65 \leq V_{CC} \leq 1.95$	C_L =30pF, R_U = R_D =500 Ω	1.0	2.0	6.0	1.0	6.8		
	2.3	$2.30 \leq V_{CC} \leq 2.70$		0.7	1.2	3.6	0.6	4.7		
		$2.70 \leq V_{CC} \leq 3.60$		0.5	1.0	3.3	0.4	4.0		
C _{IN}	Input Capacitance	0			2				pF	
C _{OUT}	Output Capacitance	0			6.5					
C _{PD}	Power Dissipation Capacitance	0.90 to 3.60	V _{IN} =0V or V _{CC} , f=10MHz		10				pF	

AC Loadings and Waveforms

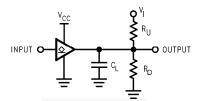


Figure 4. AC Test Circuit

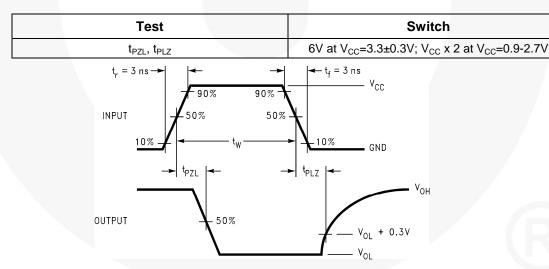


Figure 5. AC Waveforms for Inverting and Non-Inverting Functions

	V _{cc}					
Symbol	3.3V ± 0.3V	2.5V ± 0.2V	1.8V ± 0.15V	1.5V ± 0.1V	1.2V ± 0.1V	0.9V
V_{mi}	1.5V	V _{CC} /2				
V_{mo}	V _{OL} + 0.30V	V _{OL} + 0.15V	V _{OL} + 0.15V	V _{OL} + 0.10V	V _{OL} + 0.10V	V _{OL} + 0.10V

Physical Dimensions

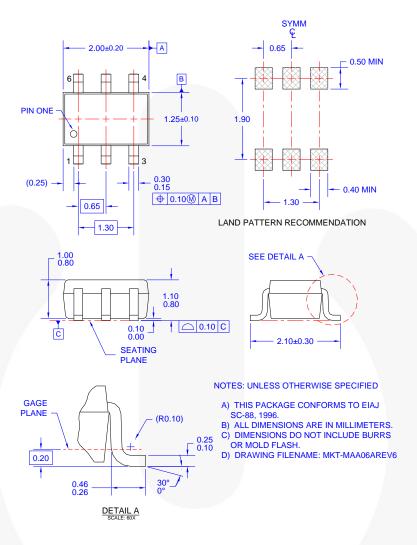


Figure 6. 6-Lead, SC70, EIAJ SC-88a, 1.25mm Wide

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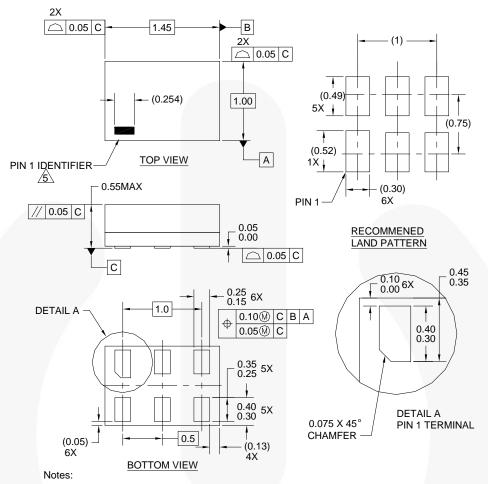
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Tape and Reel Specification

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Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
P6X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

Physical Dimensions



- 1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994 4. FILENAME AND REVISION: MAC06AREV4
- 5. PIN ONE IDENTIFIER IS 2X LENGTH OF ANY OTHER LINE IN THE MARK CODE LAYOUT.

Figure 7. 6-Lead, MicroPak™, 1.0mm Wide

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Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
L6X	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed





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- Поставка специализированных компонентов военного и аэрокосмического уровня качества (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Actel, Aeroflex, Peregrine, VPT, Syfer, Eurofarad, Texas Instruments, MS Kennedy, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Компания «Океан Электроники» является официальным дистрибьютором и эксклюзивным представителем в России одного из крупнейших производителей разъемов военного и аэрокосмического назначения «JONHON», а так же официальным дистрибьютором и эксклюзивным представителем в России производителя высокотехнологичных и надежных решений для передачи СВЧ сигналов «FORSTAR».



«JONHON» (основан в 1970 г.)

Разъемы специального, военного и аэрокосмического назначения:

(Применяются в военной, авиационной, аэрокосмической, морской, железнодорожной, горно- и нефтедобывающей отраслях промышленности)

«**FORSTAR**» (основан в 1998 г.)

ВЧ соединители, коаксиальные кабели, кабельные сборки и микроволновые компоненты:

(Применяются в телекоммуникациях гражданского и специального назначения, в средствах связи, РЛС, а так же военной, авиационной и аэрокосмической отраслях промышленности).



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