



BUK763R9-60E

N-channel TrenchMOS standard level FET

28 July 2016

Product data sheet

1. General description

Standard level N-channel MOSFET in a SOT404 package using TrenchMOS technology. This product has been designed and qualified to AEC Q101 standard for use in high performance automotive applications.

2. Features and benefits

- AEC Q101 compliant
- Repetitive avalanche rated
- Suitable for thermally demanding environments due to 175 °C rating
- True standard level gate with VGS(th) rating of greater than 1V at 175 °C

3. Applications

- 12 V Automotive systems
- Motors, lamps and solenoid control
- Start-Stop micro-hybrid applications
- Transmission control
- Ultra high performance power switching

4. Quick reference data

Table 1. Quick reference data

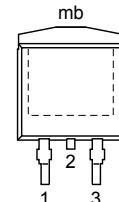
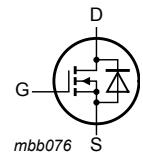
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{DS}	drain-source voltage	$T_j \geq 25 \text{ }^\circ\text{C}; T_j \leq 175 \text{ }^\circ\text{C}$		-	-	60	V
I_D	drain current	$V_{GS} = 10 \text{ V}; T_{mb} = 25 \text{ }^\circ\text{C}$; Fig. 2	[1]	-	-	100	A
P_{tot}	total power dissipation	$T_{mb} = 25 \text{ }^\circ\text{C}$; Fig. 1		-	-	263	W
Static characteristics							
R_{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ }^\circ\text{C}$; Fig. 11		-	2.94	3.9	$\text{m}\Omega$
Dynamic characteristics							
Q_{GD}	gate-drain charge	$I_D = 25 \text{ A}; V_{DS} = 48 \text{ V}; V_{GS} = 10 \text{ V}$; Fig. 13 ; Fig. 14		-	33	-	nC

[1] Continuous current is limited by package.

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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	D	drain		
3	S	source		
mb	D	mounting base; connected to drain	 D2PAK (SOT404)	

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BUK763R9-60E	D2PAK	plastic single-ended surface-mounted package (D2PAK); 3 leads (one lead cropped)	SOT404

7. Marking

Table 4. Marking codes

Type number	Marking code
BUK763R9-60E	BUK763R9-60E

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage	$T_j \geq 25^\circ\text{C}$; $T_j \leq 175^\circ\text{C}$		-	60	V
V_{DGR}	drain-gate voltage	$R_{GS} = 20\text{ k}\Omega$		-	60	V
V_{GS}	gate-source voltage	$T_j \leq 175^\circ\text{C}$; DC		-20	20	V
P_{tot}	total power dissipation	$T_{mb} = 25^\circ\text{C}$; Fig. 1		-	263	W
I_D		$T_{mb} = 25^\circ\text{C}$; $V_{GS} = 10\text{ V}$; Fig. 2	[1]	-	100	A
		$T_{mb} = 100^\circ\text{C}$; $V_{GS} = 10\text{ V}$; Fig. 2	[1]	-	100	A
I_{DM}	peak drain current	$T_{mb} = 25^\circ\text{C}$; pulsed; $t_p \leq 10\text{ }\mu\text{s}$; Fig. 3		-	706	A
T_{stg}	storage temperature			-55	175	°C
T_j	junction temperature			-55	175	°C

Symbol	Parameter	Conditions		Min	Max	Unit
Source-drain diode						
I _S	source current	T _{mb} = 25 °C	[1]	-	100	A
I _{SM}	peak source current	pulsed; t _p ≤ 10 µs; T _{mb} = 25 °C		-	706	A
Avalanche ruggedness						
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	I _D = 100 A; V _{sup} ≤ 60 V; R _{GS} = 50 Ω; V _{GS} = 60 V; T _{j(init)} = 25 °C; unclamped; Fig. 4	[2][3]	-	372	mJ

[1] Continuous current is limited by package.

[2] Single-pulse avalanche rating limited by maximum junction temperature of 175 °C.

[3] Refer to application note AN10273 for further information.

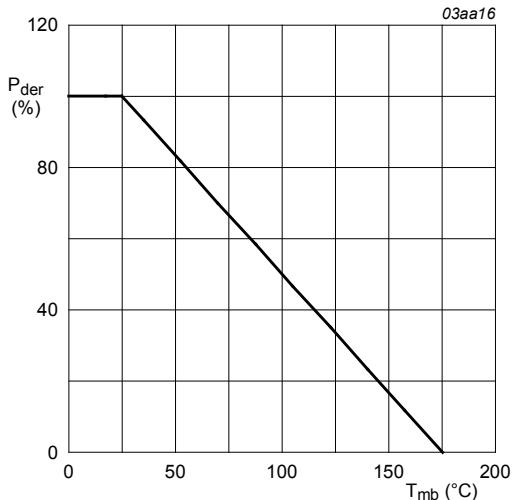


Fig. 1. Normalized total power dissipation as a function of mounting base temperature

$$P_{der} = \frac{P_{tot}}{P_{tot}(25^\circ C)} \times 100 \%$$

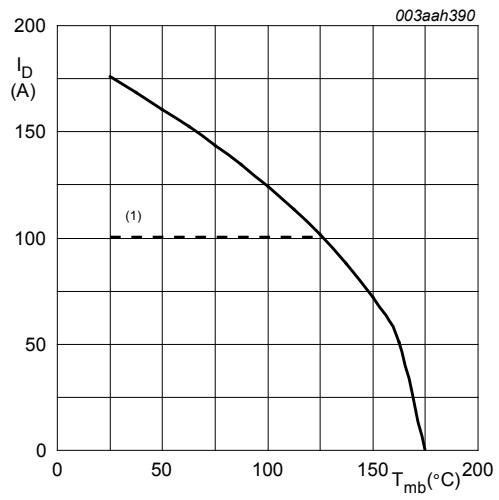


Fig. 2. Continuous drain current as a function of mounting base temperature

$$V_{GS} \geq 10V$$

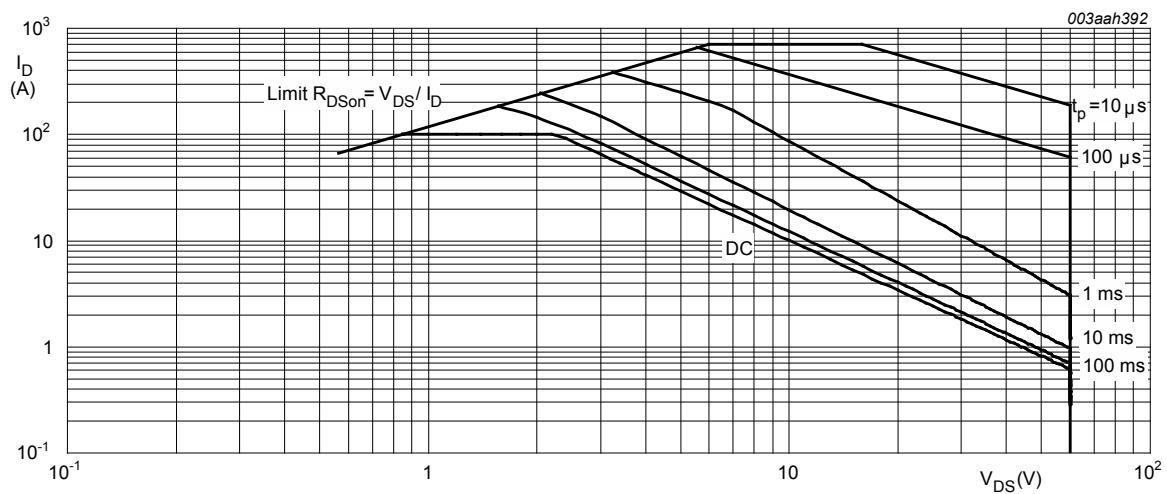


Fig. 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

$T_{mb} = 25^\circ\text{C}$; I_{DM} is a single pulse

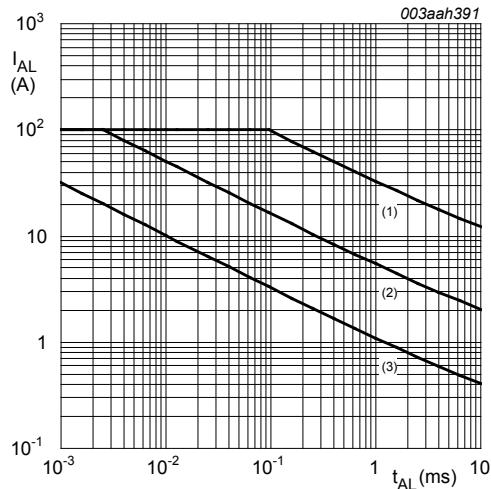


Fig. 4. Single pulse avalanche rating; avalanche current as a function of avalanche time

(1) $T_j(\text{init}) = 25^\circ\text{C}$; (2) $T_j(\text{init}) = 150^\circ\text{C}$; (3) Repetitive Avalanche

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	Fig. 5	-	-	0.57	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	minimum footprint ; mounted on a printed-circuit board	-	50	-	K/W

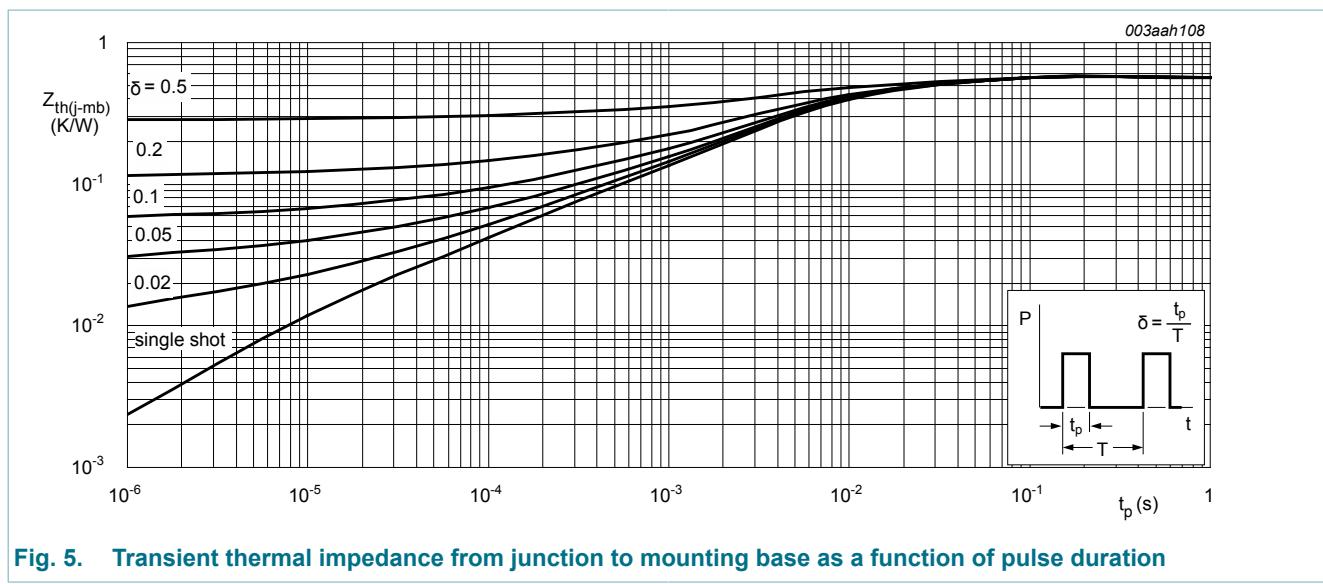


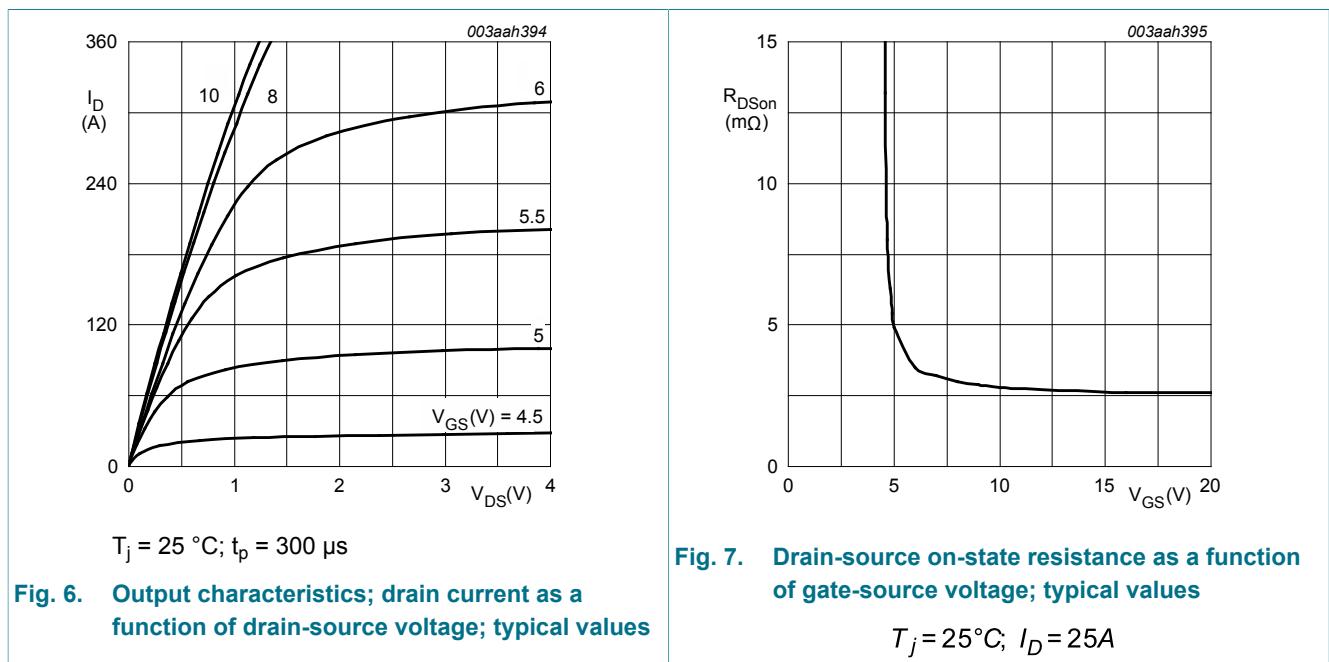
Fig. 5. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25^\circ C$		60	-	-	V
		$I_D = 250 \mu A; V_{GS} = 0 V; T_j = -55^\circ C$		54	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1 mA; V_{DS} = V_{GS}; T_j = 25^\circ C;$ Fig. 9 ; Fig. 10		2.4	3	4	V
		$I_D = 1 mA; V_{DS} = V_{GS}; T_j = 175^\circ C;$ Fig. 9		1	-	-	V
		$I_D = 1 mA; V_{DS} = V_{GS}; T_j = -55^\circ C;$ Fig. 9		-	-	4.5	V
I_{DSS}	drain leakage current	$V_{DS} = 60 V; V_{GS} = 0 V; T_j = 25^\circ C$		-	0.07	1	μA
		$V_{DS} = 60 V; V_{GS} = 0 V; T_j = 175^\circ C$		-	-	500	μA
I_{GSS}	gate leakage current	$V_{GS} = 20 V; V_{DS} = 0 V; T_j = 25^\circ C$		-	2	100	nA
		$V_{GS} = -20 V; V_{DS} = 0 V; T_j = 25^\circ C$		-	2	100	nA
R_{DSon}	drain-source on-state resistance	$V_{GS} = 10 V; I_D = 25 A; T_j = 25^\circ C;$ Fig. 11		-	2.94	3.9	$m\Omega$
		$V_{GS} = 10 V; I_D = 25 A; T_j = 175^\circ C;$ Fig. 11 ; Fig. 12		-	-	8.5	$m\Omega$
Dynamic characteristics							
$Q_{G(tot)}$	total gate charge	$I_D = 25 A; V_{DS} = 48 V; V_{GS} = 10 V;$ Fig. 13 ; Fig. 14		-	103	-	nC
Q_{GS}	gate-source charge			-	25.1	-	nC
Q_{GD}	gate-drain charge			-	33	-	nC

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
C_{iss}	input capacitance	$V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}; f = 1 \text{ MHz}; T_j = 25 \text{ }^\circ\text{C}$; Fig. 15	-	5609	7480	pF	
C_{oss}	output capacitance		-	737	884	pF	
C_{rss}	reverse transfer capacitance		-	455	624	pF	
$t_{d(on)}$	turn-on delay time	$V_{DS} = 45 \text{ V}; R_L = 1.8 \Omega; V_{GS} = 10 \text{ V}; R_{G(ext)} = 5 \Omega$	-	25.3	-	ns	
t_r	rise time		-	41.4	-	ns	
$t_{d(off)}$	turn-off delay time		-	62.7	-	ns	
t_f	fall time		-	45	-	ns	
L_D	internal drain inductance	from upper edge of mounting base to centre of die	-	2.5	-	nH	
L_S	internal source inductance	measured from source lead to source bond pad	-	7.5	-	nH	
Source-drain diode							
V_{SD}	source-drain voltage	$I_S = 25 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$; Fig. 16	-	0.8	1.2	V	
t_{rr}	reverse recovery time	$I_S = 20 \text{ A}; dI_S/dt = -100 \text{ A}/\mu\text{s}; V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}$	-	39	-	ns	
Q_r	recovered charge		-	51	-	nC	



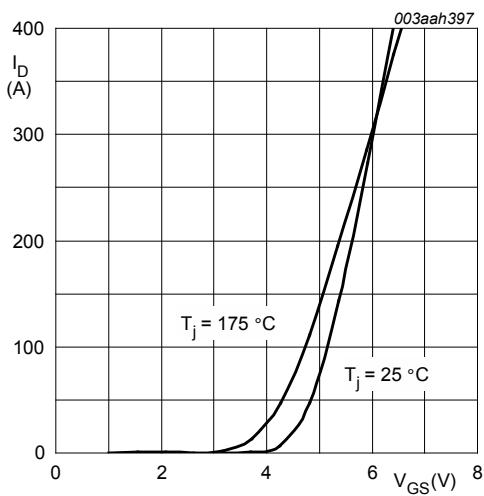


Fig. 8. Transfer characteristics; drain current as a function of gate-source voltage; typical values

$V_{DS} = 10V$

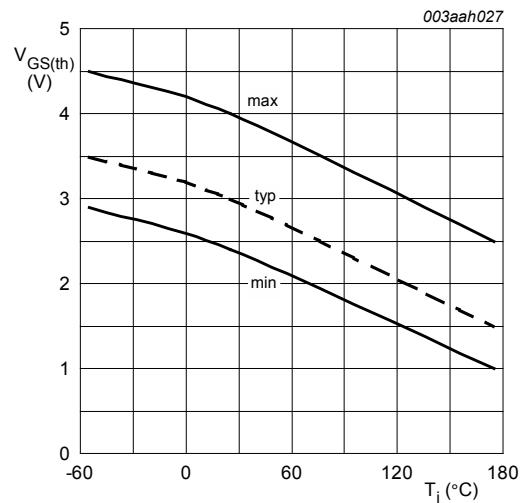


Fig. 9. Gate-source threshold voltage as a function of junction temperature

$I_D = 1 \text{ mA}; V_{DS} = V_{GS}$

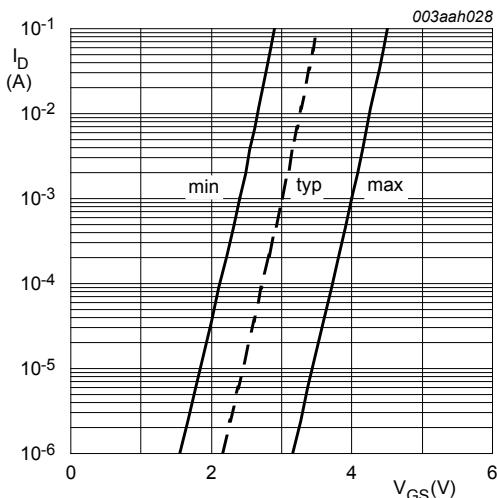
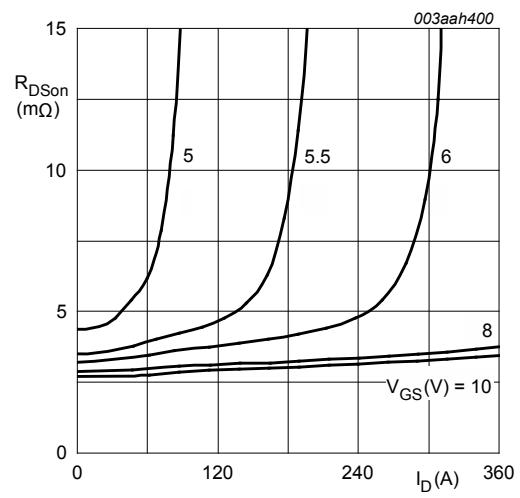


Fig. 10. Sub-threshold drain current as a function of gate-source voltage

$T_j = 25 \text{ }^\circ\text{C}; V_{DS} = 5V$



$T_j = 25 \text{ }^\circ\text{C}; t_p = 300 \mu\text{s}$

Fig. 11. Drain-source on-state resistance as a function of drain current; typical values

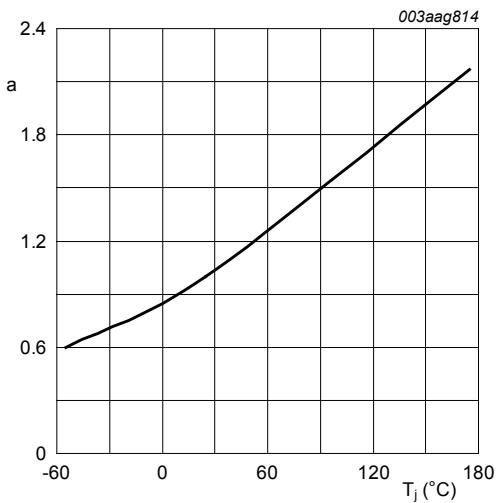


Fig. 12. Normalized drain-source on-state resistance factor as a function of junction temperature

$$a = \frac{R_{DSon}}{R_{DSon}(25^\circ\text{C})}$$

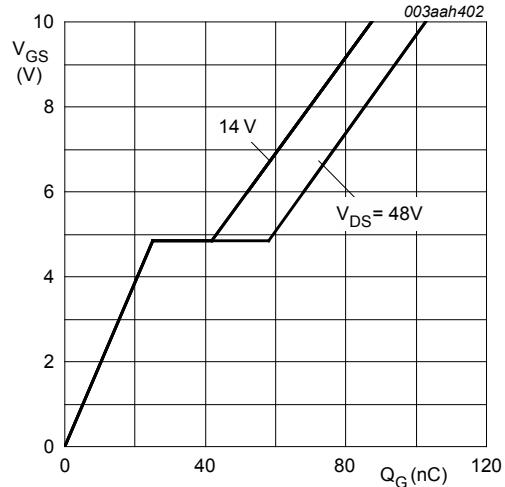


Fig. 14. Gate-source voltage as a function of gate charge; typical values

$$T_j = 25^\circ\text{C}, I_D = 25\text{A}$$

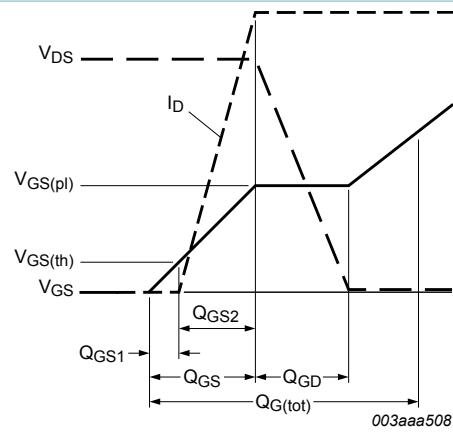


Fig. 13. Gate charge waveform definitions

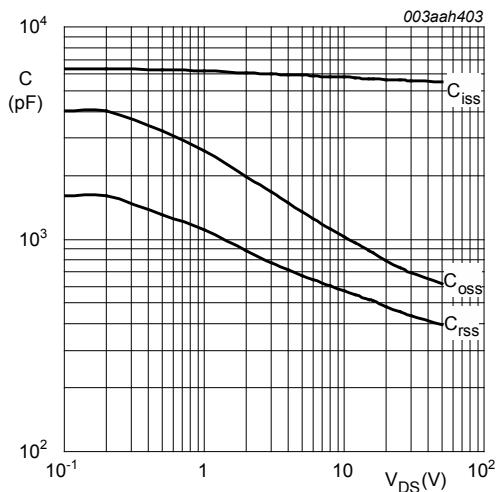


Fig. 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

$$V_{GS} = 0\text{V}, f = 1\text{MHz}$$

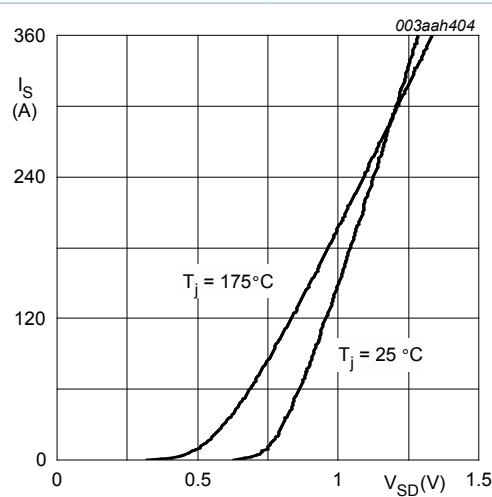


Fig. 16. Source (diode forward) current as a function of source-drain (diode forward) voltage; typical values

$$V_{GS} = 0V$$

11. Package outline

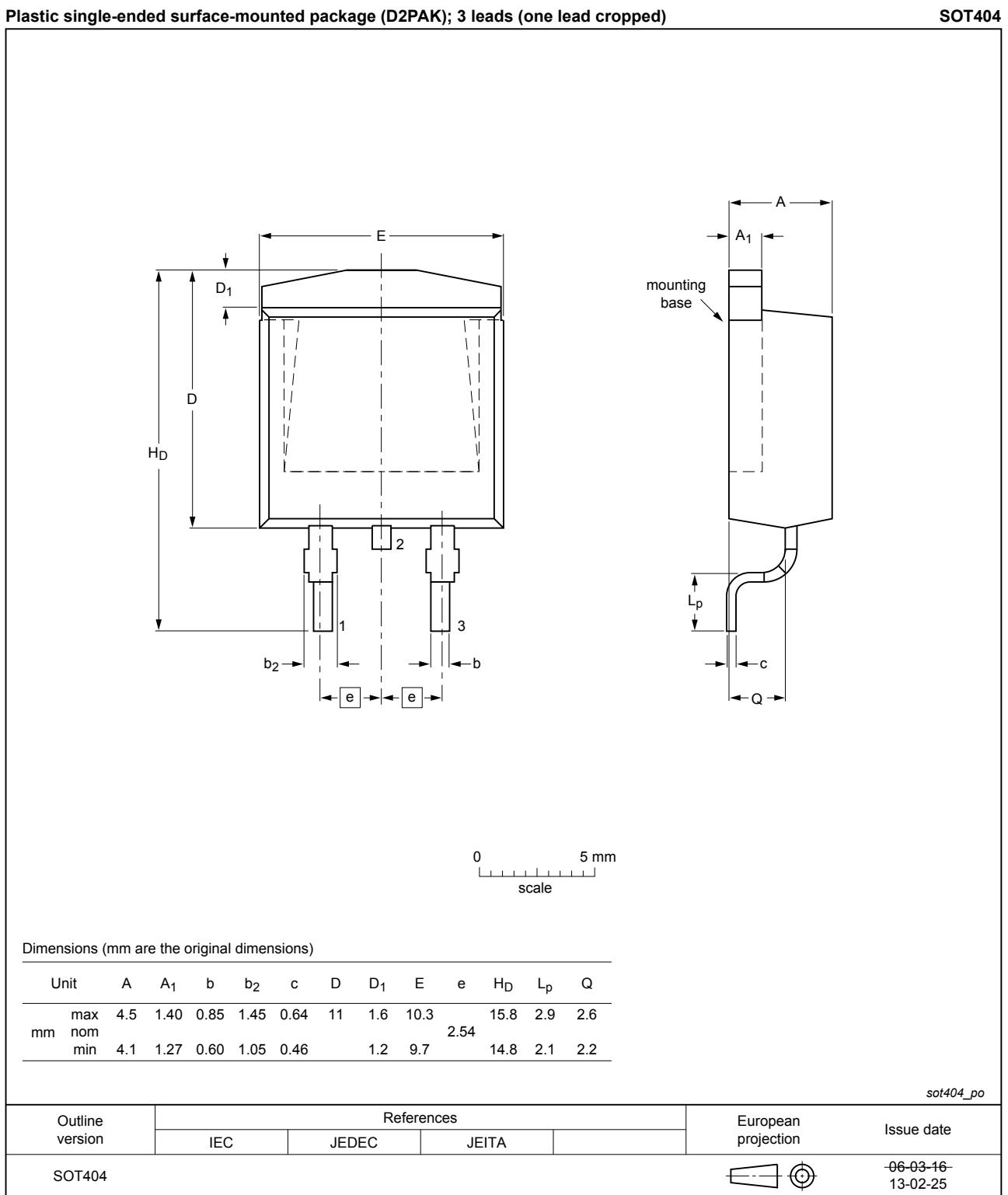


Fig. 17. Package outline D2PAK (SOT404)

12. Legal information

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Document status [1][2]	Product status [3]	Definition
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