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**EVB-LAN9252-ADD-ON
for Beckhoff EL9800
User's Guide**

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Derek Carlson
VP Development Tools

12-Sep-14
Date

NOTES:

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Preface

NOTICE TO CUSTOMERS

All documentation becomes dated, and this manual is no exception. Microchip tools and documentation are constantly evolving to meet customer needs, so some actual dialogs and/or tool descriptions may differ from those in this document. Please refer to our web site (www.microchip.com) to obtain the latest documentation available.

Documents are identified with a “DS” number. This number is located on the bottom of each page, in front of the page number. The numbering convention for the DS number is “DSXXXXA”, where “XXXX” is the document number and “A” is the revision level of the document.

For the most up-to-date information on development tools, see the MPLAB® IDE online help. Select the Help menu, and then Topics to open a list of available online help files.

INTRODUCTION

This chapter contains general information that will be useful to know before using the EVB-LAN9252-ADD-ON. Items discussed in this chapter include:

- [Document Layout](#)
- [Conventions Used in this Guide](#)
- [The Microchip Web Site](#)
- [Development Systems Customer Change Notification Service](#)
- [Customer Support](#)
- [Document Revision History](#)

DOCUMENT LAYOUT

This document describes how to use the EVB-LAN9252-ADD-ON as a development tool for the Microchip LAN9252 EtherCAT® slave controller. The manual layout is as follows:

- **Chapter 1. “Overview”** – Provides a brief description of the EVB-LAN9252-ADD-ON.
- **Chapter 2. “Board Details & Configuration”** – Includes details and instructions for using the EVB-LAN9252-ADD-ON.
- **Appendix A. “Evaluation Board Photo”** – This appendix shows the EVB-LAN9252-ADD-ON.
- **Appendix B. “Evaluation Board Schematics”** – This appendix shows the EVB-LAN9252-ADD-ON schematics.
- **Appendix C. “Bill of Materials (BOM)”** – This appendix includes the EVB-LAN9252-ADD-ON Bill of Materials (BOM).

CONVENTIONS USED IN THIS GUIDE

This manual uses the following documentation conventions:

DOCUMENTATION CONVENTIONS

Description	Represents	Examples
Arial font:		
Italic characters	Referenced books	<i>MPLAB[®] IDE User's Guide</i>
	Emphasized text	...is the <i>only</i> compiler...
Initial caps	A window	the Output window
	A dialog	the Settings dialog
	A menu selection	select Enable Programmer
Quotes	A field name in a window or dialog	"Save project before build"
Underlined, italic text with right angle bracket	A menu path	<u><i>File>Save</i></u>
Bold characters	A dialog button	Click OK
	A tab	Click the Power tab
N'Rnnnn	A number in verilog format, where N is the total number of digits, R is the radix and n is a digit.	4'b0010, 2'hF1
Text in angle brackets < >	A key on the keyboard	Press <Enter>, <F1>
Courier New font:		
Plain Courier New	Sample source code	#define START
	Filenames	autoexec.bat
	File paths	c:\mcc18\h
	Keywords	_asm, _endasm, static
	Command-line options	-Opa+, -Opa-
	Bit values	0, 1
	Constants	0xFF, 'A'
Italic Courier New	A variable argument	<i>file.o</i> , where <i>file</i> can be any valid filename
Square brackets []	Optional arguments	mcc18 [options] <i>file</i> [options]
Curly brackets and pipe character: { }	Choice of mutually exclusive arguments; an OR selection	errorlevel {0 1}
Ellipses...	Replaces repeated text	var_name [, var_name...]
	Represents code supplied by user	void main (void) { ... }

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- **General Technical Support** – Frequently Asked Questions (FAQs), technical support requests, online discussion groups, Microchip consultant program member listing
- **Business of Microchip** – Product selector and ordering guides, latest Microchip press releases, listing of seminars and events, listings of Microchip sales offices, distributors and factory representatives

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The Development Systems product group categories are:

- **Compilers** – The latest information on Microchip C compilers, assemblers, linkers and other language tools. These include all MPLAB C compilers; all MPLAB assemblers (including MPASM assembler); all MPLAB linkers (including MPLINK object linker); and all MPLAB librarians (including MPLIB object librarian).
- **Emulators** – The latest information on Microchip in-circuit emulators. This includes the MPLAB REAL ICE and MPLAB ICE 2000 in-circuit emulators.
- **In-Circuit Debuggers** – The latest information on the Microchip in-circuit debuggers. This includes MPLAB ICD 3 in-circuit debuggers and PICkit 3 debug express.
- **MPLAB IDE** – The latest information on Microchip MPLAB IDE, the Windows Integrated Development Environment for development systems tools. This list is focused on the MPLAB IDE, MPLAB IDE Project Manager, MPLAB Editor and MPLAB SIM simulator, as well as general editing and debugging features.
- **Programmers** – The latest information on Microchip programmers. These include production programmers such as MPLAB REAL ICE in-circuit emulator, MPLAB ICD 3 in-circuit debugger and MPLAB PM3 device programmers. Also included are nonproduction development programmers such as PICSTART Plus and PIC-kit 2 and 3.

CUSTOMER SUPPORT

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- Distributor or Representative
- Local Sales Office
- Field Application Engineer (FAE)
- Technical Support

Customers should contact their distributor, representative or field application engineer (FAE) for support. Local sales offices are also available to help customers. A listing of sales offices and locations is included in the back of this document.

Technical support is available through the web site at:

<http://www.microchip.com/support>

DOCUMENT REVISION HISTORY

Revision	Section/Figure/Entry	Correction
DS50002356A (03-19-15)	Initial Release of document.	
DS50002356B (10-08-15)	(Throughout document.)	Updated EVB-LAN9252-BECKHOFF-ADD-ON to EVB-LAN9252-ADD-ON.
	Section 1.1 “Introduction”	Updated/replaced entire introduction.
	Figure 1-1	Updated image.
	Section 1.3 “Terms and Abbreviations”	Updated definition of EVB. Added definition for DIGIO.
	Section 2.1 “Power”	Updated/replaced entire paragraph.
	Section 2.2 “Board-to-Board Connector Pinout”	Updated/replaced entire paragraph.
	Table 2-1	Updated title, fixed arrangement of Pin Numbers.
	Section 2.4 “Resets”	Removed entirely; numbering of succeeding sections updated as a result.
	Section 2.4 “Configuration”	Updated first paragraph to include REV B.
	Figure 2-1	Updated image.
	Figure 2-2	Updated image.
	Section 2.5.1 “Chip Mode”	Updated title. Updated/replaced entire paragraph.
	Section 2.6 “Process Data Interfaces”	Updated first paragraph, second bullet point and note.
	Section 2.6.1.1.1 “SPI Mode”	Updated/replaced entire SPI Mode section including tables.
	Section 2.6.1.1.2 “DIGIO Mode”	Updated/replaced entire DIGIO Mode section including tables.
	Section 2.6.1.2 “EVB-LAN9252-ADD-ON Software Configuration”	Updated step 1 to add definition for ESI.
	Figure 2-6	Updated image.
	Figure 2-7	Updated image.
	Figure 2-8	Updated image.
	Section 2.6.2 “Configuring PDI Selection on the Beckhoff EL9800”	Added note.
	Figure B-1	Updated schematic.
	Figure B-2	Updated schematic.

Revision	Section/Figure/Entry	Correction
	Figure B-3	Updated schematic.
	Table C-1	Updated BOM.
	Table C-2	Updated BOM - DNP Components.

Chapter 1. Overview

1.1 INTRODUCTION

The LAN9252 is a 2-port EtherCAT[®] Slave Controller (ESC) with dual-integrated Ethernet PHYs which each contain a full-duplex 100BASE-TX transceiver and support 100Mbps (100BASE-TX) operation. 100BASE-FX is supported via an external fiber transceiver.

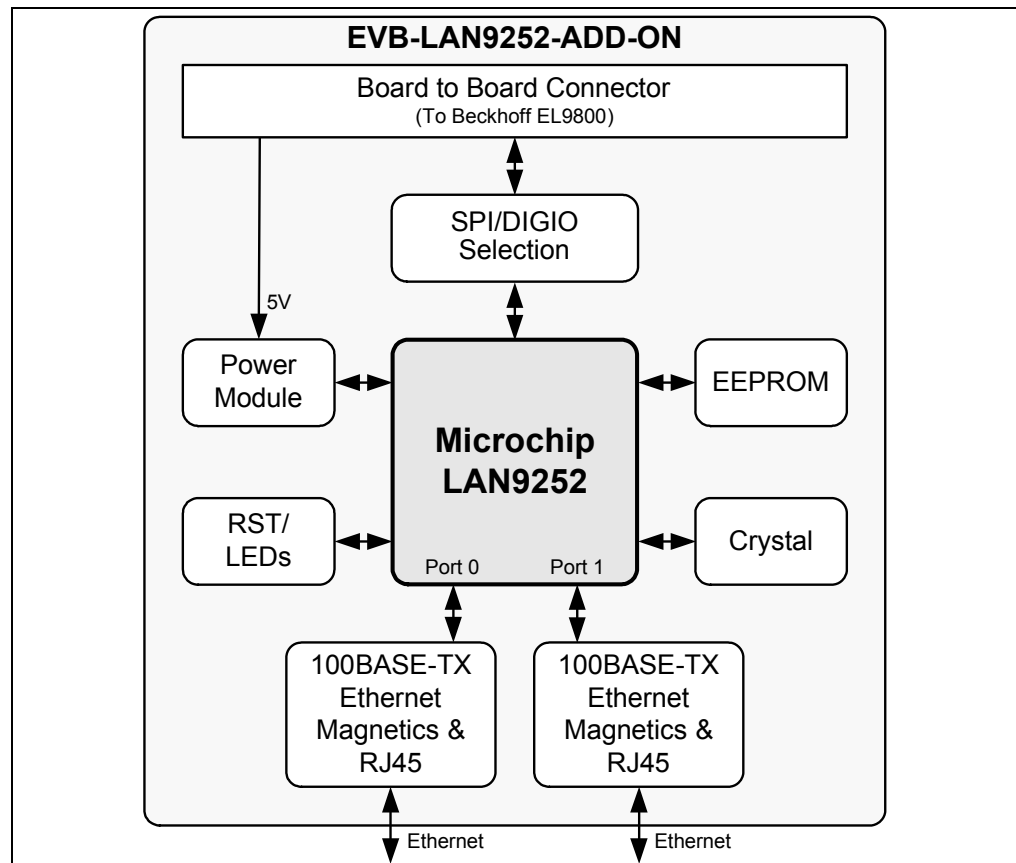
The EtherCAT module implements a 3 port EtherCAT slave controller with 4K bytes of Dual Port memory (DPRAM), 4 SyncManagers, 3 Fieldbus Memory Management Units (FMMUs) and a 64-bit Distributed Clock.

This document details the various sections of the EVB-LAN9252-ADD-ON, which is designed to be used as an add-on board (ESC board) with the Beckhoff EL9800 EtherCAT Evaluation Board.

This board supports the SPI and DIGIO PDI modes of the LAN9252 board, combining a LAN9252 EtherCAT Slave Controller, two RJ45 1x1 Tab-DOWN with LEDs, 8-pin integrated magnetics connector (ICM), and a PDI-Connector on a printed circuit board.

A simplified block diagram of the EVB-LAN9252-ADD-ON is displayed in [Figure 1-1](#).

FIGURE 1-1: EVB-LAN9252-ADD-ON BLOCK DIAGRAM



1.2 REFERENCES

Concepts and material available in the following documents may be helpful when reading this document. Visit www.microchip.com for the latest documentation.

- LAN9252 Data Sheet
- Beckhoff EL9800 EtherCAT Base Board Datasheet
- AN 8.13 Suggested Magnetics
- EVB-LAN9252-ADD-ON Schematics

1.3 TERMS AND ABBREVIATIONS

ESC - EtherCAT[®] Slave Controller

EVB - Evaluation Board

HAL - Hardware Abstraction Layer

HBI - Host Bus Interface

IDE - Integrated Development Environment

PDI - Process Data Interface

SPI - Serial Protocol Interface

SSC - Slave Stack Code

DIGIO - Digital Input/Output

Chapter 2. Board Details & Configuration

This chapter includes sub-sections on the following EVB-LAN9252-ADD-ON details:

- Power
- Board-to-Board Connector Pinout
- Clock
- Configuration
- Strap Options
- Process Data Interfaces
- Mechanicals

2.1 POWER

Power is supplied as +5V to the EVB-LAN9252-ADD-ON via the board-to-board connector. +3.3V is generated internally via an LDO. Assertion of the D2 green LED indicates +3.3V o/p. The LAN9252 includes an internal +1.2V regulator which supplies power to the internal core logic.

2.2 BOARD-TO-BOARD CONNECTOR PINOUT

[Table 2-1](#) details the pinout of connector J202 of the EVB-LAN9252 ADD ON board-to-board connector, which connects to the Beckhoff EL9800 J202 connector.

TABLE 2-1: EVB-LAN9252 CONNECTOR J202 PIN OUT

Pin Number	Signal	Pin Number	Signal
1	DIGIO 0	20	GND
2	GND	21	SCS#
3	DIGIO 2	22	NC
4	DIGIO 1	23	NC
5	DIGIO 4	24	NC
6	DIGIO 3	25	NC
7	DIGIO 6	26	NC
8	DIGIO 5	27	GND
9	GND	28	NC
10	DIGIO 7	29	NC
11	DIGIO 9	30	SO
12	DIGIO 8	31	NC
13	DIGIO 11	32	SI
14	DIGIO 10	33	NC
15	DIGIO 13	34	NC
16	DIGIO 12	35	NC
17	DIGIO 15	36	NC
18	DIGIO 14	37	WD_TRIG/SPI_CLK
19	NC	38	GND

TABLE 2-1: EVB-LAN9252 CONNECTOR J202 PIN OUT (CONTINUED)

Pin Number	Signal	Pin Number	Signal
39	SOF	46	OE_EXT
40	OUTVALID	47	NC
41	SYNC/LATCH0	48	VCC_5V_IN
42	NC	49	3V3
43	LATCH_IN	50	VCC_5V_IN
44	SYNC/LATCH1	51	TP3
45	GND	52	TP2

Note: Pins 53-56 are NC.

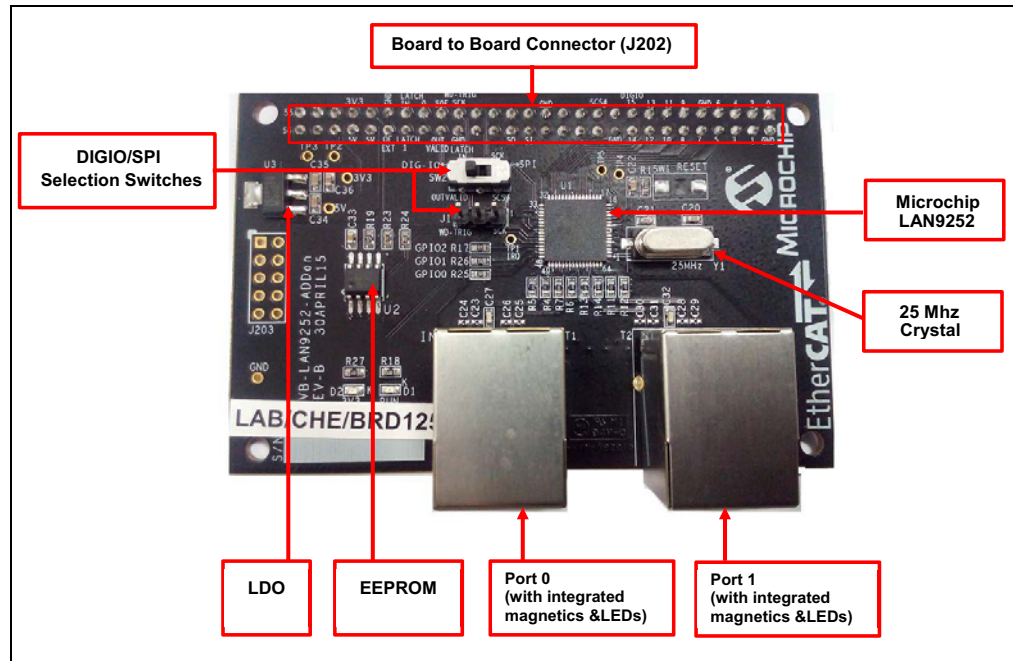
2.3 CLOCK

LAN9252 requires a fixed-frequency 25MHz clock source for use by the internal clock oscillator and PLL. This is typically provided by attaching a 25MHz crystal to the OSC1 and OSC0 pins.

2.4 CONFIGURATION

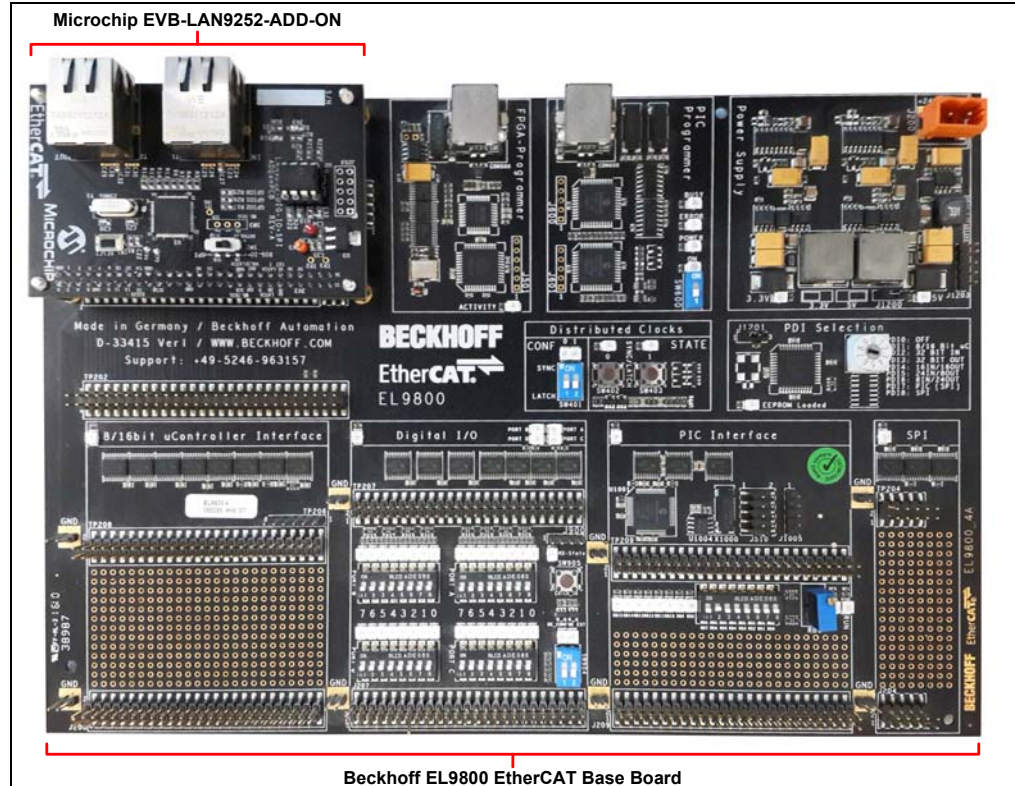
The following sub-sections describe the various board features and configuration settings. A top view of the EVB-LAN9252-ADD-ON REV-B is displayed in [Figure 2-1](#).

FIGURE 2-1: EVB-LAN9252-ADD-ON TOP VIEW WITH CALLOUTS



The EVB-LAN9252-ADD-ON connects with the Beckhoff EL9800 EtherCAT Base Board as depicted in [Figure 2-2](#).

FIGURE 2-2: BECKHOFF EL9800



2.5 STRAP OPTIONS

2.5.1 Chip Mode

The chip mode straps determine the number of active ports and port types. EVB-LAN9252 by default, supports only the 2 port mode (CHIP_MODE0 and CHIP_MODE1 signals pulled down).

2.5.2 EEPROM Size Configuration

The EEPROM size configuration strap (LAN9252 E2PSIZE signal) determines the supported EEPROM size range. A low selects 1Kbits (128 x 8) through 16Kbits (2K x 8). A high selects 32Kbits (4K x 8) through 512Kbits (64K x 8) or 4Mbits (512K x 8). On this EVB, E2PSIZE is pulled high to select 512k EEPROM.

2.6 PROCESS DATA INTERFACES

The Process Data Interface is used to communicate with external SoC. The EVB-LAN9252-ADD-ON supports two different PDI configurations:

- SPI
- DIGIO (Default Configuration)

The following section details the hardware and software configurations required for the EVB-LAN9252-ADD-ON and the Beckhoff EL9800 for both PDI configuration options.

Note: Pins 1-2 of J1201 must be shorted on the Beckhoff EL9800 board (under PDI selection area). For DIGIO Switch SW904 must be move to ON position on the Beckhoff EL9800 board (under Digital I/O selection area).

2.6.1 PDI Configuration on the EVB-LAN9252-ADD-ON

2.6.1.1 EVB-LAN9252-ADD-ON BOARD LEVEL CONFIGURATION

2.6.1.1.1 SPI Mode

The SPI Slave module provides a low pin count synchronous slave interface that facilitates communication between the LAN9252 and a host system. The SPI/Slave allows access to the System CSRs, internal FIFOs and registers. It supports single and multiple register read and write commands with incrementing, decrementing and static addressing. Single-bit lane is supported in SPI mode.

The jumper detailed in [Table 2-2](#) and switch detailed in [Table 2-3](#) should be configured as shown in order to use SPI Mode.

TABLE 2-2: SPI MODE JUMPER CONFIGURATION

Jumper	Short Pins
J1	1,2

TABLE 2-3: SPI MODE SWITCH CONFIGURATION

Switch	Short Pins	Knob Position
SW2	1-2, 4-5	Right Side

2.6.1.1.2 DIGIO Mode

For simple modules without microcontrollers, the LAN9252 can operate in Digital IO mode where 16 digital signals can be controlled or monitored by the EtherCAT master. Six control signals are also provided.

The jumper detailed in [Table 2-4](#) and switch detailed in [Table 2-5](#) should be configured as shown in order to use DIGIO Mode.

TABLE 2-4: DIGIO MODE JUMPER CONFIGURATION

Jumper	Short Pins
J1	2,3

TABLE 2-5: DIGIO MODE SWITCH CONFIGURATION

Switch	Short Pins	Knob Position
SW2	2-3, 5-6	Left Side

2.6.1.2 EVB-LAN9252-ADD-ON SOFTWARE CONFIGURATION

To configure the LAN9252 in SPI/DIGIO mode, the appropriate binary files (ESI files) must be programmed into the LAN9252 configuration EEPROM.

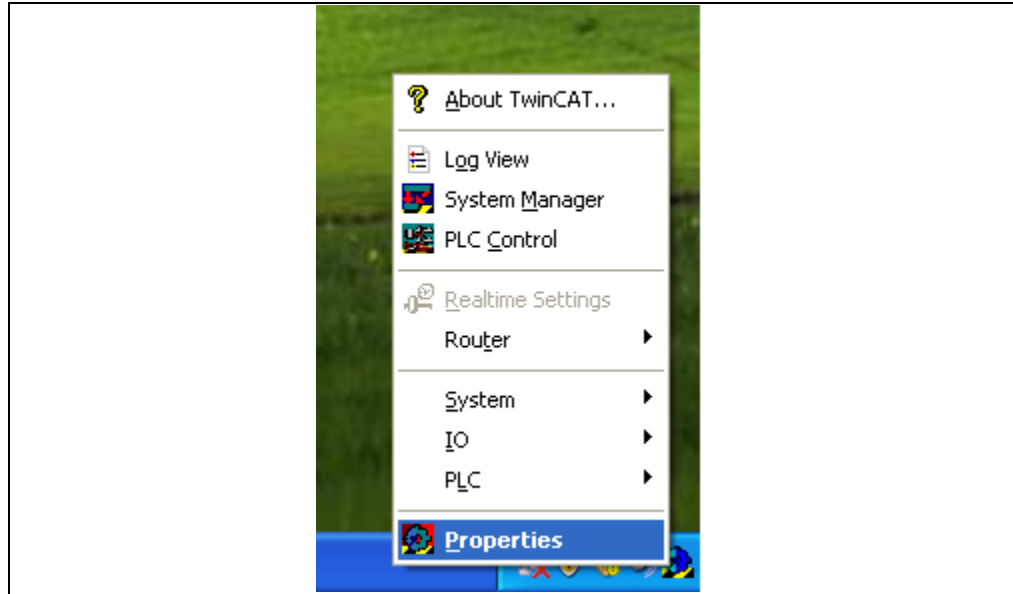
The LAN9252 configures itself to the desired mode by reading the strap settings located in EEPROM. The LAN9252 EEPROM is programmed and validated via the TwinCAT master tool. The programming procedure is as follows:

- Note 1:** This example utilizes the TwinCAT tool. Procedures may differ when using other EtherCAT master tools.
- 2:** Ensure the system network properties are configured properly for the EtherCAT frames, Ethernet cable linking your system, and EtherCAT slave board.

Board Details & Configuration

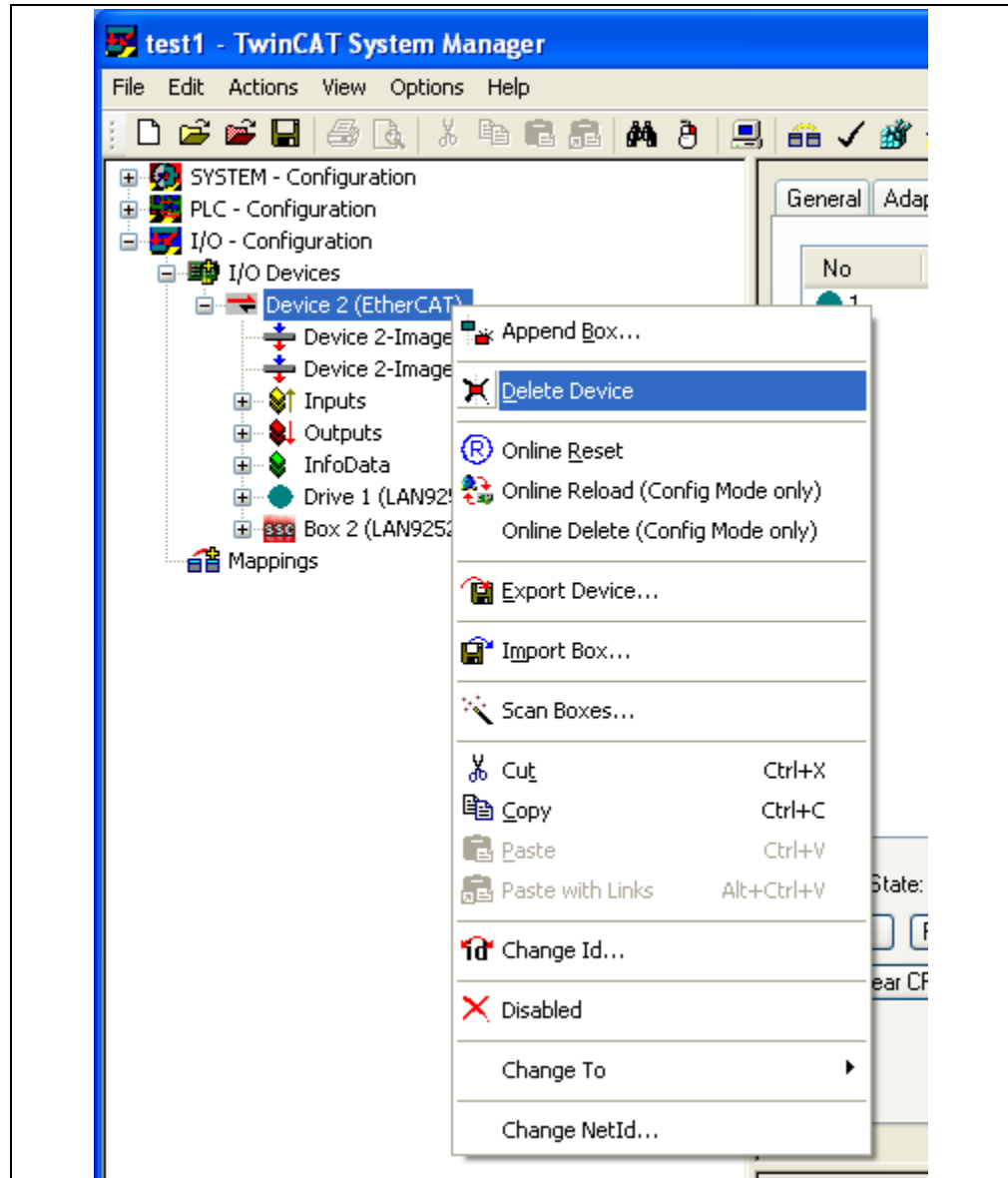
1. Load the corresponding EtherCAT Slave Information (ESI) file in the directory path “C:\TwinCAT\Io\EtherCAT” and select the appropriate ESI file. For this demo, the ESI file for the SPI Mode is used.
2. If TwinCAT installed successfully, a TwinCAT icon will be displayed in the bottom right corner of the desktop. After clicking the icon, a pop-up list will display. Select “System Manager,” as displayed in [Figure 2-3](#).

FIGURE 2-3: TWINCAT SYSTEM MANAGER



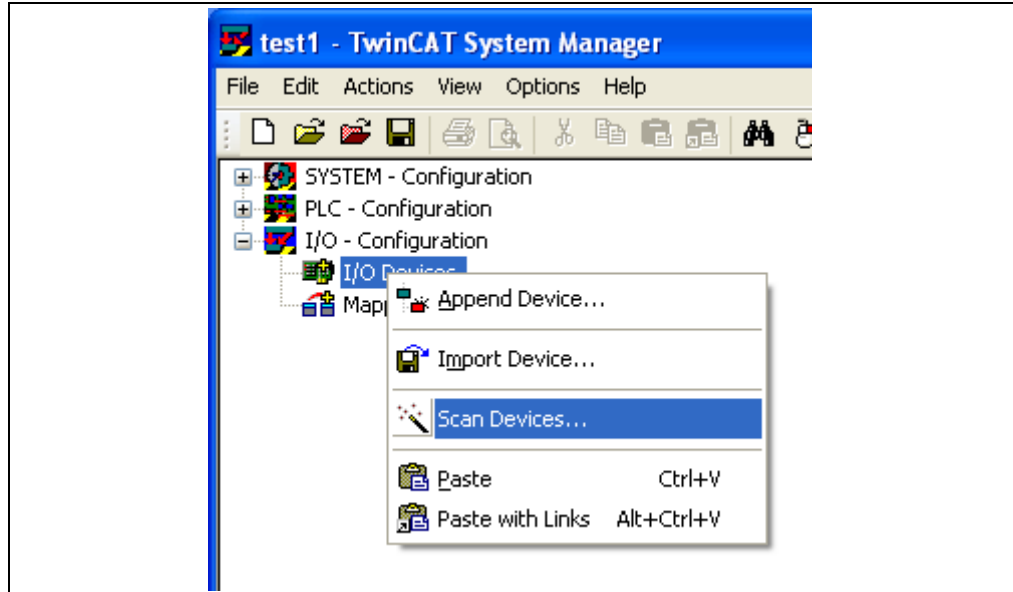
3. If any devices are present, delete them accordingly by clicking the device and selecting “Delete Device”, as displayed in [Figure 2-4](#).

FIGURE 2-4: TWINCAT DELETE DEVICE



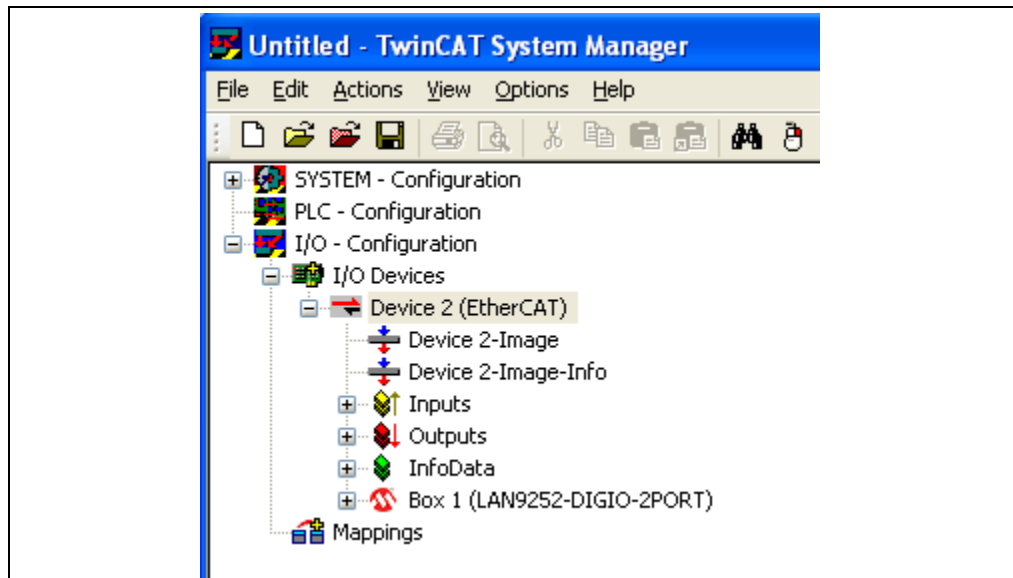
4. Scan for EtherCAT slave devices by clicking "I/O devices" and selecting "Scan Devices", as displayed in [Figure 2-5](#).

FIGURE 2-5: TWINCAT SCAN DEVICES



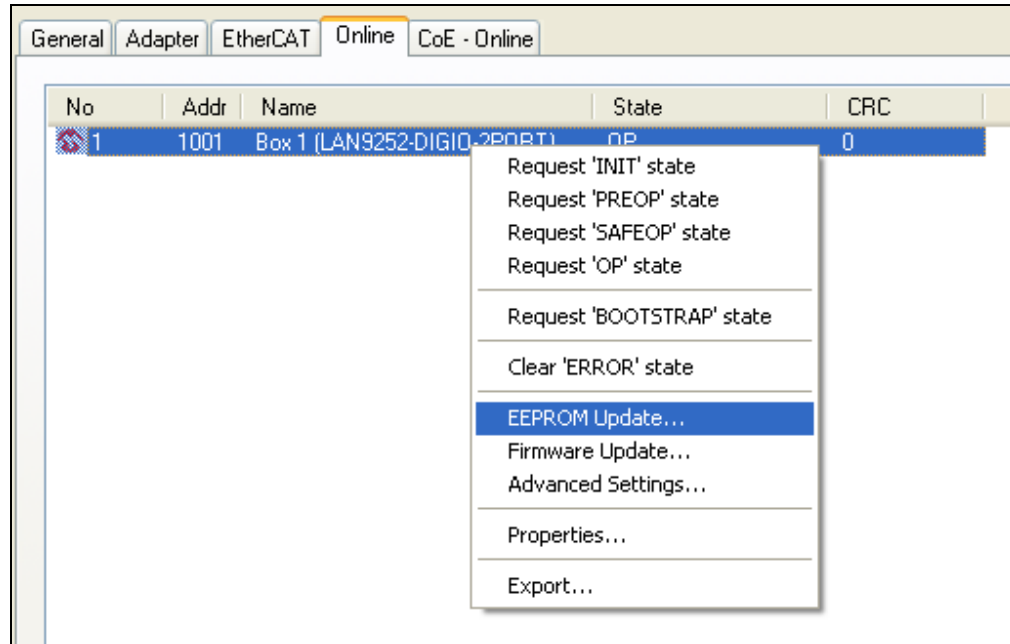
5. After scanning is complete, the right panel of the TwinCAT window will appear as displayed in [Figure 2-6](#).

FIGURE 2-6: TWINCAT DEVICE LIST



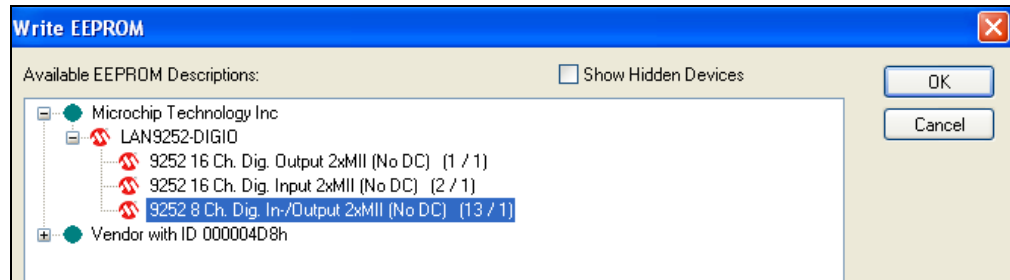
6. After a successful scan, click the “Device 2 (EtherCAT)” drop-down bar on the left panel of the TwinCAT tool (as highlighted in [Figure 2-5](#)). Then click the “Online” tab on the right-side panel of the TwinCAT tool, as displayed in [Figure 2-6](#). Right-click the LAN9252 listing and select “EEPROM Update” from the contextual menu as displayed in [Figure 2-7](#).

FIGURE 2-7: TWINCAT EEPROM UPDATE



7. Upon selecting “EEPROM Update”, the Write EEPROM window will open. Click the “OK” button to initiate EEPROM programming as displayed in [Figure 2-8](#).

FIGURE 2-8: TWINCAT WRITE EEPROM



Board Details & Configuration

2.6.2 Configuring PDI Selection on the Beckhoff EL9800

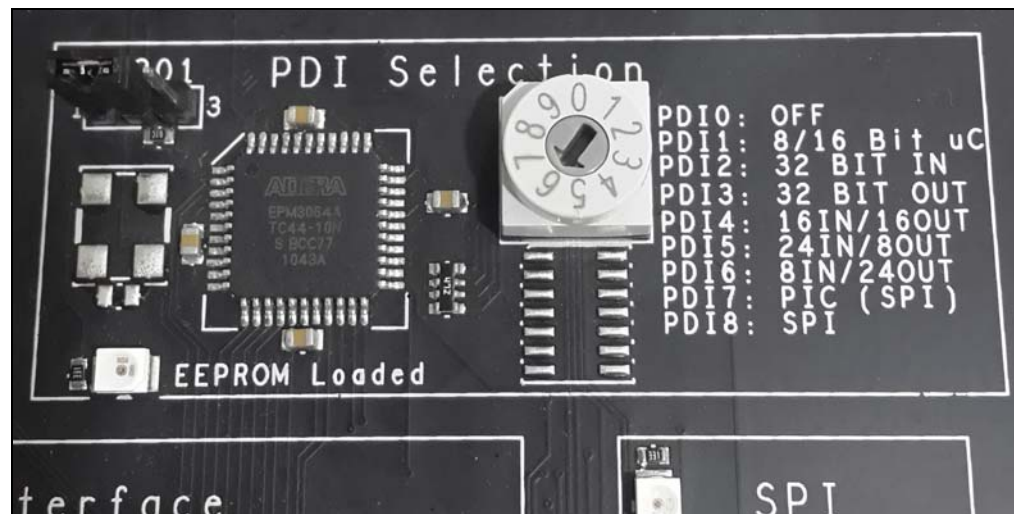
Selection of the different PDIs on the EL9800 takes place using a rotary selector in the PDI-Selection Area. Based on the four physical PDIs, eight logical PDIs are selectable over the PDI-Selector. The selector positions listed in [Table 2-6](#) activate the corresponding PDI.

TABLE 2-6: BECKHOFF EL9800 SELECTOR POSITIONS

Position	Process Data Interface
0	OFF
1	8/16 Bit Microcontroller Interface
2	32-Bit Digital interface - 32 Inputs
3	32-Bit Digital interface - 32 Outputs
4	32-Bit Digital interface - 16 Inputs / 16 Outputs
5	32-Bit Digital interface - 24 Inputs / 8 Outputs
6	32-Bit Digital interface - 8 Inputs / 24 Outputs
7	PIC (SPI)
8	SPI

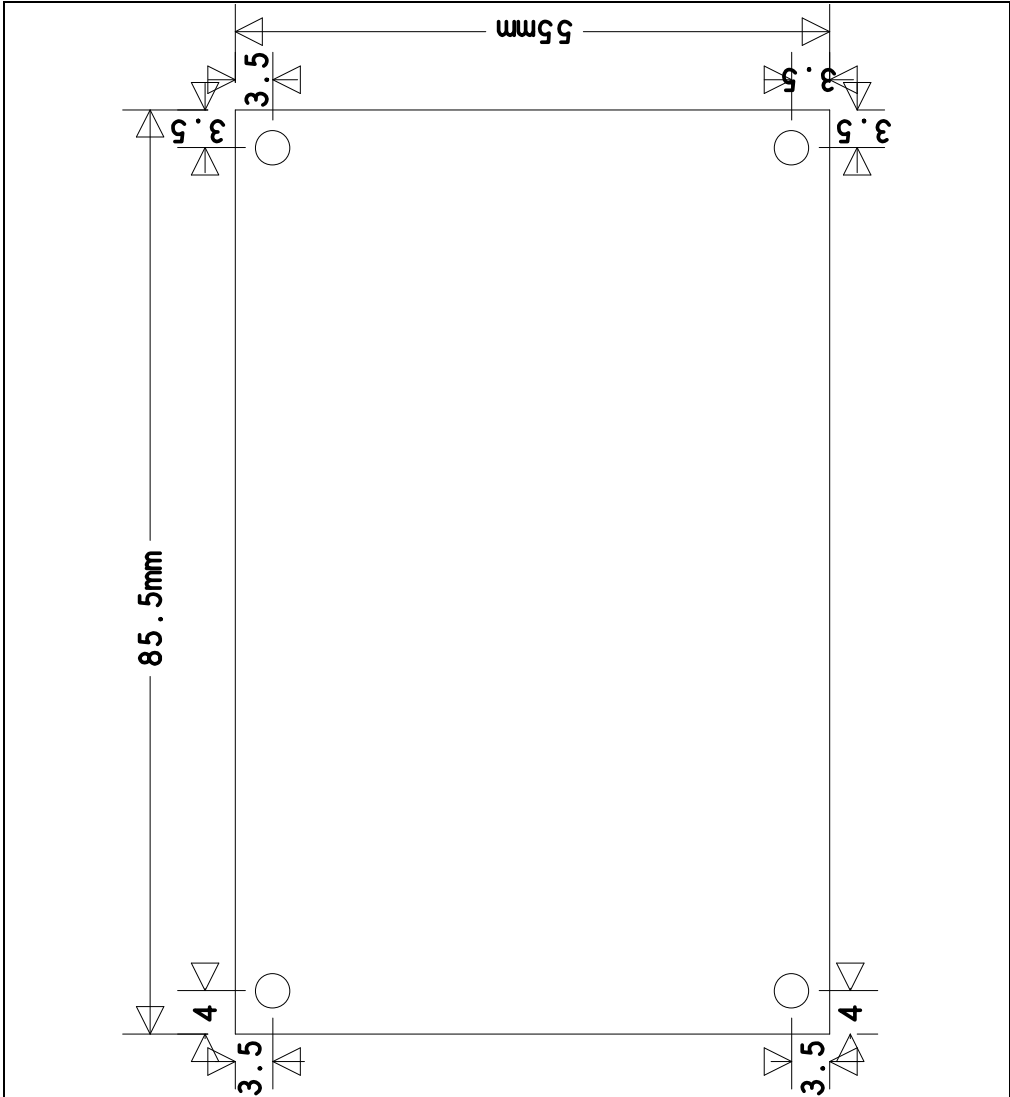
Note: The Selector Position must be changed to Position 6 to select DIGIO -8 Ch.Dig IN-/OUT Test by Default.

FIGURE 2-9: PDI SELECTOR HEX SWITCH ON THE BECKHOFF EL9800



2.7 MECHANICALS

FIGURE 2-10: EVB-LAN9252-ADD-ON MECHANICAL DIMENSIONS

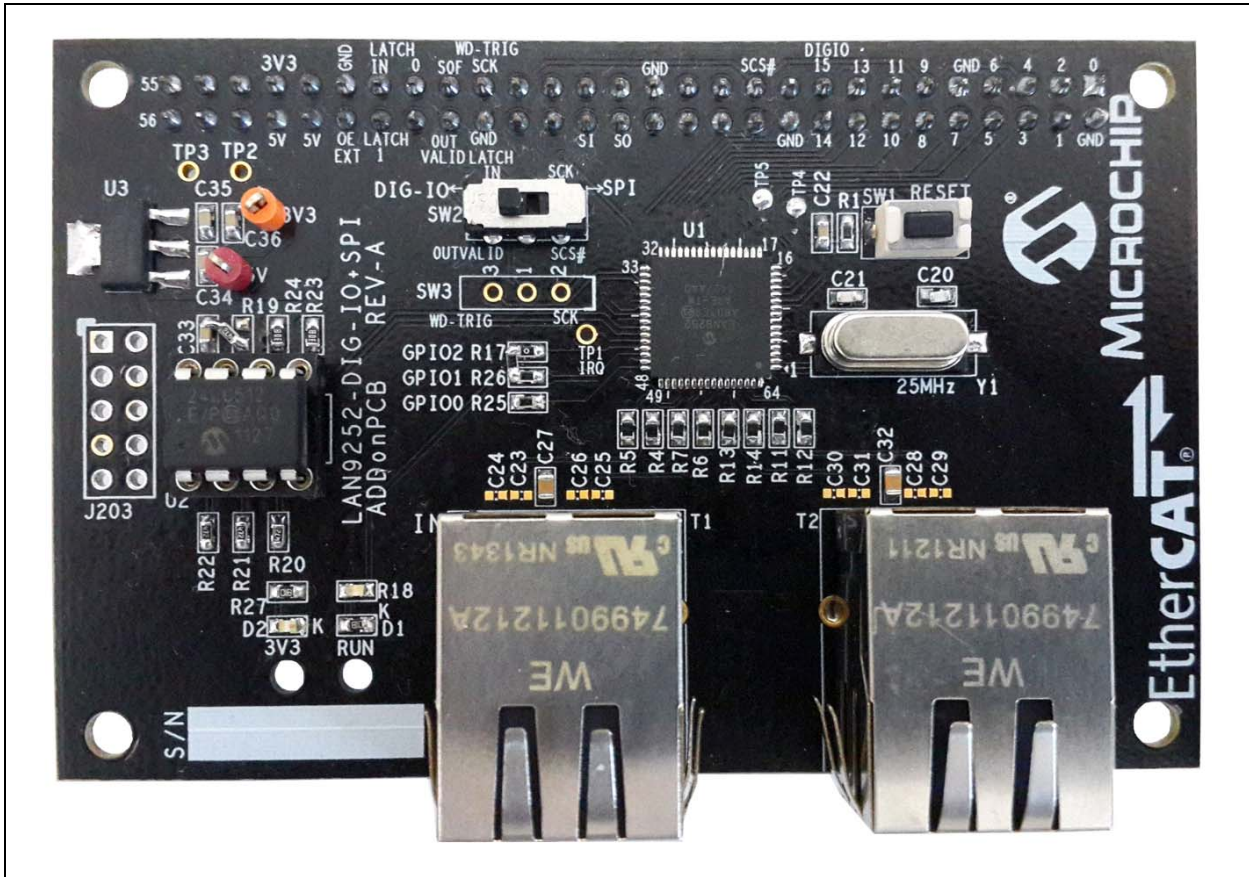


Appendix A. Evaluation Board Photo

A.1 INTRODUCTION

This appendix shows the EVB-LAN9252-ADD-ON Evaluation Board.

FIGURE A-1: EVB-LAN9252-ADD-ON EVALUATION BOARD



NOTES:

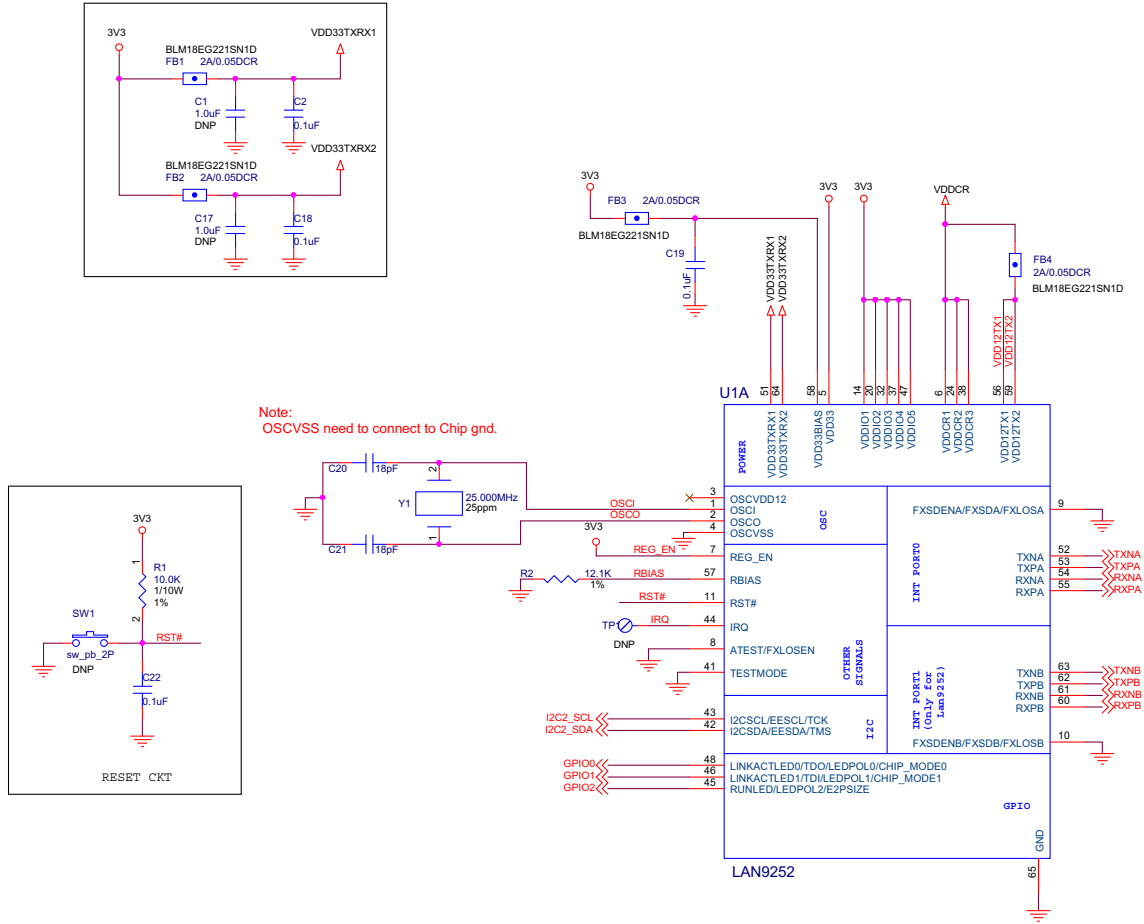


Appendix B. Evaluation Board Schematics

B.1 INTRODUCTION

This appendix shows the EVB-LAN9252-ADD-ON Evaluation Board Schematics.

FIGURE B-1: EVB-LAN9252-ADD-ON SCHEMATIC LAN9252



Note:
OSC VSS need to connect to Chip gnd.

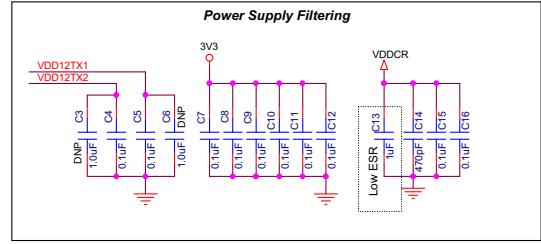
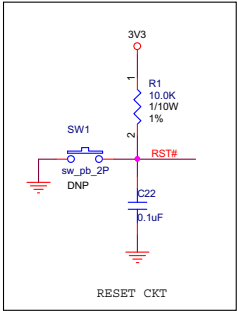


FIGURE B-2: EVB-LAN9252-ADD-ON SCHEMATIC COPPER MODE INTERFACE

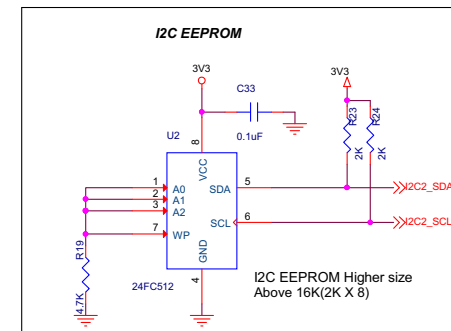
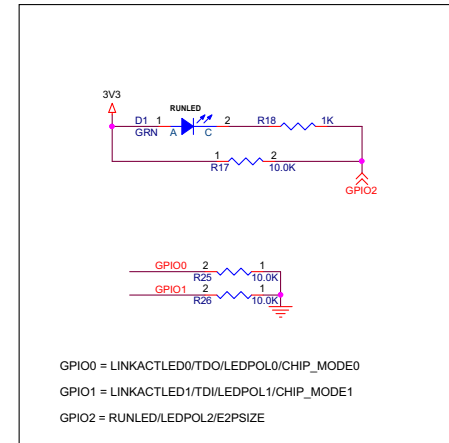
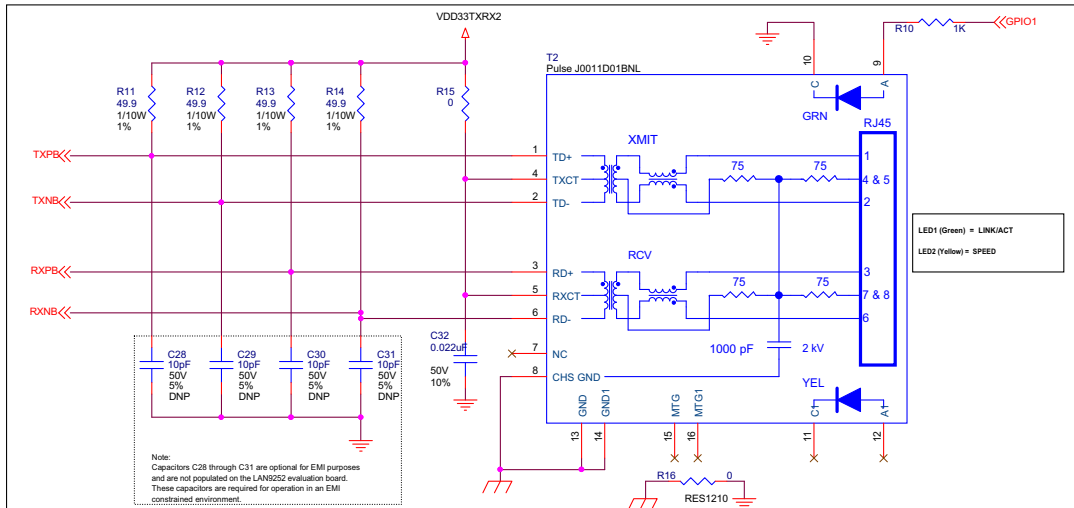
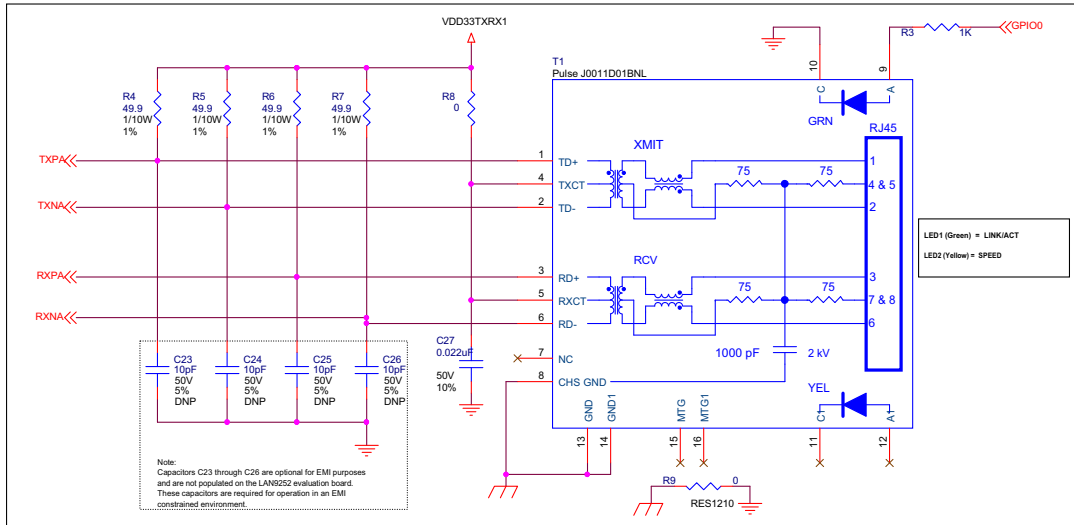
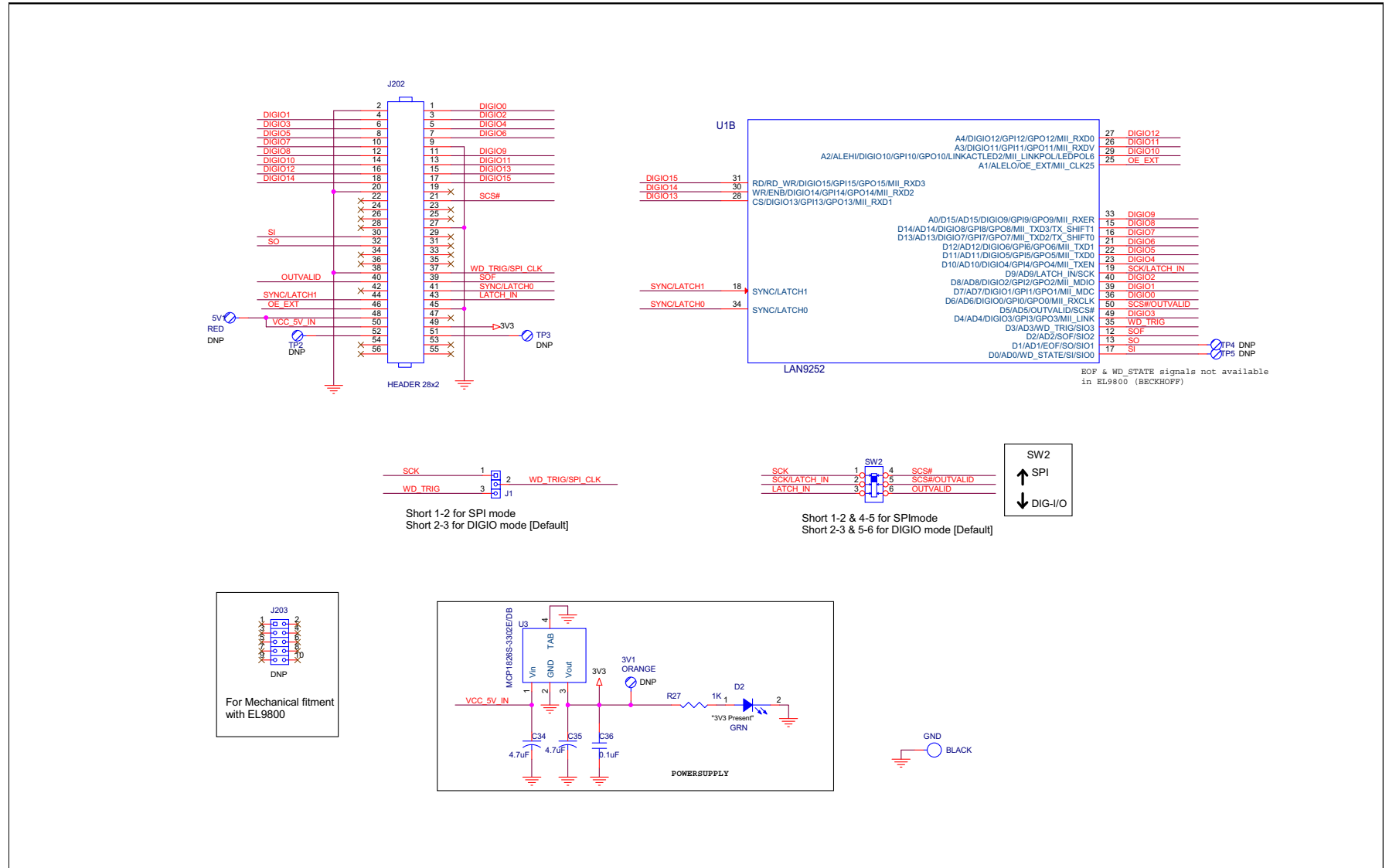


FIGURE B-3: EVB-LAN9252-ADD-ON SCHEMATIC BOARD-TO-BOARD INTERFACE





Appendix C. Bill of Materials (BOM)

C.1 INTRODUCTION

This appendix includes the EVB-LAN9252-ADD-ON Evaluation Board Bill of Materials (BOM).

TABLE C-1: EVB-LAN9292 ADD ON-BOM-REV-B BILL OF MATERIALS (BOM)

Item	Qty	Reference	Part	PCB Footprint	Vendor	Vendor P/N
2	16	C2,C4,C5,C7,C8,C9,C10,C11,C12,C15,C16,C18,C19,C22,C33,C36	0.1uF	CAP0603	Murata	GRM188R71E104KA01D
3	1	C13	1uF	CAP0603	Murata	GRM188R61C105KA93D
4	1	C14	470pF	CAP0603	Murata	GRM188R61C105KA93D
5	2	C20,C21	18pF	CAP0603	Murata	GRM033R71E471KA01D
7	2	C27,C32	0.022uF	CAP0603	Murata	GRM1885C1H180JA01D
8	2	C34,C35	4.7uF	CAP0603	Kemet	C0603C223K5RACTU
9	2	D1,D2	GRN	LED0603	Murata	GRM188R60J475KE19D
10	4	FB1,FB2,FB3,FB4	2A/0.05DCR	RES0603	Würth electronics	150 060 GS7 500 0
12	1	J1	HDR_1x3	TH_CONN_1X3P	Murata	BLM18EG221SN1D
13	1	J202	HEADER 28x2	th_conn_2x28p_F	Sullins Connector Solutions	PPTC282LFBN-RC
15	4	R1,R17,R25,R26	10.0K	RES0603	Panasonic	ERJ-3EKF1002V
16	1	R2	12.1K	RES0603	Rohm	MCR03ERTF1212
17	4	R3,R10,R18,R27	1K	RES0603	Panasonic	ERJ-3GEYJ102V
18	8	R4,R5,R6,R7,R11,R12,R13,R14	49.9	RES0603	Yageo	RC0603FR-0749R9L
19	2	R8,R15	0	RES0603	Panasonic	ERJ-3GEY0R00V
20	2	R9,R16	0	RES1210	Vishay	CRCW12100000Z0EA
21	1	R19	4.7K	RES0603	Panasonic	ERJ-3EKF4701V
22	2	R23,R24	2K	RES0603	Panasonic	ERJ-3GEYJ202V
24	1	SW2	JS202011CQN	TH_SW_DPD2_6P	Panasonic	JS202011CQN
26	2	T1,T2	Pulse - J0011D01BNL	th_conn_pulse_rj45_j0026	Pulse Electronics	J0011D01BNL
27	1	U1	LAN9252	IC_QFN64	Microchip	LAN5252
28	1	U2	24FC512	IC_SO8	Microchip	25FC512-I/SN
29	1	U3	MCP1826S-3302E/DB	SOT223	Microchip	MCP1826S-3302E/DB
30	1	Y1	25.000MHz	XTAL_HCM49	Cardinal Components Inc.	CSM1Z-A5B2C5-40-25.0D18-F

TABLE C-2: EVB-LAN9292 ADD ON-BOM-REV-B DO NOT POPULATE (DNP) COMPONENTS

Item	Qty	Reference	Part	PCB Footprint
1	4	C1,C3,C6,C17	1.0uF	CAP0603
6	8	C23,C24,C25,C26,C28,C29,C30,C31	10pF	CAP0402
11	1	GND	BLACK	TH_TP_60D40
14	1	J203	2x5	TH_CONN_2X5P
23	1	SW1	EVQ-PJU04K	sw_pb_2P
25	5	TP1,TP2,TP3,TP4,TP5	BergStick	TH_TP_60D40
31	1	3V1	ORANGE	TH_TP_60D40
32	1	5V1	RED	TH_TP_60D40



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