MIC5246

150mA μCap CMOS LDO Regulator

Final Information

General Description

The MIC5246 is an efficient, precise CMOS voltage regulator optimized for low-noise applications. The MIC5246 offers better than 1% initial accuracy, extremely-low-dropout voltage (typically 150mV at 150mA) and constant ground current (typically 85μA)over load . The MIC5246 features an error flag that indicates an output fault condition such as overcurrent, thermal shutdown and dropout. The MIC5246 provides a very low noise output, ideal for RF applications where quiet voltage sources are required.

Designed specifically for hand-held and battery-powered devices, the MIC5246 provides a TTL-logic-compatible enable pin. When disabled, power consumption drops nearly to zero.

The MIC5246 also works with low-ESR ceramic capacitors, reducing the amount of board space necessary for power applications, critical in hand-held wireless devices.

Key features include current limit, thermal shutdown, a push-pull output for faster transient response, and an active clamp to speed up device turnoff. Available in the IttyBitty™ SOT-23-5 package, the MIC5246 also offers a range of fixed output voltages.

Features

- Error flag indicates fault condition
- Ultralow dropout—100mV @ 100mA
- Load independent, ultralow ground current: 85μA
- 150mA output current
- Current limiting
- Thermal Shutdown
- Tight load and line regulation
- “Zero” off-mode current
- Stability with low-ESR capacitors
- Fast transient response
- TTL-Logic-controlled enable input

Applications

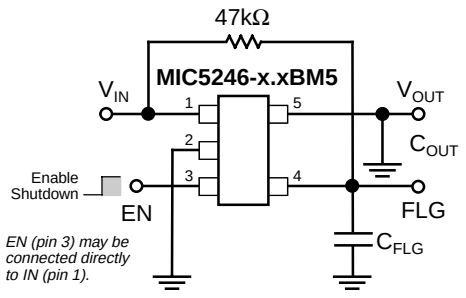
- Cellular phones and pagers
- Cellular accessories
- Battery-powered equipment
- Laptop, notebook, and palmtop computers
- PCMCIA V_{CC} and V_{PP} regulation/switching
- Consumer/personal electronics
- SMPS post-regulator/dc-to-dc modules
- High-efficiency linear power supplies

Ordering Information

Part Number	Marking	Voltage	Junction Temp. Range	Package
MIC5246-2.6BM5	LT26	2.6V	–40°C to +125°C	SOT-23-5
MIC5246-2.7BM5	LT27	2.7V	–40°C to +125°C	SOT-23-5
MIC5246-2.8BM5	LT28	2.8V	–40°C to +125°C	SOT-23-5
MIC5246-2.85BM5	LT2J	2.85V	–40°C to +125°C	SOT-23-5
MIC5246-3.0BM5	LT30	3.0V	–40°C to +125°C	SOT-23-5
MIC5246-3.3BM5	LT33	3.3V	–40°C to +125°C	SOT-23-5

Other voltages available. Contact Micrel for details.

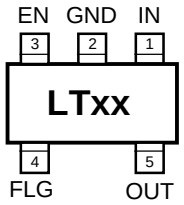
Typical Application



Low-Noise Regulator Application

IttyBitty is a trademark of Micrel, Inc.

Pin Configuration



MIC5246-x.xBM5

Pin Description

Pin Number	Pin Name	Pin Function
1	IN	Supply Input
2	GND	Ground
3	EN	Enable/Shutdown (Input): CMOS compatible input. Logic high = enable; logic low = shutdown. Do not leave open.
4	FLG	Error Flag (Output): Open-drain output. Active low indicates an output undervoltage condition.
5	OUT	Regulator Output

Absolute Maximum Ratings (Note 1)

Supply Input Voltage (V_{IN}) ..... 0V to +7V
Enable Input Voltage (V_{EN}) ..... 0V to V_{IN}
Flag Output Voltage (V_{FLG}) ..... 0V to V_{IN}
Junction Temperature (T_J) ..... +150°C
Storage Temperature ..... –65°C to +150°C
Lead Temperature (soldering, 5 sec.) ..... 260°C
ESD, **Note 3**

Operating Ratings (Note 2)

Input Voltage (V_{IN}) ..... +2.7V to +6V
Enable Input Voltage (V_{EN}) ..... 0V to V_{IN}
Flag Output Voltage (V_{FLG}) ..... 0V to V_{IN}
Junction Temperature (T_J) ..... –40°C to +125°C
Thermal Resistance (θ_{JA}) ..... 235°C/W

Electrical Characteristics

$V_{IN} = V_{OUT} + 1V$, $V_{EN} = V_{IN}$; $I_{OUT} = 100\mu A$; $T_J = 25°C$, **bold** values indicate $–40°C \le T_J \le +125°C$; unless noted.

Symbol	Parameter	Conditions	Min	Typical	Max	Units
V_O	Output Voltage Accuracy	I_{OUT} = 0mA	–1 **–2**		1 **2**	% %
ΔV_{LNR}	Line Regulation	$V_{IN} = V_{OUT}$ + 0.1V to 6V	–0.3	0	0.3	%/V
ΔV_{LDR}	Load Regulation	I_{OUT} = 0.1mA to 150mA, **Note 4**		2	3	%
$V_{IN} – V_{OUT}$	Dropout Voltage, **Note 5**	I_{OUT} = 100µA		1.5	5	mV
		I_{OUT} = 50mA		50	85	mV
		I_{OUT} = 100mA		100	150	mV
		I_{OUT} = 150mA		150	200 **250**	mV mV
I_Q	Quiescent Current	$V_{EN} \le 0.4V$ (shutdown)		0.2	1	µA
I_{GND}	Ground Pin Current, **Note 6**	I_{OUT} = 0mA		85	150	µA
		I_{OUT} = 150mA		85	150	µA
PSRR	Power Supply Rejection	f = 120Hz, $C_{OUT} = 10\mu F$		50		dB
I_{LIM}	Current Limit	V_{OUT} = 0V	160	300		mA
Enable Input						
V_{IL}	Enable Input Logic-Low Voltage	V_{IN} = 2.7V to 5.5V, regulator shutdown		0.8	**0.4**	V
V_{IH}	Enable Input Logic-High Voltage	V_{IN} = 2.7V to 5.5V, regulator enabled	**1.6**	1		V
I_{EN}	Enable Input Current	$V_{IL} \le 0.4V$		0.01		µA
		$V_{IH} \ge 1.6V$		0.01		µA
	Shutdown Resistance Discharge			500		Ω
Thermal Protection						
	Thermal Shutdown Temperature			150		°C
	Thermal Shutdown Hysteresis			10		°C
Error Flag						
V_{FLG}	Low Threshold High Threshold	% of V_{OUT} (Flag ON) % of V_{OUT} (Flag OFF)	90		 96	% %
V_{OL}	Output Logic-Low Voltage	$I_L = 100\mu A$, fault condition		0.02	0.4	V
I_{FL}	Flag Leakage Current	flag off, V_{FLG} = 6V		0.01		µA

Note 1. Exceeding the absolute maximum rating may damage the device.

Note 2. The device is not guaranteed to function outside its operating rating.

Note 3. Devices are ESD sensitive. Handling precautions recommended.

Note 4. Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 0.1mA to 150mA. Changes in output voltage due to heating effects are covered by the thermal regulation specification.

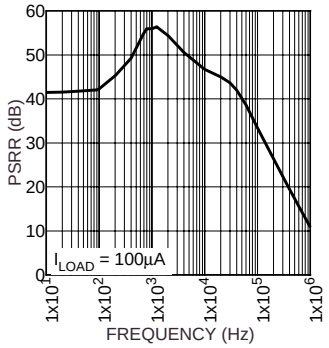
Note 5. Dropout voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value measured at 1V differential. For outputs below 2.7V, dropout voltage is the input-to-output voltage differential with the minimum input voltage 2.7V. Minimum input operating voltage is 2.7V.

Note 6. Ground pin current is the regulator quiescent current. The total current drawn from the supply is the sum of the load current plus the ground pin current.

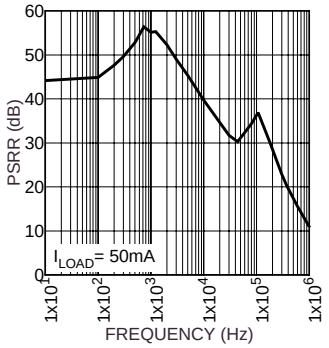
Note 7. The error flag is a function of the output voltage being 5% low and the detection of one of the following: overcurrent, overtemperature or dropout. See “Applications Information” section for additional information.

Typical Characteristics

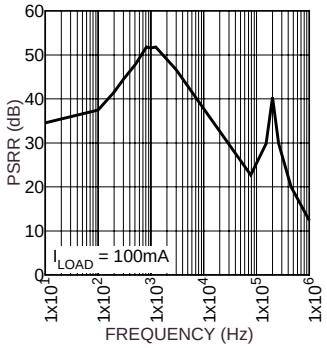
Power Supply Rejection Ratio



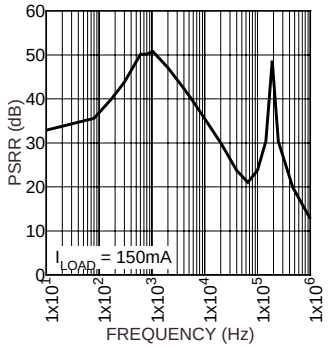
Power Supply Rejection Ratio



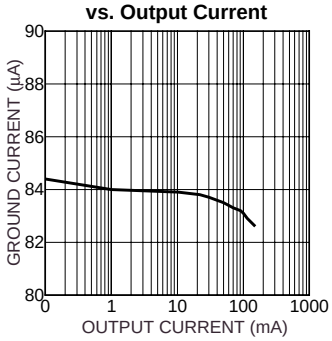
Power Supply Rejection Ratio



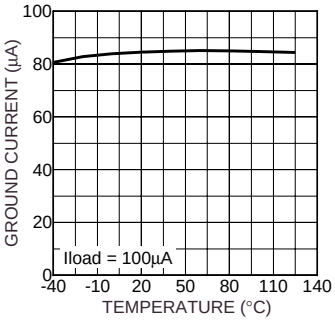
Power Supply Rejection Ratio



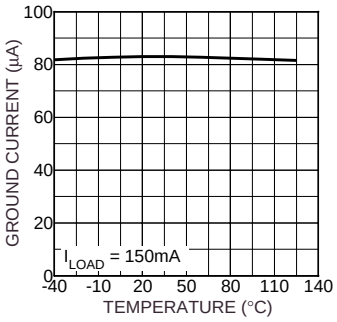
Ground Current vs. Output Current



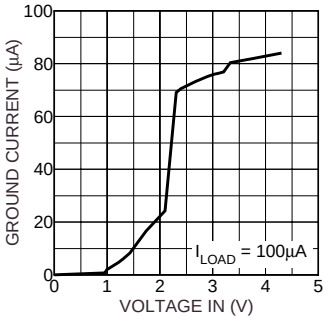
Ground Pin Current



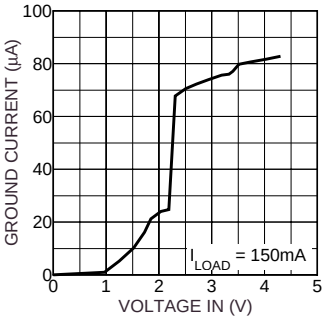
Ground Pin Current



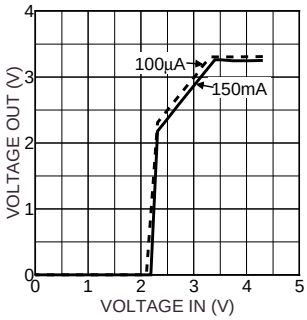
Ground Pin Current



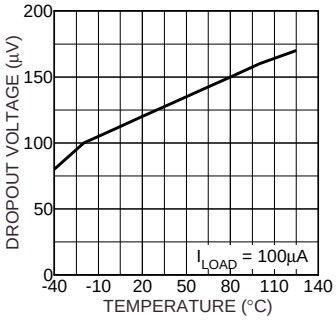
Ground Pin Current



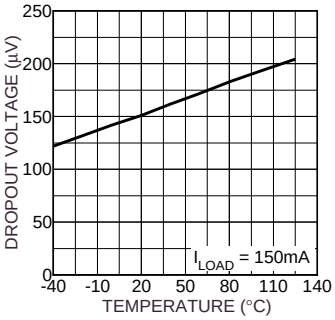
Dropout Characteristics



Dropout Voltage

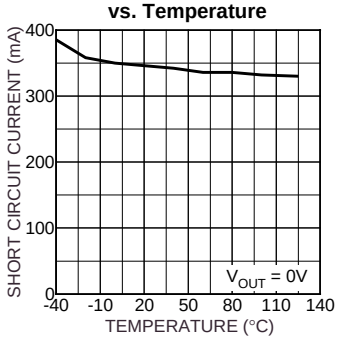


Dropout Voltage

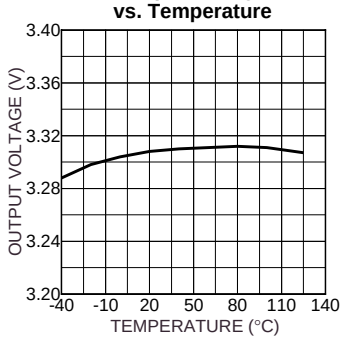


Typical Characteristics

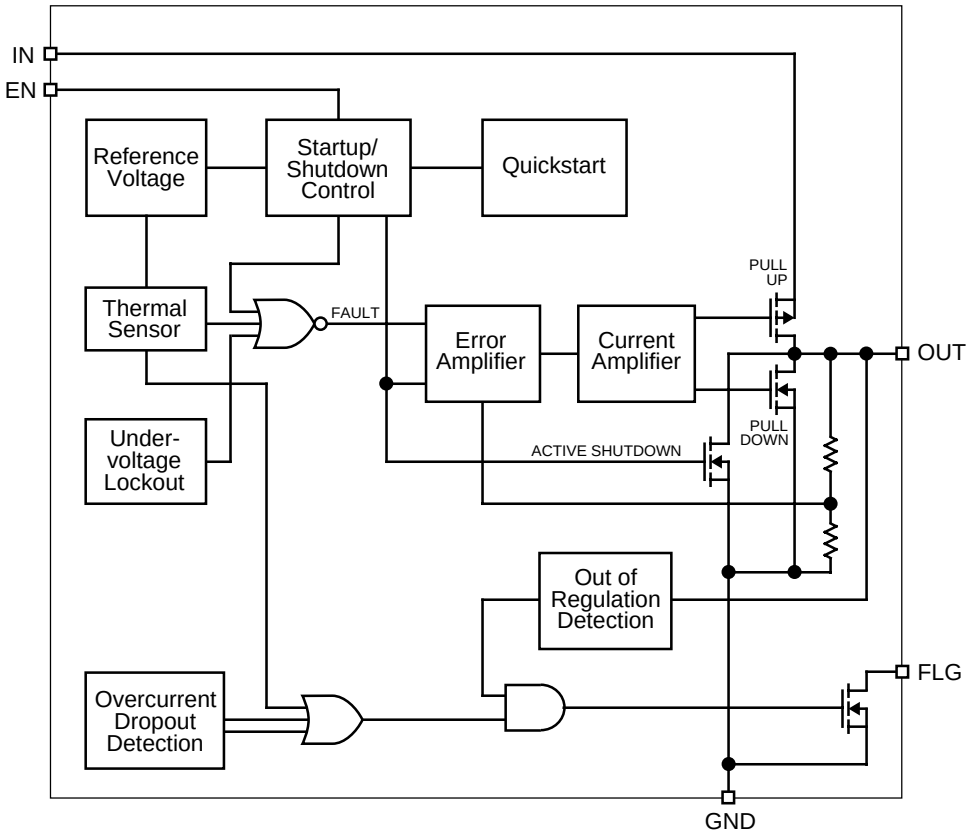
Short Circuit Current vs. Temperature



Output Voltage vs. Temperature



Block Diagrams



Applications Information

Enable/Shutdown

The MIC5246 comes with an active-high enable pin that allows the regulator to be disabled. Forcing the enable pin low disables the regulator and sends it into a "zero" off-mode-current state. In this state, current consumed by the regulator goes nearly to zero. Forcing the enable pin high enables the output voltage. This part is CMOS and the enable pin cannot be left floating; a floating enable pin may cause an indeterminate state on the output.

Input Capacitor

An input capacitor is not required for stability. A 1µF input capacitor is recommended when the bulk ac supply capacitance is more than 10 inches away from the device, or when the supply is a battery.

Output Capacitor

The MIC5246 requires an output capacitor for stability. The design requires 1µF or greater on the output to maintain stability. The capacitor can be a low-ESR ceramic chip capacitor. The MIC5246 has been designed to work specifically with the low-cost, small chip capacitors. Tantalum capacitors can also be used for improved capacitance over temperature. The value of the capacitor can be increased without bound.

X7R dielectric ceramic capacitors are recommended because of their temperature performance. X7R-type capacitors change capacitance by 15% over their operating temperature range and are the most stable type of ceramic capacitors. Z5U and Y5V dielectric capacitors change value by as much 50% and 60% respectively over their operating temperature ranges. To use a ceramic chip capacitor with Y5V dielectric, the value must be much higher than an X7R ceramic or a tantalum capacitor to ensure the same minimum capacitance value over the operating temperature range. Tantalum capacitors have a very stable dielectric (10% over their operating temperature range) and can also be used with this device.

Error Flag

The error flag output is an active-low, open-drain output that drives low when a fault condition AND an undervoltage detection occurs. Internal circuitry intelligently monitors overcurrent, overtemperature and dropout conditions and ORs these outputs together to indicate some fault condition. The output of that OR gate is ANDed with an output voltage monitor that detects an undervoltage condition. That output drives the open-drain transistor to indicate a fault. This prevents chattering or inadvertent triggering of the error flag. The error flag must be pulled up using a resistor from the flag pin to either the input or the output.

Error Flag Circuit

The error flag circuit was designed essentially to work with a capacitor to ground to act as a power-on reset generator, signaling a power-good situation once the regulated voltage was up and/or out of a fault condition. This capacitor delays the error signal from pulling high, allowing the downstream circuits time to stabilize. When the error flag is pulled up to the input without using a pull-down capacitor, then there can be a glitch on the error flag upon start up of the device. This is due to the response time of the error flag circuit as the device starts up. When the device comes out of the zero off mode current state, all the various nodes of the circuit power up before the device begins supplying full current to the output capacitor. The error flag drives low immediately and then releases after a few microseconds. The intelligent circuit that triggers an error detects the output going into current limit AND the output being low while charging the output capacitor. The error output then pulls low for the duration of the turn-on time. This glitch is filtered by putting a capacitor from the error flag to ground. The glitch does not occur if the error flag pulled up to the output.

Transient Response

The MIC5246 implements a unique output stage to dramatically improve transient response recovery time. The output is a totem-pole configuration with a P-channel MOSFET pass device and an N-channel MOSFET clamp. The N-channel clamp is a significantly smaller device that prevents the output voltage from overshooting when a heavy load is removed. This feature helps to speed up the transient response by significantly decreasing transient response recovery time during the transition from heavy load (100mA) to light load (85µA).

Active Shutdown

The MIC5246 also features an active shutdown clamp, which is an N-channel MOSFET that turns on when the device is disabled. This allows the output capacitor and load to discharge, de-energizing the load.

Thermal Considerations

The MIC5246 is designed to provide 150mA of continuous current in a very small package. Maximum power dissipation can be calculated based on the output current and the voltage drop across the part. To determine the maximum power dissipation of the package, use the junction-to-ambient thermal resistance of the device and the following basic equation:

$$P_{D(max)} = \left(\frac{T_{J(max)} - T_A}{\theta_{JA}} \right)$$

$T_{J(max)}$ is the maximum junction temperature of the die, 125°C, and T_A is the ambient operating temperature. θ_{JA} is layout dependent; Table 1 shows examples of junction-to-ambient thermal resistance for the MIC5246.

Package	θ_{JA} Recommended Minimum Footprint	θ_{JA} 1" Square Copper Clad	θ_{JC}
SOT-23-5 (M5)	235°C/W	185°C/W	145°C/W

Table 1. SOT-23-5 Thermal Resistance

The actual power dissipation of the regulator circuit can be determined using the equation:

$$P_D = (V_{IN} - V_{OUT})\ I_{OUT} + V_{IN}\ I_{GND}$$

Substituting $P_{D(max)}$ for P_D and solving for the operating conditions that are critical to the application will give the maximum operating conditions for the regulator circuit. For example, when operating the MIC5246-3.0BM5 at 50°C with a minimum footprint layout, the maximum input voltage for a set output current can be determined as follows:

$$P_{D(max)} = \left(\frac{125°C - 50°C}{235°C/W} \right)$$

$$P_{D(max)} = 315mW$$

The junction-to-ambient thermal resistance for the minimum footprint is 235°C/W, from Table 1. The maximum power dissipation must not be exceeded for proper operation. Using the output voltage of 3.0V and an output current of 150mA, the maximum input voltage can be determined. Because this device is CMOS and the ground current is typically 100µA over the load range, the power dissipation contributed by the ground current is < 1% and can be ignored for this calculation.

$$315mW = (V_{IN} - 3.0V)\ 150mA$$

$$315mW = V_{IN} \cdot 150mA - 450mW$$

$$810mW = V_{IN} \cdot 150mA$$

$$V_{IN(max)} = 5.4V$$

Therefore, a 3.0V application at 150mA of output current can accept a maximum input voltage of 5.4V in a SOT-23-5 package. For a full discussion of heat sinking and thermal effects on voltage regulators, refer to the Regulator Thermals section of Micrel's *Designing with Low-Dropout Voltage Regulators* handbook.

Fixed Regulator Applications

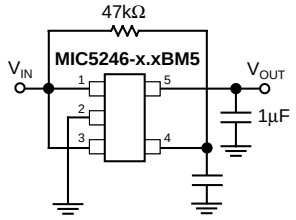


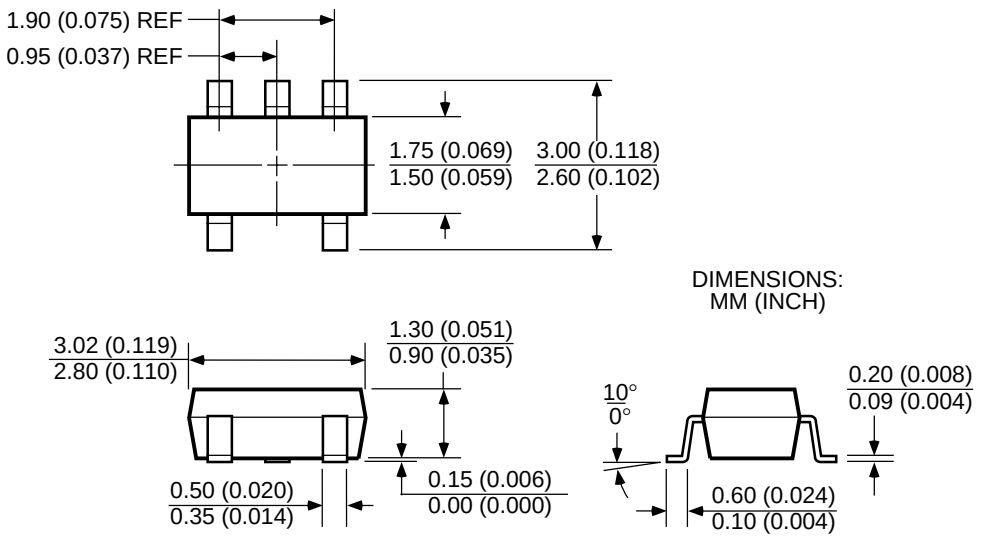
Figure 1. Low-Noise Fixed Voltage Application

Figure 1 shows a standard low-noise configuration with a 47kΩ pull-up resistor from the error flag to the input voltage and a pull-down capacitor to ground for the purpose of fault indication.

Dual-Supply Operation

When used in dual supply systems where the regulator load is returned to a negative supply, the output voltage must be diode clamped to ground.

Package Information



SOT-23-5 (M)

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