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## **N-Channel Power MOSFET 60V, 70A, 14 mΩ**

These are N-Channel power MOSFETs manufactured using the MegaFET process. This process, which uses feature sizes approaching those of LSI circuits, gives optimum utilization of silicon, resulting in outstanding performance. They were designed for use in applications such as switching regulators, switching converters, motor drivers and relay drivers. These transistors can be operated directly from integrated circuits.

Formerly developmental type TA78440.

### **Ordering Information**

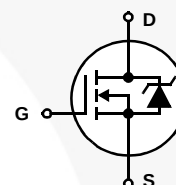
| PART NUMBER | PACKAGE  | BRAND    |
|-------------|----------|----------|
| RFP70N06    | TO-220AB | RFP70N06 |

NOTE: When ordering use the entire part number. Add the suffix 9A to obtain the TO-263AB variant in tape and reel, e.g. RF1S70N06SM9A.

### **Features**

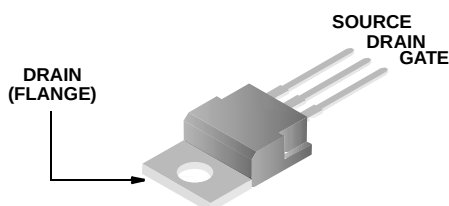
- 70A, 60V
- $r_{DS(on)} = 0.014\Omega$
- Temperature Compensated PSPICE® Model
- Peak Current vs Pulse Width Curve
- UIS Rating Curve (Single Pulse)
- 175°C Operating Temperature
- Related Literature
  - TB334 "Guidelines for Soldering Surface Mount Components to PC Boards"

### **Symbol**



### **Packaging**

**JEDEC TO-220AB**



# RFP70N06

## Absolute Maximum Ratings $T_C = 25^{\circ}\text{C}$ , Unless Otherwise Specified

|                                                                          | RFP70N06                    | UNITS                 |
|--------------------------------------------------------------------------|-----------------------------|-----------------------|
| Drain to Source Voltage (Note 1) . . . . .                               | 60                          | V                     |
| Drain to Gate Voltage ( $R_{GS} = 20\text{k}\Omega$ ) (Note 1) . . . . . | 60                          | V                     |
| Continuous Drain Current . . . . .                                       | 70                          | A                     |
| Pulsed Drain Current (Note 3) . . . . .                                  | Refer to Peak Current Curve |                       |
| Gate to Source Voltage . . . . .                                         | $\pm 20$                    | V                     |
| Single Pulse Avalanche Rating . . . . .                                  | Refer to UIS Curve          | A                     |
| Power Dissipation . . . . .                                              | 150                         | W                     |
| Linear Derating Factor . . . . .                                         | 1.0                         | W/ $^{\circ}\text{C}$ |
| Operating and Storage Temperature . . . . .                              | -55 to 175                  | $^{\circ}\text{C}$    |
| Maximum Temperature for Soldering                                        |                             |                       |
| Leads at 0.063in (1.6mm) from Case for 10s. . . . .                      | 300                         | $^{\circ}\text{C}$    |
| Package Body for 10s, See Techbrief 334 . . . . .                        | 260                         | $^{\circ}\text{C}$    |

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

### NOTE:

1.  $T_J = 25^{\circ}\text{C}$  to  $150^{\circ}\text{C}$ .

## Electrical Specifications $T_C = 25^{\circ}\text{C}$ , Unless Otherwise Specified

| PARAMETER                               | SYMBOL          | TEST CONDITIONS                                                                                                                        | MIN | TYP  | MAX       | UNITS                |
|-----------------------------------------|-----------------|----------------------------------------------------------------------------------------------------------------------------------------|-----|------|-----------|----------------------|
| Drain to Source Breakdown Voltage       | $BV_{DSS}$      | $I_D = 250\mu\text{A}$ , $V_{GS} = 0\text{V}$ (Figure 11)                                                                              | 60  | -    | -         | V                    |
| Gate Threshold Voltage                  | $V_{GS(TH)}$    | $V_{GS} = V_{DS}$ , $I_D = 250\mu\text{A}$ (Figure 10)                                                                                 | 2   | -    | 4         | V                    |
| Zero Gate Voltage Drain Current         | $I_{DSS}$       | $V_{DS} = 60\text{V}$ , $V_{GS} = 0\text{V}$                                                                                           | -   | -    | 1         | $\mu\text{A}$        |
|                                         |                 | $V_{DS} = 0.8 \times \text{Rated } BV_{DSS}$ , $T_C = 150^{\circ}\text{C}$                                                             | -   | -    | 25        | $\mu\text{A}$        |
| Gate to Source Leakage Current          | $I_{GSS}$       | $V_{GS} = \pm 20\text{V}$                                                                                                              | -   | -    | $\pm 100$ | nA                   |
| Drain to Source On Resistance (Note 2)  | $r_{DS(ON)}$    | $I_D = 70\text{A}$ , $V_{GS} = 10\text{V}$ (Figure 9)                                                                                  | -   | -    | 0.014     | $\Omega$             |
| Turn-On Time                            | $t_{(ON)}$      | $V_{DD} = 30\text{V}$ , $I_D \approx 70\text{A}$ , $R_L = 0.43\Omega$ ,<br>$V_{GS} = 10\text{V}$ , $R_{GS} = 2.5\Omega$<br>(Figure 13) | -   | -    | 190       | ns                   |
| Turn-On Delay Time                      | $t_{d(ON)}$     |                                                                                                                                        | -   | 10   | -         | ns                   |
| Rise Time                               | $t_r$           |                                                                                                                                        | -   | 137  | -         | ns                   |
| Turn-Off Delay Time                     | $t_{d(OFF)}$    |                                                                                                                                        | -   | 32   | -         | ns                   |
| Fall Time                               | $t_f$           |                                                                                                                                        | -   | 24   | -         | ns                   |
| Turn-Off Time                           | $t_{(OFF)}$     |                                                                                                                                        | -   | -    | 73        | ns                   |
| Total Gate Charge                       | $Q_{g(TOT)}$    | $V_{GS} = 0\text{V}$ to $20\text{V}$                                                                                                   | -   | 120  | 156       | nC                   |
| Gate Charge at 10V                      | $Q_{g(10)}$     | $V_{GS} = 0\text{V}$ to $10\text{V}$                                                                                                   | -   | 65   | 85        | nC                   |
| Threshold Gate Charge                   | $Q_{g(TH)}$     | $V_{GS} = 0\text{V}$ to $2\text{V}$                                                                                                    | -   | 5.0  | 6.5       | nC                   |
| Input Capacitance                       | $C_{ISS}$       | $V_{DS} = 25\text{V}$ , $V_{GS} = 0\text{V}$ , $f = 1\text{MHz}$<br>(Figure 12)                                                        | -   | 2250 | -         | pF                   |
| Output Capacitance                      | $C_{OSS}$       |                                                                                                                                        | -   | 792  | -         | pF                   |
| Reverse Transfer Capacitance            | $C_{RSS}$       |                                                                                                                                        | -   | 206  | -         | pF                   |
| Thermal Resistance, Junction to Case    | $R_{\theta JC}$ |                                                                                                                                        | -   | -    | 1.0       | $^{\circ}\text{C/W}$ |
| Thermal Resistance, Junction to Ambient | $R_{\theta JA}$ | TO-220                                                                                                                                 | -   | -    | 62        | $^{\circ}\text{C/W}$ |
|                                         |                 | -                                                                                                                                      | -   | -    | -         | -                    |

## Source to Drain Diode Specifications

| PARAMETER                     | SYMBOL   | TEST CONDITIONS                                                | MIN | TYP | MAX | UNITS |
|-------------------------------|----------|----------------------------------------------------------------|-----|-----|-----|-------|
| Source to Drain Diode Voltage | $V_{SD}$ | $I_{SD} = 70\text{A}$                                          |     | -   | 1.5 | V     |
| Reverse Recovery Time         | $t_{rr}$ | $I_{SD} = 70\text{A}$ , $dI_{SD}/dt = 100\text{A}/\mu\text{s}$ |     | -   | 52  | ns    |

### NOTES:

2. Pulse test: pulse width  $\leq 300\text{ms}$ , duty cycle  $\leq 2\%$ .
3. Repetitive rating: pulse width is limited by maximum junction temperature. See Transient Thermal Impedance curve (Figure 3) and Peak Current Capability Curve (Figure 5).

# Typical Performance Curves $T_C = 25^\circ\text{C}$ , Unless Otherwise Specified

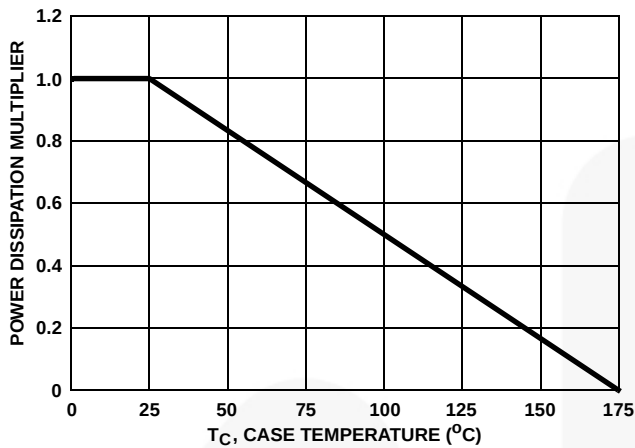


FIGURE 1. NORMALIZED POWER DISSIPATION vs CASE TEMPERATURE

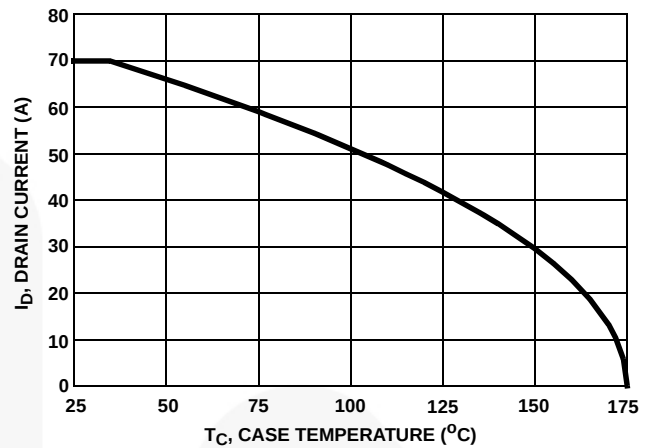


FIGURE 2. MAXIMUM CONTINUOUS DRAIN CURRENT vs CASE TEMPERATURE

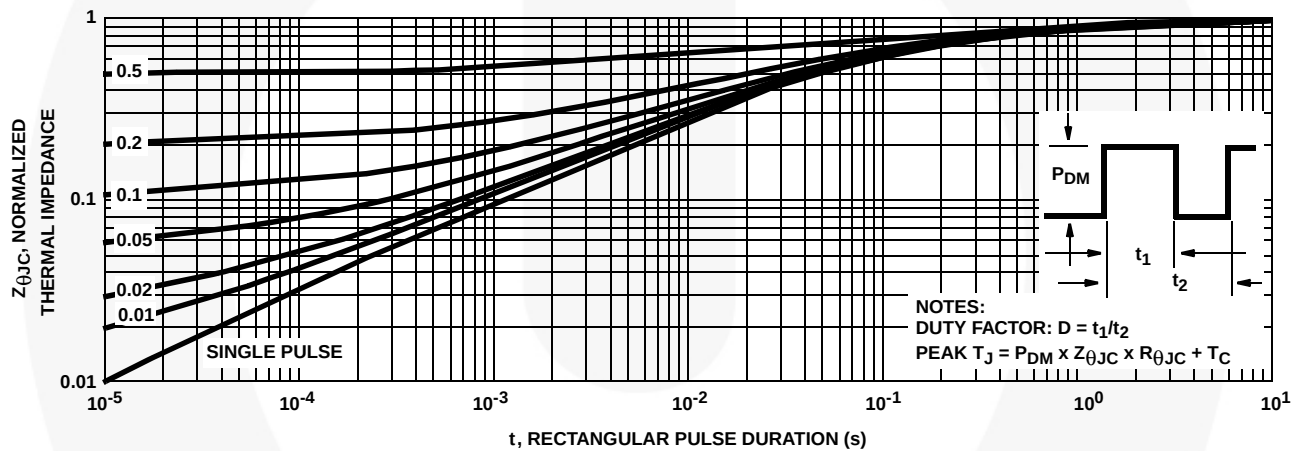


FIGURE 3. NORMALIZED MAXIMUM TRANSIENT THERMAL IMPEDANCE

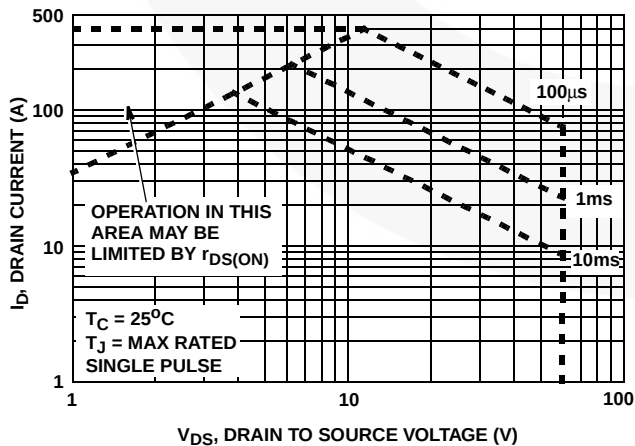


FIGURE 4. FORWARD BIAS SAFE OPERATING AREA

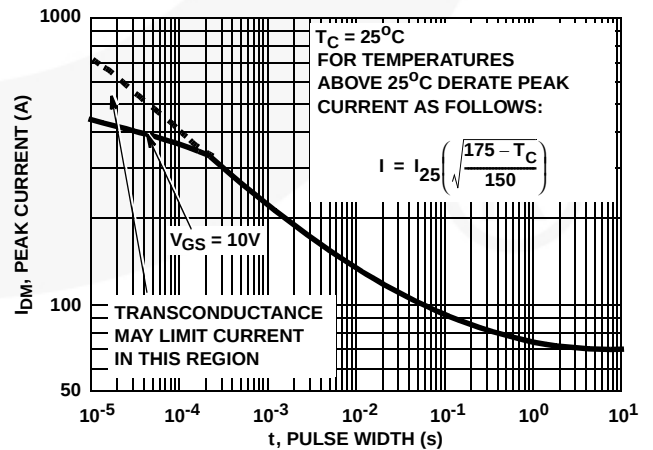
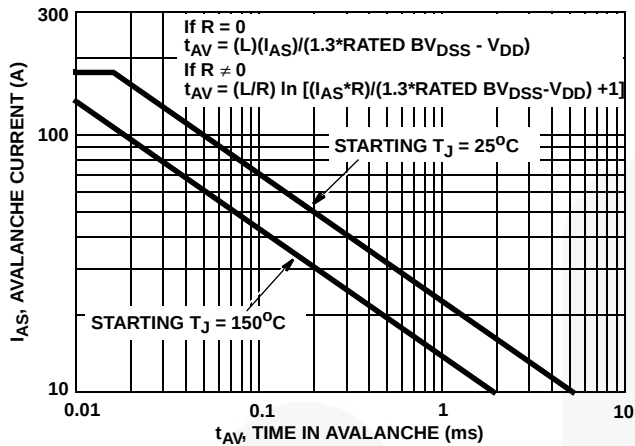


FIGURE 5. PEAK CURRENT CAPABILITY

# Typical Performance Curves $T_C = 25^\circ\text{C}$ , Unless Otherwise Specified (Continued)



NOTE: Refer to Fairchild Application Notes AN9321 and AN9322.

FIGURE 6. UNCLAMPED INDUCTIVE SWITCHING CAPABILITY

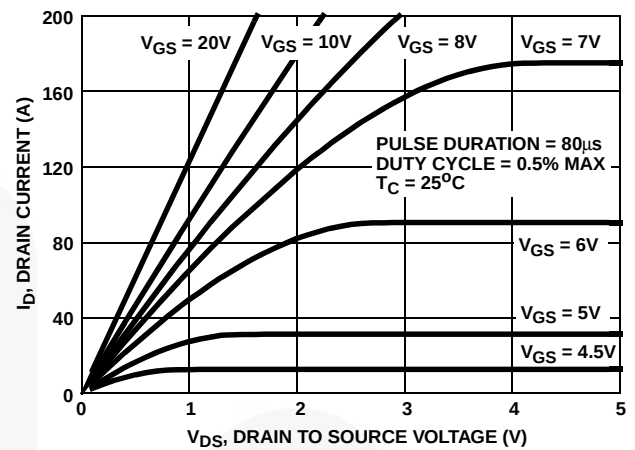


FIGURE 7. SATURATION CHARACTERISTICS

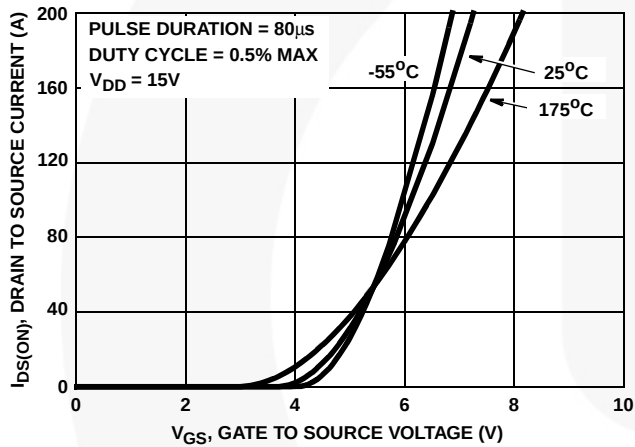


FIGURE 8. TRANSFER CHARACTERISTICS

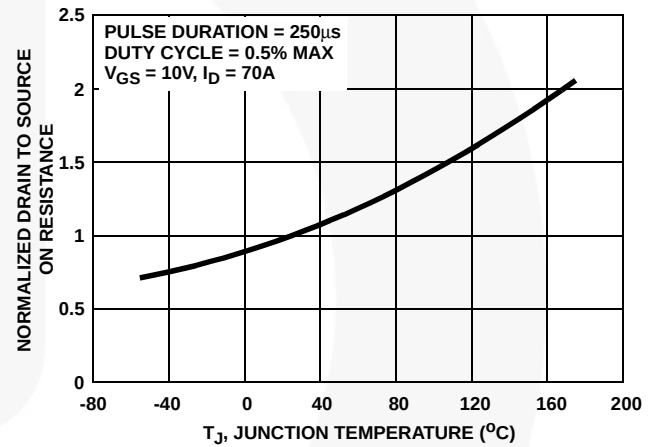


FIGURE 9. NORMALIZED DRAIN TO SOURCE ON RESISTANCE vs JUNCTION TEMPERATURE

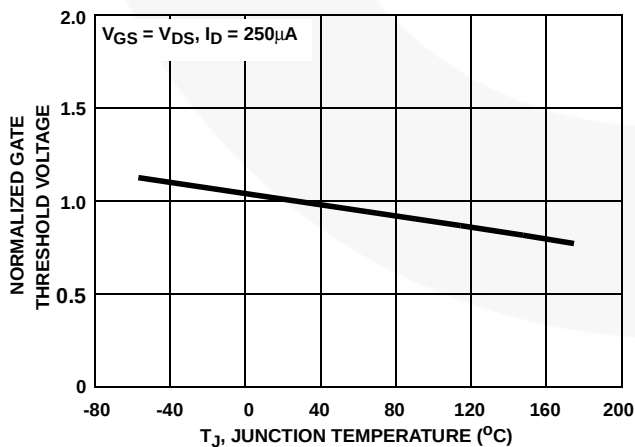


FIGURE 10. NORMALIZED GATE THRESHOLD VOLTAGE vs JUNCTION TEMPERATURE

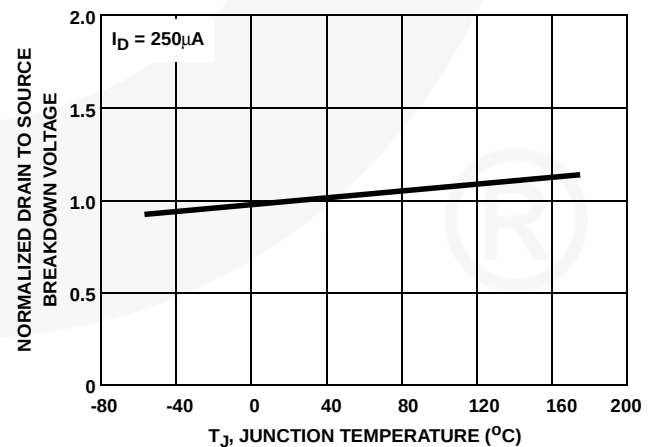


FIGURE 11. NORMALIZED DRAIN TO SOURCE BREAKDOWN VOLTAGE vs JUNCTION TEMPERATURE

# Typical Performance Curves

$T_C = 25^\circ\text{C}$ , Unless Otherwise Specified (Continued)

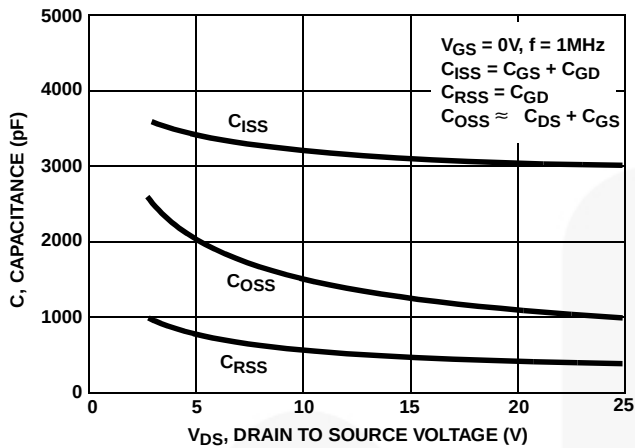
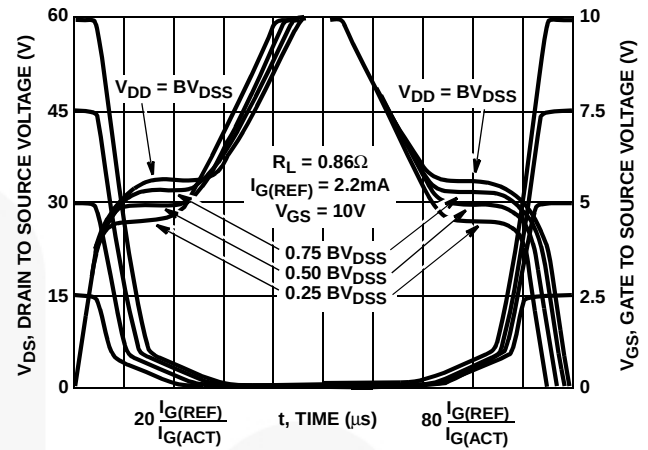


FIGURE 12. CAPACITANCE vs DRAIN TO SOURCE VOLTAGE



NOTE: Refer to Fairchild Application Notes AN7254 and AN7260.

FIGURE 13. NORMALIZED SWITCHING WAVEFORMS FOR CONSTANT GATE CURRENT

## Test Circuits and Waveforms

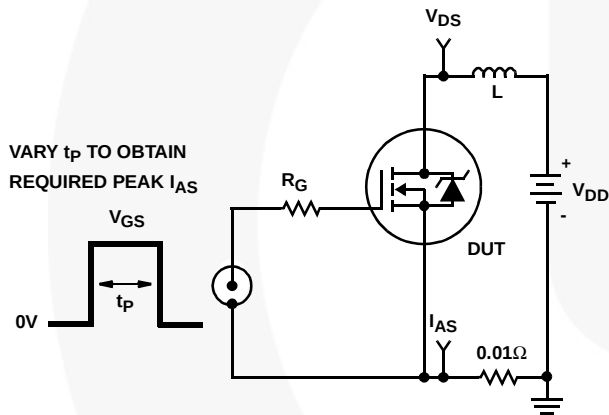


FIGURE 14. UNCLAMPED ENERGY TEST CIRCUIT

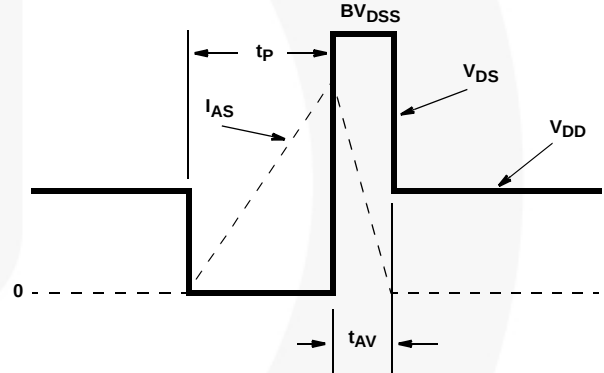


FIGURE 15. UNCLAMPED ENERGY WAVEFORMS

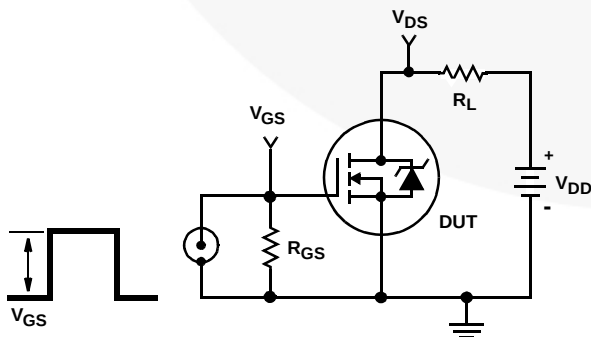


FIGURE 16. SWITCHING TIME TEST CIRCUIT

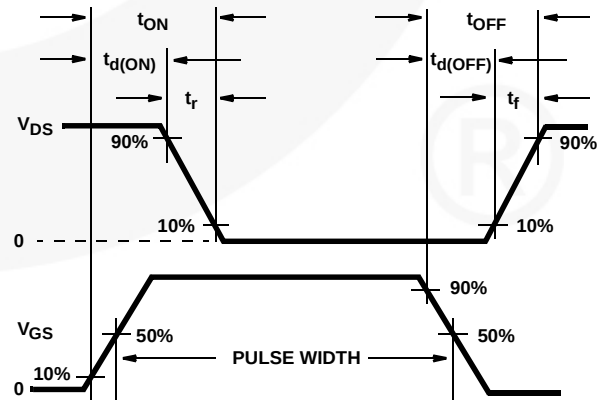


FIGURE 17. SWITCHING WAVEFORMS

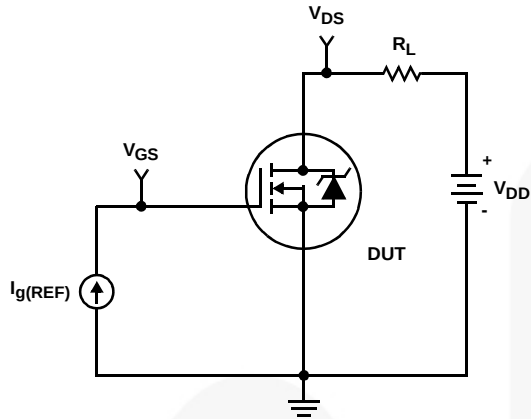
**Test Circuits and Waveforms** (Continued)

FIGURE 18. GATE CHARGE TEST CIRCUIT

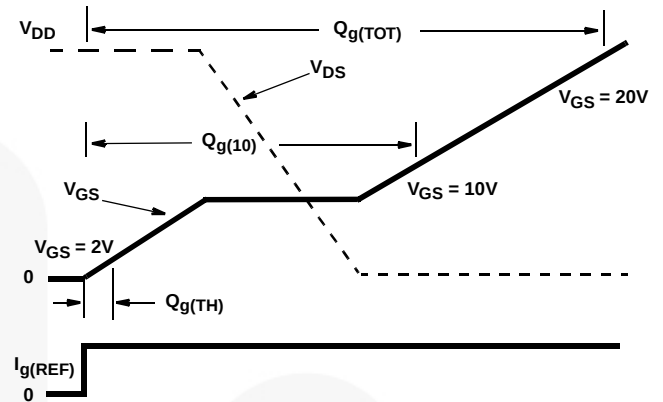


FIGURE 19. GATE CHARGE WAVEFORM





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- Поставка оригинальных импортных электронных компонентов напрямую с производств Америки, Европы и Азии, а так же с крупнейших складов мира;
- Широкая линейка поставок активных и пассивных импортных электронных компонентов (более 30 млн. наименований);
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Помощь Конструкторского Отдела и консультации квалифицированных инженеров;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Поставка электронных компонентов под контролем ВП;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- При необходимости вся продукция военного и аэрокосмического назначения проходит испытания и сертификацию в лаборатории (по согласованию с заказчиком);
- Поставка специализированных компонентов военного и аэрокосмического уровня качества (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Actel, Aeroflex, Peregrine, VPT, Syfer, Eurofarad, Texas Instruments, MS Kennedy, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Компания «Океан Электроники» является официальным дистрибьютором и эксклюзивным представителем в России одного из крупнейших производителей разъемов военного и аэрокосмического назначения «JONHON», а так же официальным дистрибьютором и эксклюзивным представителем в России производителя высокотехнологичных и надежных решений для передачи СВЧ сигналов «FORSTAR».



## JONHON

«JONHON» (основан в 1970 г.)

Разъемы специального, военного и аэрокосмического назначения:

(Применяются в военной, авиационной, аэрокосмической, морской, железнодорожной, горно- и нефтедобывающей отраслях промышленности)

«FORSTAR» (основан в 1998 г.)

ВЧ соединители, коаксиальные кабели,  
кабельные сборки и микроволновые компоненты:

(Применяются в телекоммуникациях гражданского и специального назначения, в средствах связи, РЛС, а так же военной, авиационной и аэрокосмической отраслях промышленности).



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