

# PSMNR70-40SSH

N-channel 40 V, 0.7 m $\Omega$ , 425 Amps continuous, standard level MOSFET in LFPAK88 using NextPowerS3 Technology

19 June 2019 Product data sheet

## 1. General description

425 Amp continuous current, standard level gate drive, N-channel enhancement mode MOSFET in LFPAK88 package. NextPowerS3 family using Nexperia's unique "SchottkyPlus" technology delivers high efficiency and low spiking performance usually associated with MOSFETs with an integrated Schottky or Schottky-like diode but without problematic high leakage current. NextPowerS3 is particularly suited to high efficiency applications at high switching frequencies, and also safe and reliable switching at high load-current.

### 2. Features and benefits

- · 425 Amp continuous current capability
- LFPAK88 (8 x 8 mm) LFPAK-style low-stress exposed lead-frame for ultimate reliability, optimum soldering and easy solder-joint inspection
- Copper-clip and solder die attach for low package inductance and resistance, and high I<sub>D</sub> (max) rating
- Ideal replacement for D2PAK and 10 x 12 mm leadless package types
- Qualified to 175 °C
- Meets UL2595 requirements for creepage and clearance
- Avalanche rated, 100 % tested
- Low Q<sub>G</sub>, Q<sub>GD</sub> and Q<sub>OSS</sub> for high efficiency, especially at higher switching frequencies
- Superfast switching with soft body-diode recovery for low-spiking and ringing, recommended for low EMI designs
- Unique "SchottkyPlus" technology for Schottky-like switching performance and low I<sub>DSS</sub> leakage
- Narrow V<sub>GS(th)</sub> rating for easy paralleling and improved current sharing
- Very strong linear-mode / safe operating area characteristics for safe and reliable switching at high-current conditions

## 3. Applications

- · Brushless DC motor control
- · Synchronous rectifier in high-power AC-DC applications, e.g. server power supplies
- · Battery protection
- eFuse and load switch
- Hotswap / in-rush current management

### 4. Quick reference data

#### Table 1. Quick reference data

Table II Quiek leielei eile data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C		-	-	40	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 25 °C; <u>Fig. 2</u>	[1]	-	-	425	Α
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; <u>Fig. 1</u>		-	-	375	W



<u> </u>		2		1			illiology
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	Fig. 5		-	0.35	0.4	K/W
Static chara	cteristics						
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS}$ = 10 V; $I_{D}$ = 25 A; $T_{j}$ = 25 °C; Fig. 11		0.43	0.62	0.7	mΩ
Dynamic ch	aracteristics						
Q <sub>G(tot)</sub>	total gate charge	I <sub>D</sub> = 25 A; V <sub>DS</sub> = 32 V; V <sub>GS</sub> = 10 V;		-	144	202	nC
$Q_{GD}$	gate-drain charge	Fig. 13; Fig. 14		-	25	50	nC
Source-drai	n diode						
Q <sub>r</sub>	recovered charge	$I_S$ = 25 A; $dI_S/dt$ = -100 A/ $\mu$ s; $V_{GS}$ = 0 V; $V_{DS}$ = 20 V; $Fig. 17$	[2]	-	74	-	nC

<sup>[1] 425</sup>A. Continuous current has been successfully demonstrated during application. Practically, the current will be limited by the PCB, thermal design and operating temperature.

## 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		D
2	S	source		
3	S	source		G—(FIA)
4	S	source		mbb076 S
mb	D	mounting base; connected to drain	LFPAK88 (SOT1235)	

## 6. Ordering information

**Table 3. Ordering information** 

Type number	Package					
	Name	Description	Version			
PSMNR70-40SSH	LFPAK88	plastic, single-ended surface-mounted package (LFPAK88); 4 leads; 2 mm pitch; 8 mm x 8 mm x 1.6 mm body	SOT1235			

## 7. Limiting values

### **Table 4. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C	-	40	V
$V_{DSM}$	peak drain-source voltage	$t_p \le 20 \text{ ns}; f \le 500 \text{ kHz}; E_{DS(AL)} \le 200 \text{ nJ};$ pulsed	-	45	V
$V_{DGR}$	drain-gate voltage	25 °C ≤ $T_j$ ≤ 175 °C; $R_{GS}$ = 20 kΩ	-	40	V
$V_{GS}$	gate-source voltage		-20	20	V
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; <u>Fig. 1</u>	-	375	W

<sup>[2]</sup> includes capacitive recovery

Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 25 °C; <u>Fig. 2</u>	[1]	-	425	Α
		V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 100 °C; <u>Fig. 2</u>		-	350	Α
I <sub>DM</sub>	peak drain current	pulsed; t <sub>p</sub> ≤ 10 μs; T <sub>mb</sub> = 25 °C; <u>Fig. 3</u>		-	1983	Α
T <sub>stg</sub>	storage temperature			-55	175	°C
Tj	junction temperature			-55	175	°C
T <sub>sld(M)</sub>	peak soldering temperature			-	260	°C
Source-drai	n diode				•	
Is	source current	T <sub>mb</sub> = 25 °C	[2]	-	500	Α
I <sub>SM</sub>	peak source current	pulsed; t <sub>p</sub> ≤ 10 µs; T <sub>mb</sub> = 25 °C		-	1983	Α
Avalanche r	ruggedness			'		
E <sub>DS(AL)S</sub>	non-repetitive drain- source avalanche energy	$I_D$ = 120 A; $V_{sup} \le 40$ V; $R_{GS}$ = 50 Ω; $V_{GS}$ = 10 V; $T_{j(init)}$ = 25 °C; unclamped; Fig. 4		-	940	mJ
I <sub>AS</sub>	non-repetitive avalanche current	$V_{sup}$ = 40 V; $V_{GS}$ = 10 V; $T_{j(init)}$ = 25 °C; $R_{GS}$ = 50 $\Omega$	[3]	-	294	А

- [1] 425A. Continuous current has been successfully demonstrated during application. Practically, the current will be limited by the PCB, thermal design and operating temperature.
- [2] 500A. Continuous current has been successfully demonstrated during application. Practically, the current will be limited by the PCB, thermal design and operating temperature.
- [3] Protected by 100% test

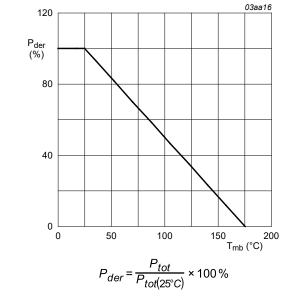
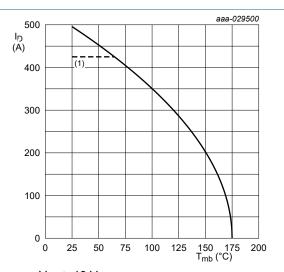


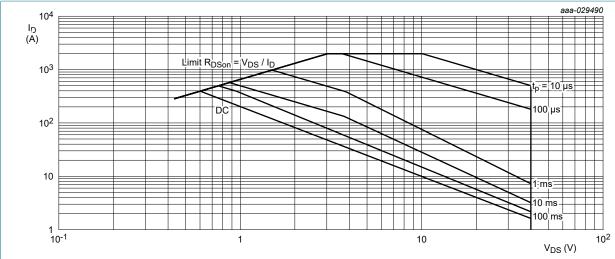
Fig. 1. Normalized total power dissipation as a function of mounting base temperature



 $V_{GS} \ge 10 \text{ V}$  (1) 425A continuous current has been successfully demonstrated during application tests. Practically the current will be limited by PCB, thermal design and operating temperature.

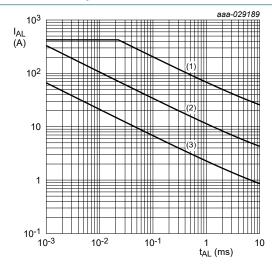
Fig. 2. Continuous drain current as a function of mounting base temperature

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T<sub>mb</sub> = 25 °C; I<sub>DM</sub> is a single pulse

Fig. 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage



(1)  $T_{j \text{ (init)}}$  = 25 °C; (2)  $T_{j \text{ (init)}}$  = 150 °C; (3) Repetitive Avalanche

Fig. 4. Avalanche rating; avalanche current as a function of avalanche time

### 8. Thermal characteristics

**Table 5. Thermal characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base	Fig. 5	-	0.35	0.4	K/W
R <sub>th(j-a)</sub>	thermal resistance from	Fig. 6	-	35	-	K/W
	junction to ambient	Fig. 7	-	70	-	K/W

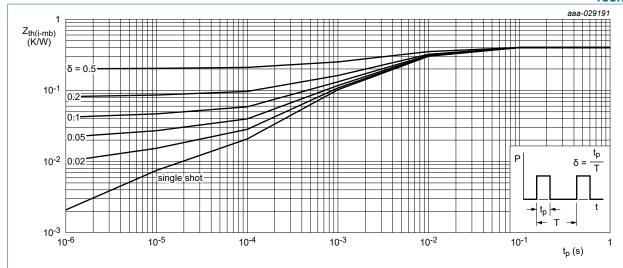
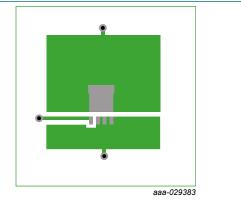
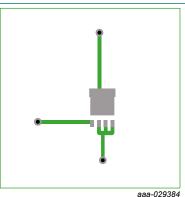


Fig. 5. Transient thermal impedance from junction to mounting base as a function of pulse duration



Copper square 25.4 mm x 25.4 mm; 70  $\mu$ m thick on FR4 board

Fig. 6. PCB layout for resistance from junction to ambient



70 µm thick copper on FR4 board

Fig. 7. PCB layout with minimum footprint for thermal resistance from junction to ambient

### 9. Characteristics

**Table 6. Characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static charac	teristics					
V <sub>(BR)DSS</sub>	drain-source	I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	40	43	-	V
	breakdown voltage	I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = -55 °C	36	40	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	2.4	3	3.6	V
$\Delta V_{GS(th)}/\Delta T$	gate-source threshold voltage variation with temperature	25 °C ≤ T <sub>j</sub> ≤ 175 °C	-	-8.2	-	mV/K
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 32 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	1.6	μΑ
		V <sub>DS</sub> = 32 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 175 °C	-	264	-	μΑ
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	2	100	nA
		V <sub>GS</sub> = -20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	2	100	nA
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 25 ^{\circ}\text{C};$ Fig. 11	0.43	0.62	0.7	mΩ

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
		$V_{GS}$ = 10 V; $I_D$ = 25 A; $T_j$ = 175 °C; Fig. 12		0.85	1.23	1.53	mΩ
R <sub>G</sub>	gate resistance	f = 1 MHz; T <sub>j</sub> = 25 °C		0.5	1.2	3	Ω
Dynamic ch	aracteristics			•			
Q <sub>G(tot)</sub>	total gate charge	I <sub>D</sub> = 25 A; V <sub>DS</sub> = 32 V; V <sub>GS</sub> = 10 V; Fig. 13; Fig. 14		-	144	202	nC
		I <sub>D</sub> = 0 A; V <sub>DS</sub> = 0 V; V <sub>GS</sub> = 10 V		-	72	-	nC
Q <sub>GS</sub>	gate-source charge	I <sub>D</sub> = 25 A; V <sub>DS</sub> = 32 V; V <sub>GS</sub> = 10 V;		-	40	60	nC
Q <sub>GS(th)</sub>	pre-threshold gate- source charge	Fig. 13; Fig. 14		-	29	43	nC
Q <sub>GS(th-pl)</sub>	post-threshold gate- source charge			-	11	17	nC
$Q_{GD}$	gate-drain charge			-	25	50	nC
V <sub>GS(pl)</sub>	gate-source plateau voltage	I <sub>D</sub> = 25 A; V <sub>DS</sub> = 32 V; <u>Fig. 13</u> ; <u>Fig. 14</u>		-	4.1	-	V
C <sub>iss</sub>	input capacitance	$V_{DS} = 25 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$ $T_j = 25 \text{ °C}; \underline{\text{Fig. 15}}$		-	11228	15719	pF
C <sub>oss</sub>	output capacitance			-	2363	3308	pF
C <sub>rss</sub>	reverse transfer capacitance			-	415	913	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS} = 30 \text{ V}; R_L = 1.2 \Omega; V_{GS} = 10 \text{ V};$		-	35	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 5 \Omega$		-	30	-	ns
t <sub>d(off)</sub>	turn-off delay time			-	94	-	ns
t <sub>f</sub>	fall time			-	41	-	ns
Q <sub>oss</sub>	output charge	$V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}; f = 1 \text{ MHz};$ $T_j = 25 \text{ °C}$		-	102	-	nC
Source-drai	in diode						
V <sub>SD</sub>	source-drain voltage	$I_S = 25 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}; Fig. 16$		-	0.75	1	V
t <sub>rr</sub>	reverse recovery time	$I_S = 25 \text{ A}; dI_S/dt = -100 \text{ A/}\mu\text{s}; V_{GS} = 0 \text{ V};$		-	53	-	ns
Q <sub>r</sub>	recovered charge	V <sub>DS</sub> = 20 V; <u>Fig. 17</u>	[1]	-	74	-	nC
t <sub>a</sub>	reverse recovery rise time			-	30	-	ns
t <sub>b</sub>	reverse recovery fall time			-	23	-	ns

<sup>[1]</sup> includes capacitive recovery

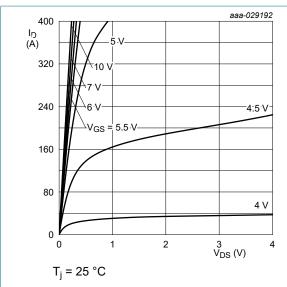


Fig. 8. Output characteristics; drain current as a function of drain-source voltage; typical values

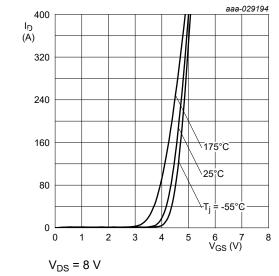


Fig. 10. Transfer characteristics; drain current as a function of gate-source voltage; typical values

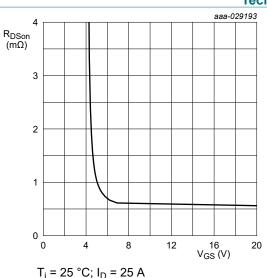


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

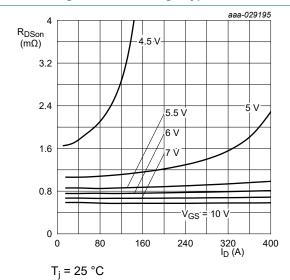


Fig. 11. Drain-source on-state resistance as a function of drain current; typical values

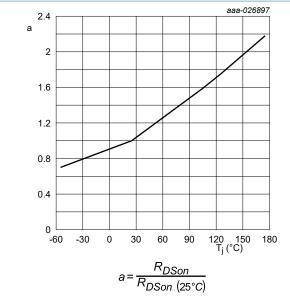


Fig. 12. Normalized drain-source on-state resistance factor as a function of junction temperature

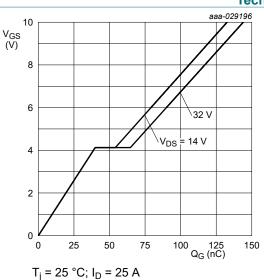


Fig. 13. Gate-source voltage as a function of gate charge; typical values

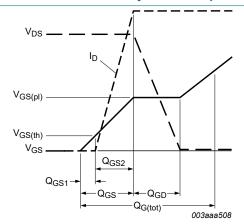
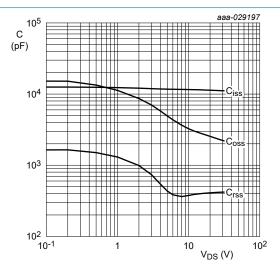


Fig. 14. Gate charge waveform definitions



 $V_{GS} = 0 V; f = 1 MHz$ 

Fig. 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

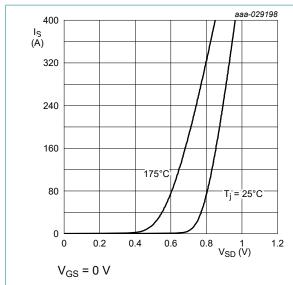


Fig. 16. Source-drain (diode forward) current as a function of source-drain (diode forward) voltage; typical values

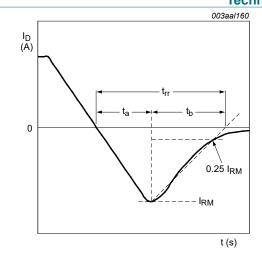


Fig. 17. Reverse recovery timing definition

## 10. Package outline

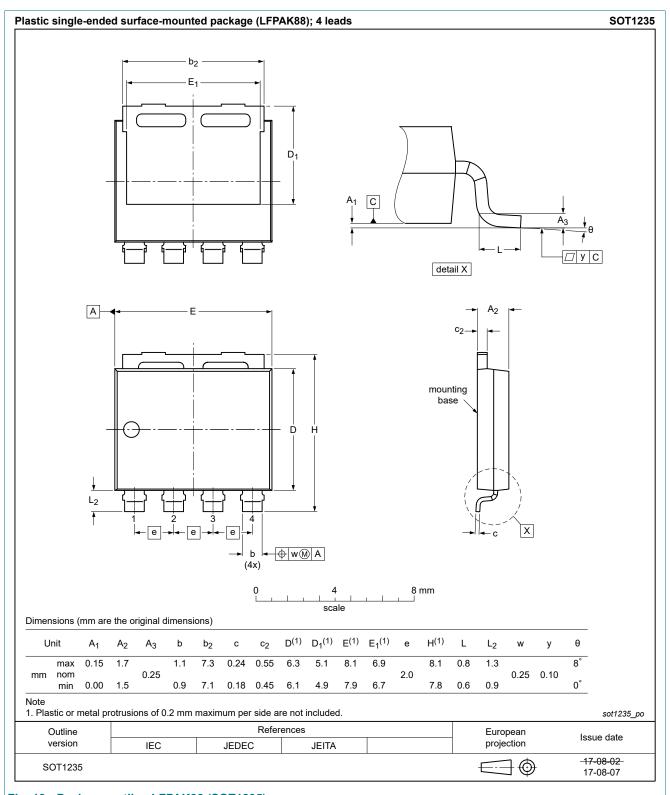
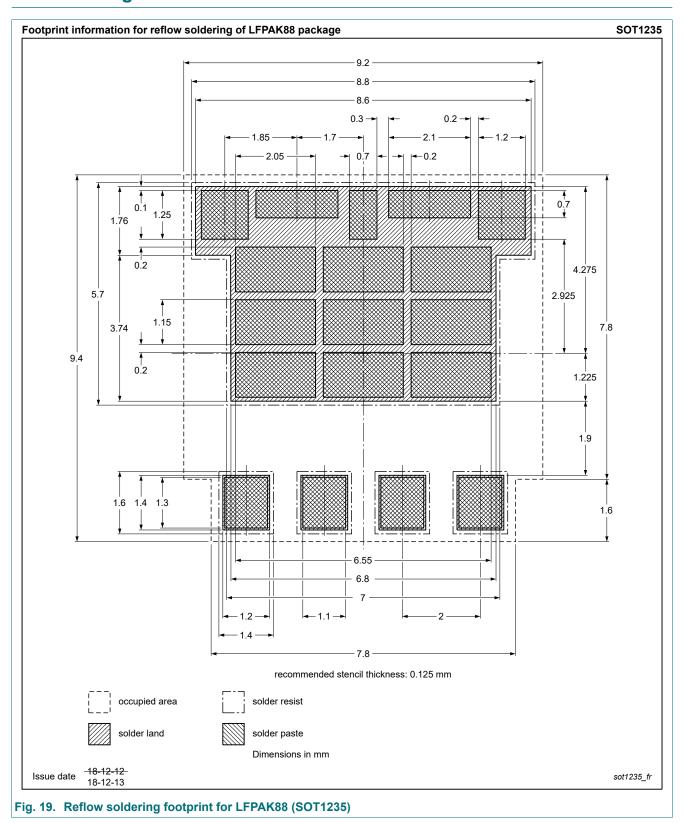


Fig. 18. Package outline LFPAK88 (SOT1235)

## 11. Soldering



## 12. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Помощь Конструкторского Отдела и консультации квалифицированных инженеров;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Поставка электронных компонентов под контролем ВП;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- При необходимости вся продукция военного и аэрокосмического назначения проходит испытания и сертификацию в лаборатории (по согласованию с заказчиком);
- Поставка специализированных компонентов военного и аэрокосмического уровня качества (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Actel, Aeroflex, Peregrine, VPT, Syfer, Eurofarad, Texas Instruments, MS Kennedy, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Компания «Океан Электроники» является официальным дистрибьютором и эксклюзивным представителем в России одного из крупнейших производителей разъемов военного и аэрокосмического назначения «JONHON», а так же официальным дистрибьютором и эксклюзивным представителем в России производителя высокотехнологичных и надежных решений для передачи СВЧ сигналов «FORSTAR».



**«JONHON»** (основан в 1970 г.)

Разъемы специального, военного и аэрокосмического назначения:

(Применяются в военной, авиационной, аэрокосмической, морской, железнодорожной, горно- и нефтедобывающей отраслях промышленности)

«**FORSTAR**» (основан в 1998 г.)

ВЧ соединители, коаксиальные кабели, кабельные сборки и микроволновые компоненты:

(Применяются в телекоммуникациях гражданского и специального назначения, в средствах связи, РЛС, а так же военной, авиационной и аэрокосмической отраслях промышленности).



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